



Electrical Specifications:

- 1.0 Turn Ratio:(P1~P2):(J1~J2):1:1±3%
(P7~P8):(J3~J6):1:1±3%
- 2.0 Inductance:(P1~P2):350uH Min@0.1V,100KHz,8mA DC Bias
(P7~P8):350uH Min@0.1V,100KHz,8mA DC Bias
- 3.0 Leakage Inductance:P1~P2(With J1 And J2 Short):0.35uH Max @1 MHz
P7~P8(With J3 And J6 Short):0.35uH Max @1 MHz
- 4.0 Interwindind:Capacitance:(P1~P2):(J1~J2):35pF Max @ 1MHz
(P7~P8):(J3~J6):35pF Max @ 1MHz
- 5.0 DC Resistance:(J1~J2):(P7~P8):1.2 Ohms Max
- 6.0 Return Loss:1MHz~30MHz:-18dB Min
30MHz~80MHz:-12dB Min
- 7.0 Dielectric Withstand:(P1~P2):(J1~J2):1500VAC
(P7~P8):(J3~J6):1500VAC
- 8.0 Insertion Loss:Rs=RL=100 Ohm
100KHz~80MHz:-1.2dB Min
- 9.0 Cross Talk:1MHz~100MHz:35dB Typ
- 10.0 Common~Common Mode Attenuation:30MHz~100MHz:30dB Typ



DRAWING No.		APPROVED BY		UNLESS OTHERWISE SPECIFIED					
19154		MEI		TOLERANCES ARE					
		CHECKED BY		.X ±0.25					
		HZF		ANG:					
		DRAWING BY		UNIT		△ A0	NEW RELEASE	HZF	03/17/2008
		JLT		mm		NO.REV.	REVISIONS	CHK	DATE
SCALE	NONE	REV.	A0						