DSA9402

Silicon PNP epitaxial planar type

For general amplification

Features

- Low collector-emitter saturation voltage V_{CE(sat)}
- Contributes to miniaturization of sets, reduction of component count.
- Eco-friendly Halogen-free package

Packaging

Embossed type (Thermo-compression sealing): 3000 pcs / reel (standard)

Absolute Maximum Ratings $T_a = 25^{\circ}C$

Parameter	Symbol	Rating	Unit					
Collector-base voltage (Emitter open)	V _{CBO}	-15	V					
Collector-emitter voltage (Base open)	V _{CEO}	-12	V					
Emitter-base voltage (Collector open)	V _{EBO}	-5	V					
Collector current	I _C	-500	mA					
Peak collector current	I _{CP}	-1	А					
Collector power dissipation	P _C	125	mW					
Junction temperature	Tj	150	°C					
Storage temperature	T _{stg}	-55 to +150	°C					

Electrical Characteristics $T_a = 25^{\circ}C \pm 3^{\circ}C$

Parameter	Symbol	Conditions	Min	Тур	Max	Unit
Collector-base voltage (Emitter open)	V _{CBO}	$I_{\rm C} = -10 \ \mu {\rm A}, I_{\rm E} = 0$	-15			V
Collector-emitter voltage (Base open)	V _{CEO}	$I_{\rm C} = -1 {\rm mA}, I_{\rm B} = 0$	-12			V
Emitter-base voltage (Collector open)	V _{EBO}	$I_{\rm E} = -10 \ \mu A, I_{\rm C} = 0$	-5			V
Collector-base cutoff current (Emitter open)	I _{CBO}	$V_{CB} = -10 \text{ V}, I_E = 0$			- 0.1	μΑ
Forward current transfer ratio	h _{FE}	$V_{CE} = -2 V, I_C = -10 mA$	270		680	
Collector-emitter saturation voltage	V _{CE(sat)}	$I_{\rm C} = -200 \text{ mA}, I_{\rm B} = -10 \text{ mA}$			-250	mV
Transition frequency	f_{T}	$V_{CE} = -2 V, I_C = -10 mA$		300		MHz
Collector output capacitance (Common base, input open circuited)	C _{ob}	$V_{CB} = -10 \text{ V}, I_E = 0, f = 1 \text{ MHz}$		4.0		pF

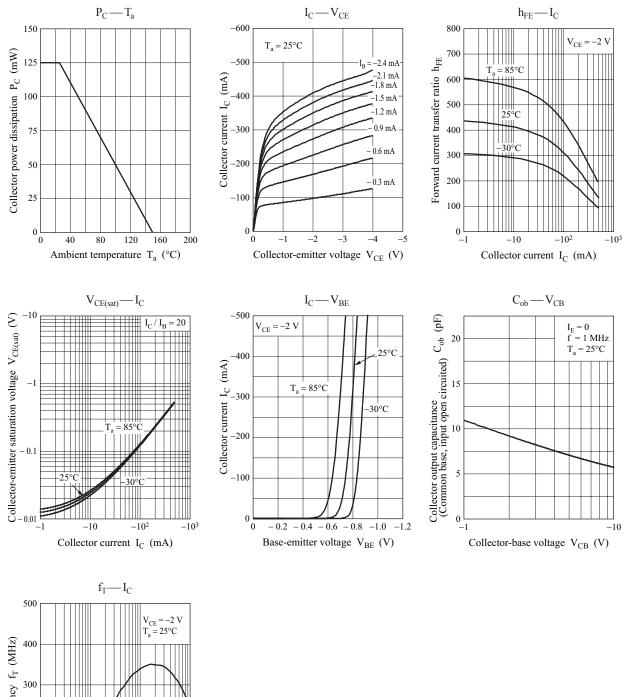
Note) Measuring methods are based on JAPANESE INDUSTRIAL STANDARD JIS C 7030 measuring methods for transistors.

Package

- Code
- SSMini3-F3-B
- Pin Name
 - 1. Base
 - 2. Emitter
 - 3. Collector
- Marking Symbol: B2

DSA9402

Panasonic

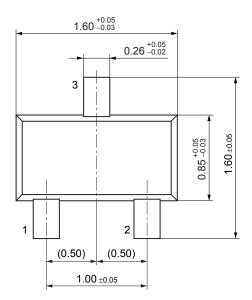


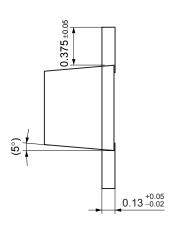
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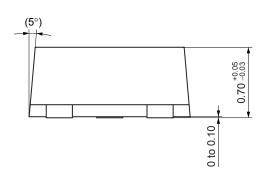
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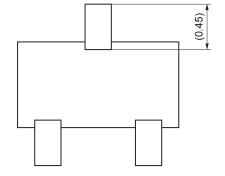
SSMini3-F3-B

Unit: mm









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