

Quality Engineering Test Report

SERIES: SPR010 1W REGULATED SINGLE OUTPUT DC-DC CONVERTER

SAMPLE: A.SPR010-05 +5VDC / 200mA

NO	TEST ITEM	TEST CONDITION / SPECIFICATION	RESULT	VERDICT								
1	DC INPUT VOLTAGE RANGE	I/P:TESTING SPEC:43.2~52.8VDC O/P:FULL LOAD	A:38.4~52.8VDC	P								
2	LINE REGULATION	I/P: 43.2~52.8VDC SPEC: ±1% O/P: FULL LOAD	A: -0.08% ~ +0.06%	P								
3	LOAD REGULATION	I/P: 48VDC SPEC: ±1% O/P:20% TO FULL LOAD	A: -0.08% ~ +0.18%	P								
4	OUTPUT VOLTAGE TOLERANCE	I/P: 43.2~52.8VDC SPEC: NONE O/P:20% TO FULL LOAD	A: -0.14% ~ +0.18%	P								
5	RIPPLE&NOISE	I/P: 48VDC SPEC: 100mV O/P:FULL LOAD	A:10mV	P								
6	DC INPUT CURRENT	I/P: 48VDC SPEC: 35mA O/P:FULL LOAD	A:32.8mA	P								
7	OVER LOAD PROTECTION	I/P: 48VDC SPEC:NONE O/P:TESTING	A:210%	P								
8	O/P VOLTAGE ACCURACY	I/P: 48VDC SPEC: ±2% O/P:FULL LOAD	A:+0.8%	P								
9	SET UP TIME	I/P: 48VDC SPEC: NONE O/P:FULL LOAD	A:0.62mS	P								
10	EFFICIENCY	I/P: 48VDC SPEC: A:62% O/P:FULL LOAD	A:64.1%	P								
11	INSULATION RESISTANCE	SPEC:I/P-O/P 500VDC/100M Ohms MIN.	A:I/P-O/P >100M Ohms	P								
12	DIELECTRIC / WITHSTAND VOLTAGE	SPEC: I/P- O/P: 1000VDC/ 1 min (10mA CUT-OFF)	A:NO BREAK I/P-O/P :<0.02mA	P								
13	BURN-IN TEST	I/P:48VDC O/P:FULL LOAD TA:25°C BURN-IN DURATION : 2 hrs	A:NO BREAK	P								
14	TEMPERATURE RISE TEST T rise OF PARTS	A:I/P :48VDC AFTER 4 hrs BURN-IN O/P :FULL LOAD TA:25°C <table border="1" style="margin-left: auto; margin-right: auto;"> <thead> <tr> <th>POSITION</th> <th>SAMPLE</th> <th>TEMP</th> <th>T rise</th> </tr> </thead> <tbody> <tr> <td>CASE</td> <td>SUS010-05</td> <td>47.0°C</td> <td>22.0°C</td> </tr> </tbody> </table>		POSITION	SAMPLE	TEMP	T rise	CASE	SUS010-05	47.0°C	22.0°C	P
POSITION	SAMPLE	TEMP	T rise									
CASE	SUS010-05	47.0°C	22.0°C									
15	CONSTRUCTION INSPECTION (FOR QC INSPECTION REFERENCE ONLY)	1. PACKING : 軌道型塑膠包裝 2. MARKING : MODEL 3. MECHANICAL : PLASTIC CASE 4. DIMENSIONS : WXHXD=19.6X10.14X6.1mm, PIN : 3.8mm										
DATE		TEST RESULT		TEST	APPROVAL							
2000.4.26		SPR010-05 PASS		T.K.CHENG	MAX LIN							