



# Test Report : SMU01L-05

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1W DC-DC Unregulated Single Output Converter

## ■ DESIGN VERIFY TEST

Output Function Test

Input Function Test

## ■ SAFETY TEST

Safety Test

**DESIGN VERIFY TEST**
**OUTPUT FUNCTION TEST**

NO	TEST ITEM	SPECICATION	TEST CONDITION	RESULT	VERDICT
1	VOLTAGE ACCURACY	-2%~+2% (Max)	I/P:5VDC O/P:100% LOAD Ta:25°C	+1.28%	P
2	RIPPLE & NOISE	100mVp-p (Max)	I/P:5VDC O/P:FULL LOAD Ta:25°C	18mV	P
3	LINE REGULATION	-12%~+12% (Max)	I/P:4.5VDC-5.5VDC O/P:FULL LOAD Ta:25°C	-11.52% ~ +10.72%	P
4	LOAD REGULATION	-8.0%~+8.0% (Max)	I/P:5VDC O/P:MIN-FULL LOAD Ta:25°C	-4.25% ~ +2.80%	P

**INPUT FUNCTION TEST**

NO	TEST ITEM	SPECICATION	TEST CONDITION	RESULT	VERDICT
1	EFFICIENCY	77% (Typ)	I/P:5VDC O/P:FULL LOAD Ta:25°C	77.91%	P
2	DC CURRENT	265 mA / FULL LOAD (Max) 32 mA / NO LOAD (Max)	I/P:5VDC O/P:NO / FULL LOAD Ta:25°C	260 mA / FULL LOAD 27 mA / NO LOAD	P

**SAFETY TEST**
**SAFETY TEST**

NO	TEST ITEM	SPECICATION	TEST CONDITION	RESULT	VERDICT
1	WITHSTAND VOLTAGE	I/P-O/P:1500 VDC/min	I/P-O/P:1500 VDC/min Ta:25°C	I/P-O/P: 0.02mA NO DAMAGE	P
2	ISOLATION RESISTANCE	I/P-O/P:500VDC>100MΩ	I/P-O/P:500 VDC Ta:25°C	I/P-O/P>100MΩ NO DAMAGE	P

DATE	SAMPLE	TEST RESULT	TESTER	APPROVAL
2009.9.5	PRODUCT SAMPLE	PASS	PETER	VINCENT ZENG