



VOLTAGE	L1	L2	L3	JOIN & INSULATE			
HIGH	T1	Т2	Т3	(T4,T7) (T5,T8) (T6,T9)			
LOW	T1,T7	T2,T8	Т3,Т9	T4,T5,T6			

				TOLERANCES UNLESS SPECIFIED				لناكالنا		DRAWN	RDW 04/12	2/02
			DEC. INCHES		INCHES] (((())) <u> </u>				СНК		
				.x	±.1		<u> </u>			APPD		
				.xx	±.01	TITLE	EXTERNAL WIRING DIAGRAM			SCALE	1=1	
				.xxx	±.005]	3 PHASE W/O PROTECTOR			REF	FIG.2-51	
				.xxxx	±.0005	MAT'L.	DECAL - 004014			FMF		
NO.	REVISION BY & DATE		СНК	ANG	±1/2°	FINISH				PREV		
THIS DRAWING IN DESIGN AND DETAIL IS OUR PROPERTY AND MUST NOT BE USED EXCEPT IN CONNECTION WITH OUR WORK ALL RIGHTS OF DESIGN AND INVENTION ARE RESERVED THIS IS AN ELECTRONICALLY GENERATED DOCUMENT — DO NOT SCALE THIS PRINT			RFP	0.	4/12/02	CAD FILE	00501001ME	SIZE	DRAWING NO	D.		REV.
			DIST			•		$\neg A$	0050	10-0)1ME	