

## Chapter 18 Specifications

All the specifications are guaranteed except the parameters marked with "Typical" and the oscilloscope needs to operate for more than 30 minutes under the specified operation temperature.

### Sample

Sample Mode	Real-time Sample
Real Time Sample Rate	Analog channel: 1 GSa/s (single-channel), 500 Msa/s (dual-channel), 250 MSa/s (3/4-channel) Digital channel: 1 GSa/s (8-channel), 500 MSa/s (16-channel)
Peak Detect	Analog channel: 4 ns Digital channel: 4 ns
Averaging	After all the channels finish N samples at the same time, N can be 2, 4, 8, 16, 32, 64, 128, 256, 512 or 1024
High Resolution	12 bit (max)
Interpolation	Sin(x)/x (optional)
Min Detect Pulse Width	Digital channel: 10 ns
Memory Depth	Analog channel: standard 12M pts (single-channel), 6M pts (dual-channel), 3M pts (3/4-channel); optional 24 Mpts (single-channel), 12 Mpts(dual-channel), 6 Mpts (3/4-channel) Digital channel: standard 12 Mpts (8-channel)/6 Mpts(16-channel); optional 24 Mpts(8-channel)/12 Mpts(16-channel)

### Input

Number of Channels	MSO1XX4Z/1XX4Z-S: 4 analog channels+16 digital channels DS1XX4Z/1XX4Z-S: 4 analog channels
Input Coupling	DC, AC or GND
Input Impedance	Analog channel: (1 M $\Omega$ $\pm$ 1%)    (15 pF $\pm$ 3 pF) Digital channel: (100 k $\Omega$ $\pm$ 1%)    (8 pF $\pm$ 3 pF)
Probe Attenuation Coefficient	Analog channel: 0.01X to 1000X, in 1-2-5 step
Maximum Input Voltage (1M $\Omega$ )	Analog Channel: CAT I 300 Vrms, CAT II 100 Vrms, Transient Overvoltage 1000 Vpk With RP2200 10:1 probe: CAT II 300 Vrms Digital channel: CAT I 40Vrms, Transient Overvoltage 800 Vpk

**Horizontal**

Timebase Scale	5 ns/div to 50 s/div
Max Record Length	24 Mpts (optional)
Timebase Accuracy <sup>[1]</sup>	≤±25 ppm
Clock Drift	≤±5 ppm/year
Max Delay Range	Negative delay: ≥1/2 screen width Positive delay: 1 s to 5000 s
Timebase Mode	YT, XY, Roll
Number of X-Y	1
Waveform Capture Rate <sup>[2]</sup>	30,000 wfms/s (dots display)

**Vertical**

Bandwidth (-3dB)	MSO/DS 1104Z/1104Z-S: DC to 100 MHz MSO/DS 1074Z/1074Z-S: DC to 70 MHz DS1054Z: DC to 50 MHz
Single-shot Bandwidth	MSO/DS 1104Z/1104Z-S: DC to 100 MHz MSO/DS 1074Z/1074Z-S: DC to 70 MHz DS1054Z: DC to 50 MHz
Vertical Resolution	Analog channel: 8 bit Digital channel: 1 bit
Vertical Scale	1 mV/div to 10 V/div
Offset Range (Probe ratio is 1X)	1 mV/div to 499 mV/div: ±2 V 500 mV/div to 10 V/div: ±100 V
Bandwidth Limit <sup>[1]</sup>	20 MHz
Low Frequency Response (AC Coupling, -3dB)	≤5 Hz (on BNC)
Calculated Rise Time <sup>[1]</sup>	MSO/DS 1104Z/1104Z-S: 3.5 ns MSO/DS 1074Z/1074Z-S: 5 ns DS1054Z: 7 ns
DC Gain Accuracy <sup>[3]</sup>	<10 mV: ±4% full scale ≥10 mV: ±3% full scale
DC Offset Accuracy	±0.1 div ±2 mV ±1% offset value
Channel to Channel Isolation	DC to maximum bandwidth: >40 dB

**Vertical (Digital Channel)**

Threshold	Adjustable threshold of 8 channels per group
Threshold Selection	TTL (1.4 V)
	5.0 V CMOS (+2.5 V), 3.3 V CMOS (+1.65 V)
	2.5 V CMOS (+1.25 V), 1.8 V CMOS (+0.9 V)
	ECL (-1.3 V)
	PECL (+3.7 V)
	LVDS (+1.2 V)
	0 V
	User
Threshold Range	$\pm 15.0V$ , 10 mV step
Threshold Accuracy	$\pm (100 \text{ mV} + 3\% \text{ threshold setting})$
Dynamic Range	$\pm 10.0 \text{ V} + \text{threshold}$
Minimum Voltage Swing	500 mVpp
Vertical Resolution	1 bit

**Trigger**

Trigger Level Range	$\pm 5$ div from center of the screen
Trigger Mode	Auto, Normal, Single
Holdoff Range	16 ns to 10 s
High Frequency Rejection <sup>[1]</sup>	75 kHz
Low Frequency Rejection <sup>[1]</sup>	75 kHz
Trigger Sensitivity <sup>[1]</sup>	1.0 div (below 5 mV or noise rejection is enabled) 0.3 div (above 5 mV and noise rejection is disabled)

**Edge Trigger**

Edge Type	Rising, Falling, Rising/Falling
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**Pulse Trigger**

Pulse Condition	Positive Pulse Width (greater than, lower than, within specific interval) Negative Pulse Width (greater than, lower than, within specific interval)
Pulse Width	8 ns to 10 s

**Runt Trigger (Option)**

Pulse Width Condition	None, >, <, <>
Pulse Polarity	Positive, Negative

Pulse Width Range	8 ns to 10 s
<b>Window Trigger (Option)</b>	
Window Type	Rising, Falling, Rising/Falling
Trigger Position	Enter, Exit, Time
Window Time	8 ns to 10 s
<b>Nth Edge Trigger (Option)</b>	
Edge Type	Rising, Falling
Idle Time	16 ns to 10 s
Edge Number	1 to 65535
<b>Slope Trigger</b>	
Slope Condition	Positive Slope (greater than, lower than, within specific interval) Negative Slope (greater than, lower than, within specific interval)
Time Setting	8 ns to 10 s
<b>Video Trigger</b>	
Signal Standard	NTSC, PAL/SECAM, 480P, 576P
<b>Pattern Trigger</b>	
Pattern Setting	H, L, X, Rising, Falling
<b>Delay Trigger (Option)</b>	
Edge Type	Rising, Falling
Delay Type	>, <, <>, ><
Delay Time	8 ns to 10 s
<b>TimeOut Trigger (Option)</b>	
Edge Type	Rising, Falling, Rising/Falling
Timeout time	16 ns to 10 s
<b>Duration Trigger (Option)</b>	
Pattern	H, L, X
Trigger Condition	>, <, <>
Duration Time	8 ns to 10 s
<b>Setup/Hold Trigger (Option)</b>	
Edge Type	Rising, Falling
Data Type	H, L, X
Setup Time	8 ns to 1 s
Hold Time	8 ns to 1 s
<b>RS232/UART Trigger (Option)</b>	
Polarity	Normal, Invert
Trigger Condition	Start, Error, Check Error, Data
Baud Rate	2400 bps, 4800 bps, 9600 bps, 19200 bps, 38400 bps, 57600 bps, 115200 bps, 230400bps, 460800bps, 921600bps, 1Mbps and User

Data Bits	5 bit, 6 bit, 7 bit, 8 bit
<b>I2C Trigger (Option)</b>	
Trigger Condition	Start, Restart, Stop, Missing ACK, Address, Data, A&D
Address Bits	7 bits, 8 bits, 10 bits
Address Range	0x0 to 0x7F, 0x0 to 0xFF, 0x0 to 1023
Byte Length	1 to 5
<b>SPI Trigger (Option)</b>	
Trigger Condition	Timeout, CS
Timeout Value	16 ns to 10 s
Data Bits	4 bit to 32 bit
Data Line Setting	H, L, X

## Measure

Cursor	Manual Mode	Voltage Deviation between Cursors ( $\Delta V$ ) Time Deviation between Cursors ( $\Delta T$ ) Reciprocal of $\Delta T$ (Hz) ( $1/\Delta T$ )
	Track Mode	Voltage and Time Values of the Waveform Point
	Auto Mode	Allow to display cursors during auto measurement
Auto Measurement	<p>Analog channel:            Period, Frequency, Rise Time, Fall Time, Positive Pulse Width, Negative Pulse Width, Positive Duty Cycle, Negative Duty Cycle, <math>t_{Vmax}</math>, <math>t_{Vmin}</math>, Positive Rate, Negative Rate, Delay 1<math>\rightarrow</math>2<math>f</math>, Delay 1<math>\rightarrow</math>2<math>\tau</math>, Phase 1<math>\rightarrow</math>2<math>f</math>, Phase 1<math>\rightarrow</math>2<math>\tau</math>, Maximum, Minimum, Peak-Peak Value, Top Value, Bottom Value, Amplitude, Upper Value, Middle Value, Lower Value, Average, Mean Square Root, Overshoot, Pre-shoot, Area, Period Area, Variance</p> <p>Digital channel:            Frequency, Period, Positive Pulse Width, Negative Pulse Width, Positive Duty Cycle, Negative Duty Cycle, Delay 1<math>\rightarrow</math>2<math>f</math>, Delay 1<math>\rightarrow</math>2<math>\tau</math>, Phase 1<math>\rightarrow</math>2<math>f</math>, Phase 1<math>\rightarrow</math>2<math>\tau</math></p>	
Number of Measurements	Display 5 measurements at the same time.	
Measurement Range	Screen or cursor	
Measurement Statistic	Average, Max, Min, Standard Deviation, Number of Measurements	
Frequency Counter	Hardware 6 bit frequency counter (channels are selectable)	

## Math Operation

Waveform Operation	A+B, A-B, A×B, A/B, FFT, A&&B, A  B, A^B, !A, intg, Diff, Sqrt, Lg, Ln, Exp, Abs
FFT Window Function	Rectangle, Hanning, Blackman, Hamming, Flat Top, Triangle
FFT Display	Half, Full
FFT Vertical Scale	dB/dBm, Vrms
Number of Buses for Decoding	2
Decoding Type	Parallel (standard), RS232/UART (option), I2C (option), SPI (option)

## Display

Screen Type	7.0 inch (203 mm) TFT LCD display
Display Resolution	800 Horizontal ×RGB×480 Vertical Pixel
Display Color	160,000 Color (TBD)
Persistence Time	Min, 100 ms, 200 ms, 500 ms, 1 s, 5 s, 10 s, Infinite
Display Type	Dots, Vectors

## I/O

Standard Ports	USB HOST, USB Device, LAN, Aux Output (TrigOut/PassFail), GPIB (use USB HOST interface to expand)
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## Signal Source (MSO1000Z-S/DS1000Z-S)

Channels	2	
Sample Rate	200 MSa/s	
Vertical Resolution	14 bits	
Max. Frequency	25 MHz	
Standard Waveform	Sine, Square, Pulse, Ramp, Noise, DC	
Built-in Waveform	Sinc, Exponential Rise, Exponential Fall, ECG, Gauss, Lorentz, Haversine	
Sine	Frequency Range	0.1 Hz to 25 MHz
	Flatness	±0.5 dB (relative to 1 kHz)
	Harmonic Distortion	-40 dBc
	Stray	-40 dBc

	(Non-harmonic)	
	Total Harmonic Distortion	1%
	S/N Ratio	40 dB
Square/Pulse	Frequency Range	0.1 Hz to 15 MHz
	Rise/Fall Time	<15 ns
	Overshoot	<5%
	Duty Cycle	10% to 90%
	Duty Cycle Resolution	1% or 10 ns (the larger of the two)
	Min. Pulse Width	20 ns
	Pulse Width Resolution	10 ns or 5 bits (the larger of the two)
	Jitter	500 ps
Ramp	Frequency Range	0.1 Hz to 100 kHz
	Linearity	1%
	Symmetry	0 to 100%
Noise <sup>[1]</sup>	Bandwidth	25 MHz
Built-in Waveform	Frequency Range	0.1 Hz to 1 MHz
Arbitrary Waveform	Frequency Range	0.1 Hz to 10 MHz
	Waveform Length	2 to 16k points
Frequency	Accuracy	100 ppm (lower than 10 kHz) 50 ppm (higher than 10 kHz)
	Resolution	0.1 Hz or 4 bits, the larger of the two
Amplitude	Output Range	20 mVpp to 5 Vpp, HighZ 10 mVpp to 2.5 Vpp, 50 Ω
	Resolution	100 μV or 3 bits, the larger of the two
	Accuracy	2% (1 kHz)
DC Offset	Range	±2.5 V, HighZ ±1.25 V, 50 Ω
	Resolution	100 μV or 3 bits, the larger of the two
	Accuracy	2% (1 kHz)

## General Specifications

### Probe Compensation Output

Output Voltage <sup>[1]</sup>	About 3 V, peak-peak
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Frequency <sup>[1]</sup>	1 kHz
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### Power

Power Voltage	100 V-240 V, 45 Hz-440 Hz
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Power	Maximum 50 W
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Fuse	2 A, T degree, 250 V
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### Environment

Temperature	Operating: 0 °C to +50 °C
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Range	Non-operating: -40 °C to +70 °C
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Cooling Method	Fan cooling
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Humidity Range	0 °C to +30 °C: ≤95% Relative Humidity
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+30 °C to +40 °C: ≤75% Relative Humidity
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+40 °C to +50 °C: ≤45% Relative Humidity
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Altitude	Operating: under 3,000 meters
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Non-operating: under 15,000 meters
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### Physical Characteristics

Size <sup>[4]</sup>	Width×Height×Depth = 313.1 mm× 160.8 mm×122.4 mm	
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Weight <sup>[4]</sup>	Package Excluded	3.2 kg±0.2 kg
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Package Included	3.8 kg±0.5 kg
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### Calibration Interval

The recommended calibration interval is one year.

### Regulatory Information

Electromagnetic	2004/108/EC
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Compatibility	Execution standard EN 61326-1:2006 EN 61326-2-1:2006
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Safety	UL 61010-1:2004; CAN/CSA-C22.2 NO. 61010-1-2004; EN 61010-1:2001; IEC 61010-1:2001
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**Note<sup>[1]</sup>**: Typical.

**Note<sup>[2]</sup>**: Maximum value. 50ns, single-channel mode, dots display, auto memory depth.

**Note<sup>[3]</sup>**: Supporting legs and handle folded, knob height included.

**Note<sup>[4]</sup>**: Standard configuration.