

50Ω Divide-BY-5, DC-7 GHz

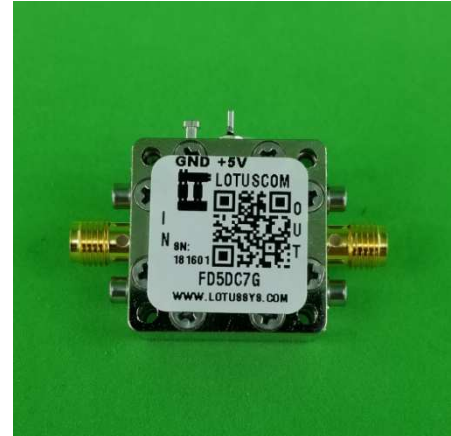
Case PN: 6UDD2W6S1A2

Features:

- Divide by M: (M = 5)
- Input frequency range: DC-7 GHz
- Low additive phase noise
- Rugged, shielded case (SMA Connector)

Applications:

- Cellular, Satellite Communication Systems
- PCS, W-CDMA
- ISM, LTE, SDR & Ham Radio



Electrical Specifications (Test Conditions: $T_A = +25^\circ\text{C}$, 50 Ohm System, $V_{cc} = +5\text{V}$)

| No. | Parameter | Conditions | Electrical Specification | | | |
|-----|----------------------------------|---------------------------------------|--------------------------|------|------|--------|
| | | | MIN | TYP. | MAX | UNITS |
| 1 | Maximum Input Freq. | | 7.0 | 7.5 | | GHz |
| 2 | Minimum Input Freq. | Sine Wave Input [1] | | 0.1 | | GHz |
| 3 | Input Power Range | $F_{in} = 1$ to 5 GHz | -15 | | +12 | dBm |
| | | $F_{in} = 5$ to 6 GHz | -15 | | +10 | |
| | | $F_{in} = 6$ to 7 GHz | -15 | | +5 | |
| 4 | Output Power | | -4 | -1 | | dBm |
| 5 | Reverse Leakage | Both RF Output Terminated | | -50 | | dBm |
| 6 | SSB Phase Noise (100 kHz offset) | $P_{in} = 0$ dBm, $F_{in} = 6$ GHz | | -153 | | dBc/Hz |
| 7 | Output Transition Time | $P_{in} = 0$ dBm, $F_{out} = 882$ MHz | | 100 | | Ps |
| 8 | Supply Current (I_{cc}) | $V_{cc} = 5.0$ V | | 80 | | mA |
| 9 | Supply Voltage (V_{cc}) | | 4.75 | 5 | 5.25 | V |

1. Below 100 MHz, a square wave input is required. Divider will operate down to DC for square-wave input signal.

Absolute Maximum Ratings

| Item | Parameter | Rating | UNITS |
|------|--|-------------|------------------|
| 1 | RF Input Power ($V_{cc} = +3\text{V}$) | 13 | dBm |
| 2 | Storage Temperature | -65 to +125 | $^\circ\text{C}$ |
| 3 | Operating Temperature | -40 to +85 | $^\circ\text{C}$ |

Outline Drawing (Inch)

