

50Ω Divide-BY-3, DC-7 GHz

Case PN: 6UDD2W6S1A2

Features:

- Divide by M: (M = 3)
- Input frequency range: DC-7 GHz
- Low additive phase noise
- Rugged, shielded case (SMA Connector)

Applications:

- Cellular, Satellite Communication Systems
- PCS, W-CDMA
- ISM, LTE
- SDR & Ham Radio

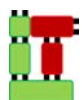
**Electrical Specifications (Test Conditions: T_A = +25°C, 50 Ohm System, V_{cc}=+5V)**

No.	Parameter	Conditions	Electrical Specification			
			MIN	TYP.	MAX	UNITS
1	Maximum Input Freq.		7.0	7.5		GHz
2	Minimum Input Freq.	Sine Wave Input [1]		0.1		GHz
3	Input Power Range	Fin = 1 to 6 GHz	-12		+12	dBm
		Fin = 6 to 7 GHz	-10		+10	
4	Output Power		-4	-1		dBm
5	Reverse Leakage	Both RF Output Terminated		-50		dBm
6	SSB Phase Noise (100 kHz offset)	Pin=0 dBm, Fin=6 GHz		-153		dBc/Hz
7	Output Transition Time	Pin=0 dBm, Fout=882 MHz		100		Ps
8	Supply Current (I _{cc})	V _{cc} = 5.0 V		69		mA
9	Supply Voltage (V _{cc})		4.75	5	5.25	V

1. Below 100 MHz, a square wave input is required. Divider will operate down to DC for square-wave input signal.

Absolute Maximum Ratings

Item	Parameter	Rating	UNITS
1	RF Input Power (V _{cc} = +3V)	13	dBm
2	Storage Temperature	-65 to +125	°C
3	Operating Temperature	-40 to +85	°C



Outline Drawing (Inch)

