

50 $\Omega$  Divide-BY-2, DC-8 GHz

Case PN: 6UDD2W6S1A2

## Features:

- Divide by M: (M = 2)
- Input frequency range: DC-8 GHz
- Low additive phase noise
- Rugged, shielded case (SMA Connector)

## Applications:

- Cellular, Satellite Communication Systems
- PCS, W-CDMA
- ISM, LTE
- SDR & Ham Radio

Electrical Specifications (Test Conditions:  $T_A = +25^\circ\text{C}$ , 50 Ohm System,  $V_{cc} = +3\text{V}$ )

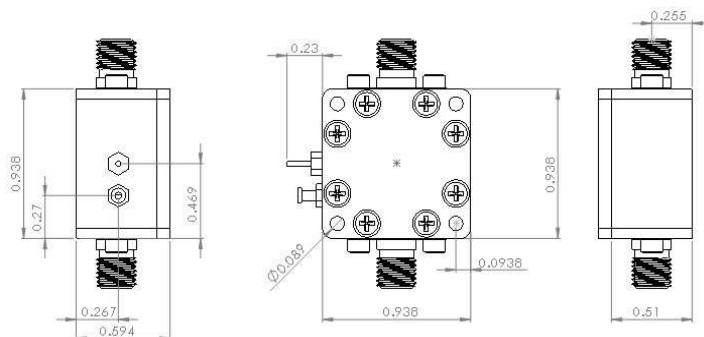
No.	Parameter	Conditions	Electrical Specification			
			MIN	TYP.	MAX	UNITS
1	Maximum Input Freq.		8	8.5		GHz
2	Minimum Input Freq.	Sine Wave Input [1]		0.2		GHz
3	Input Power Range	Fin = 1 to 7 GHz	-12		+12	dBm
		Fin = 7 to 8 GHz	-4		+10	
4	Output Power	Fin = 4 GHz	-6	-3		dBm
		Fin = 8 GHz	-12	-9		dBm
5	Reverse Leakage	RF Output Terminated, Fin=4 GHz, Pin=0 dBm		-30		dBm
6	SSB Phase Noise (100 kHz offset)	Pin=0 dBm, Fin=4 GHz		-148		dBc/Hz
7	Output Transition Time	Pin=0 dBm, Fout=882 MHz		145		Ps
8	Supply Current (Icc)	$V_{cc} = 3.0\text{ V}$		42	56	mA

1. Divider will operate down to DC for square-wave input signal.

## Absolute Maximum Ratings

Item	Parameter	Rating	UNITS
1	RF Input Power ( $V_{cc} = +3\text{V}$ )	15	dBm
2	Storage Temperature	-65 to +125	$^\circ\text{C}$
3	Operating Temperature	-40 to +85	$^\circ\text{C}$

## Outline Drawing (Inch)



<https://cdn.shopify.com/s/files/1/1592/7469/files/FD2DC8G.pdf>

