

4 SMA Outputs 10 MHz

Case PN: 6UED2W6H41SMAA1

Features:

- 10 MHz Frequency (4 outputs)
- Temperature Stability: ± 0.28 ppm (Stratum 3)
- Low RMS additive phase jitter
- Phase Noise: -155 dBc/Hz @ 10KHz offset
- Low Power Consumption
- Power Supply: +5~12V
- Temperature: -40 °C to 60 °C
- SMA Female Connector (50 ohm system)
- Option (External Clock Input)

General Description:

CRYTCXO10M4 is a high-performance TCXO/LVCMOS clock fanout. It can provide 4 outputs. If outputs are not used, terminate with 50Ohm load. It has low RMS additive phase jitter (42fs typical). The clock distribution module can operate from +5~12V voltage.

Applications:

- * Stratum 3
- * Clock Distribution
- * Test Instrumentation



Electrical Specifications:

Item	Parameter		Condition	Electrical Specification			
				MIN	TYP	MAX	UNITS
1	Nominal Frequency			10.000 (4 outputs)			MHz
2	Supply Voltage			5		12	V
3	Operating Temperature			-40		+60	°C
4	Storage Temperature			-55		+125	°C
5	Current Drain				50		mA
6	Frequency Tolerance					0.5	ppm
7	Frequency Stability	vs. Temperature	-40°C to 85°C			± 25	ppm
		vs. Load	Standard output load $\pm 5\text{pF}@15\text{pF}$ load		± 10		ppb
		vs. Supply Voltage	Standard Vcc $\pm 5\%$		± 10		ppb
9	Aging Tolerance		First year			± 1	ppm
			20 years			± 3	ppm
10	Holdover Stability		24 hours		$< \pm 4$		ppb
11	Warm-up Time				< 3		minute
12	Root Allan Variance		@25°C, $\tau=1.0\text{s}$		$< 1 \times 10^{-10}$		
13	Output Type			LVCMOS			
14	High-Level Output Voltage			90%*3.3			V
15	Low-Level Output Voltage					10%*3.3	V
16	Phase Noise @10MHz Carrier	@ 10 Hz offset			-98		dBc/Hz
		@ 100 Hz offset			-123		dBc/Hz
		@ 1 kHz offset			-142		dBc/Hz
		@ 10 kHz offset			-149		dBc/Hz
		@ 100 kHz offset			-150		dBc/Hz

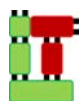
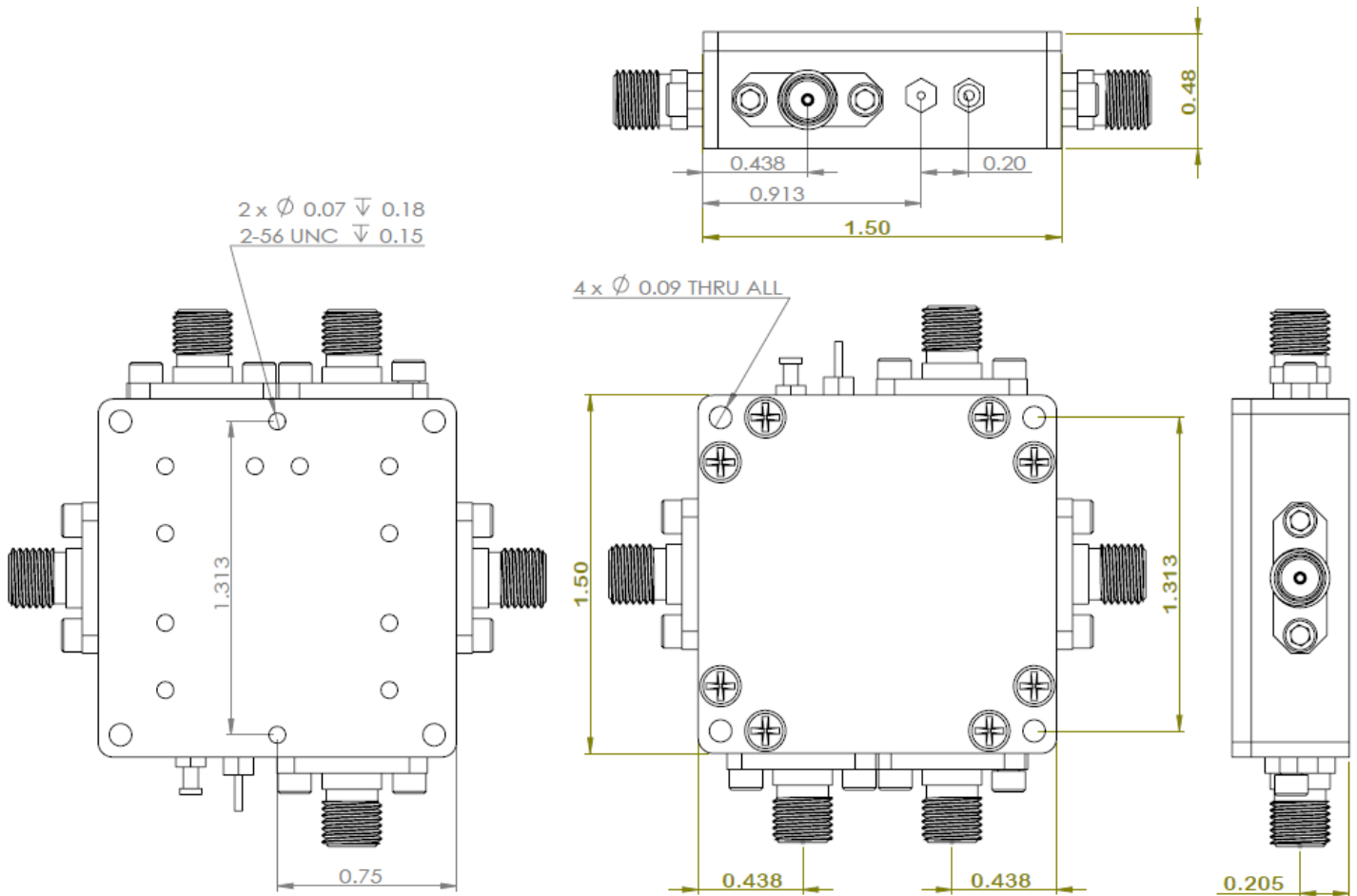


Coaxial

TCXO Clock Buffer (1:4) Fan Out

CRYTCXO10M4

Outline Drawing (inch):



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Rev. A CRYTCXO10M4