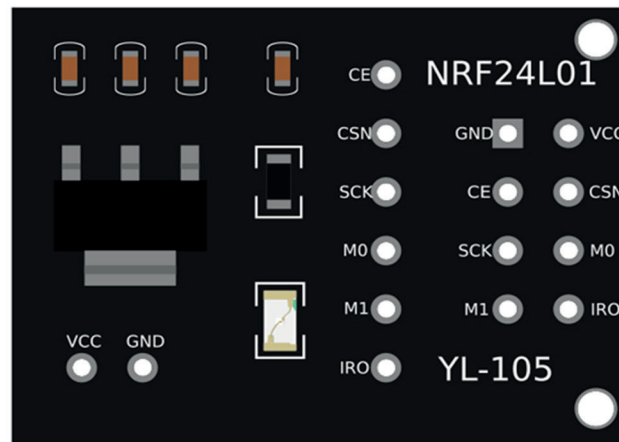


- Chip Enable pin **CE** ○
- SPI Chip Select pin **CSN** ○
- SPI Clock pin **SCK** ○
- SPI Slave Data Input pin **MOSI** ○
- SPI Slave Data Output pin **MISO** ○
- Active Low Interupt pin **IRQ** ○



Power Supply pin 5V **VCC**

Ground **GND**

Ground **GND** □ ○ **VCC** Power Supply pin 3.3V

Chip Enable pin **CE** ○ ○ **CSN** SPI Chip Select pin

SPI Clock pin **SCK** ○ ○ **MOSI** SPI Slave Data Input pin

SPI Slave Data Output pin **MISO** ○ ○ **IRQ** Active Low Interupt pin