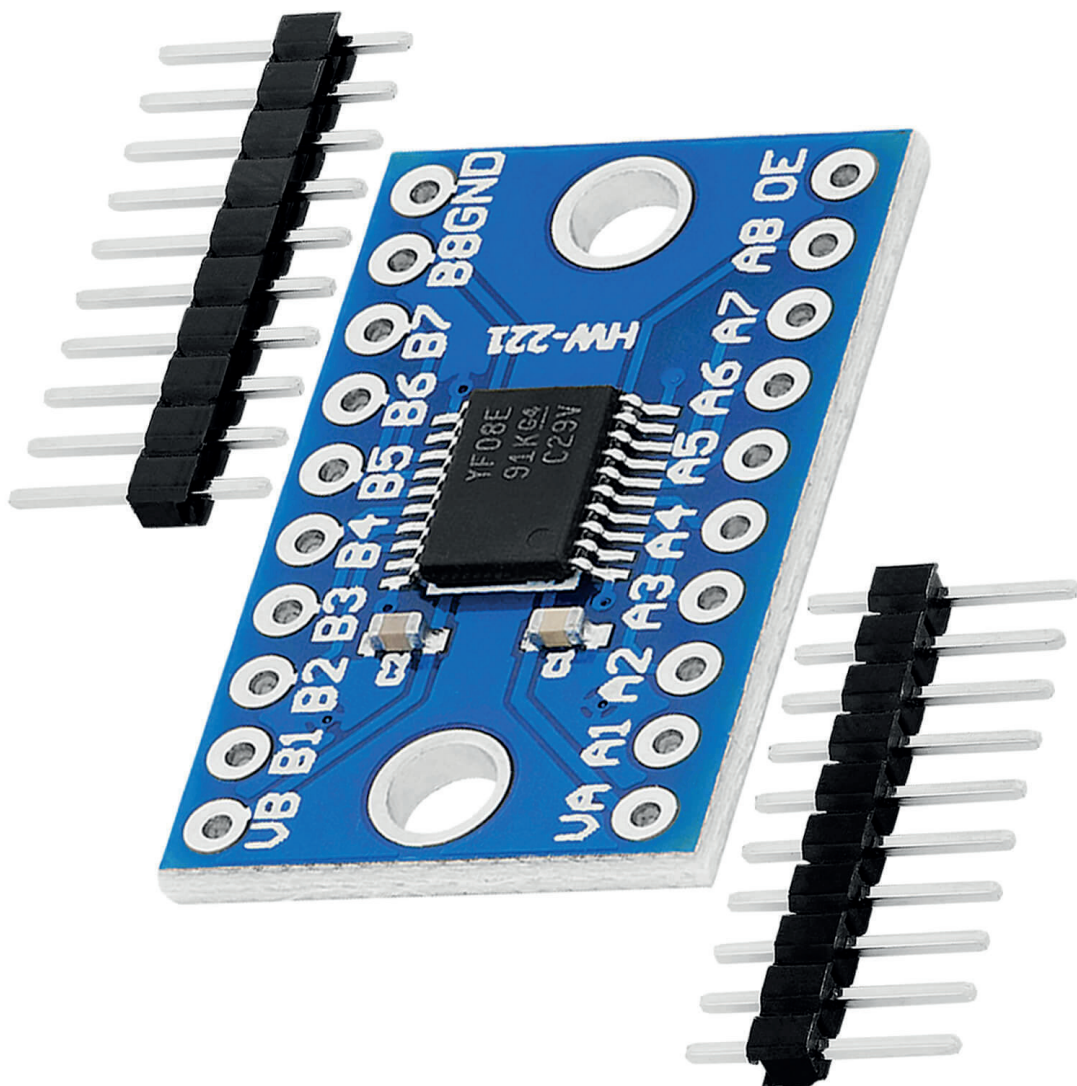


## Logic Level Converter 8 Kanal Datenblatt



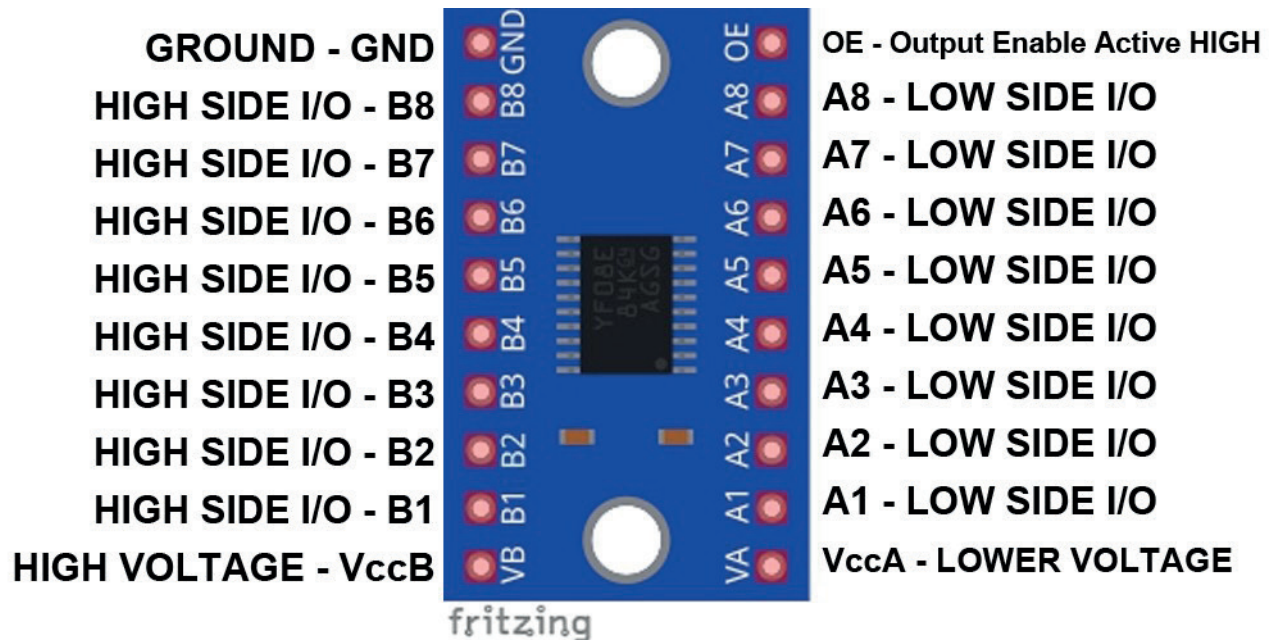
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## 1. Specifications

- Bi-directional
- Automatic direction control
- Maximum data rates: 110 Mbps (push pull)
- 1.2 Mbps (open drain)
- Low voltage 1.4 to 3.6 V
- High voltage 1.65 to 5.5 V
- Dimensions: 26 mm x 16 mm

## 2. Pinout



The VCCA pin receives any supply voltage between 1.4 V and 3.6 V. The A port tracks the VCCA pin supply voltage. The VCCB pin receives any supply voltage between 1.65 V and 5.5 V. The B port tracks the VCCB pin supply voltage. Two 0.1mF capacitors are recommended between the VCC connectors and GROUND.

GROUND is connected to both devices. And OE (Output Enable) is connected to VCCA (the lower voltage) through a 10 kOhm pullup resistor. B1 to B8 and A1 to A8 are eight independent signal pins. Pull-up resistors for the signal lines have been integrated in the chip.

### 3. Connection Diagram

