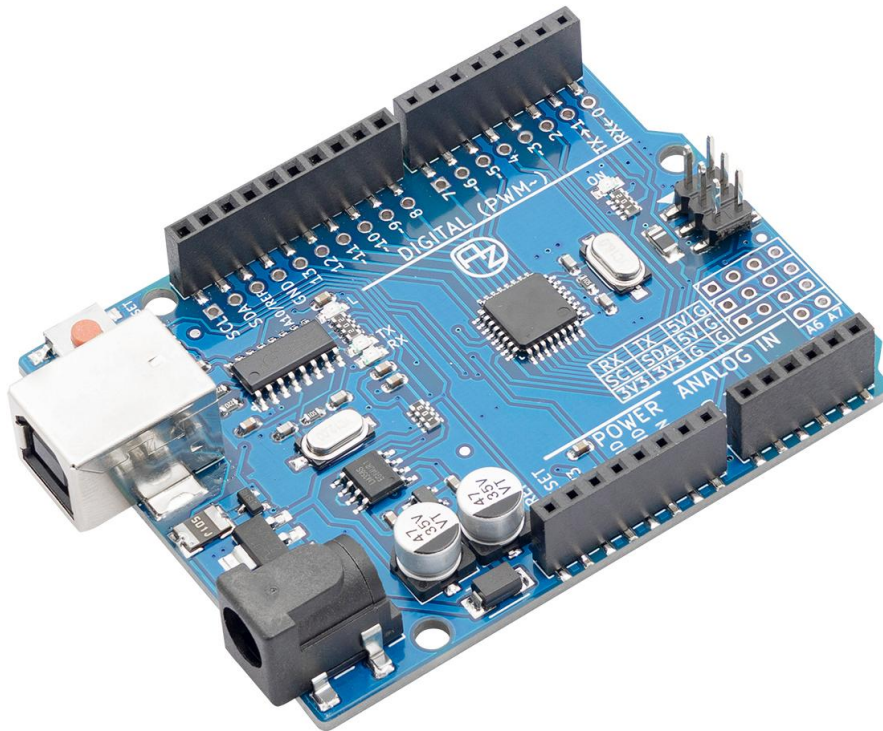


LGT8F328P

datasheet

# Microcontroller LGT8F328P

Type 3



## Specifications

Processor	LGT8F328P-QFN32
USB to serial bridge	CH340
SRAM	2KB
Flash size	32KB (boot occupies 1K, analogue eeprom occupies 2K)
EEPROM	1 - 8KB (shared with flash)
Voltage range on Vin pin(and DC Jack)	7-12V DC
Number of digital IOs	14
PWM	6
ADC	8 channels 12Bit
Reference voltage and accuracy	1.024V/2.048V/4.096V $\pm$ 1%
DAC	1 channel 8 bits
I/O DC output capability	30mA (with 6 channels supporting 80mA high current)
Bootloader burning method	SWD
Dimensions	74.8 x 53 x 13
Weight	ca. 23g

## Pin definition

D0	digital I/O Port; UART RXD
D1	digital I/O Port; UART TXD
D2	digital I/O Port; INT0 external interrupt input
D3	digital I/O Port; INT1 external interrupt input; PWM
D4	digital I/O Port; Internal DAC output
D5	digital I/O Port; PWM
D6	digital I/O Port; PWM
D7	digital I/O Port
D8	digital I/O Port
D9	digital I/O Port; PWM
D10	digital I/O Port; PWM; SPI slave mode chip select
D11	digital I/O Port; PWM; SPI MOSI
D12	digital I/O Port; SPI MISO
D13	digital I/O Port; SPI SCK
A0	Analogue input ADC0
A1	Analogue input ADC1
A2	Analogue input ADC2
A3	Analogue input ADC3
A4	Analogue input ADC4
A5	Analogue input ADC5
A6	Analogue input ADC6
A7	Analogue input ADC7