



Fig. A



Fig. B

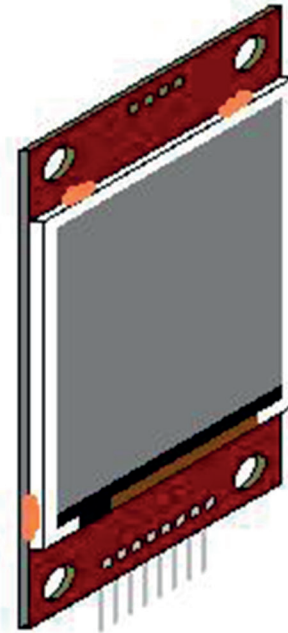


Fig. C



Fig. E

LED
SCK
SDA
A0
RESET
CS
GND
VCC

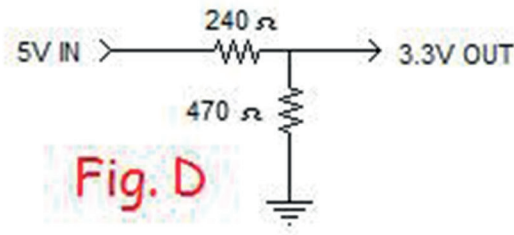


Fig. D

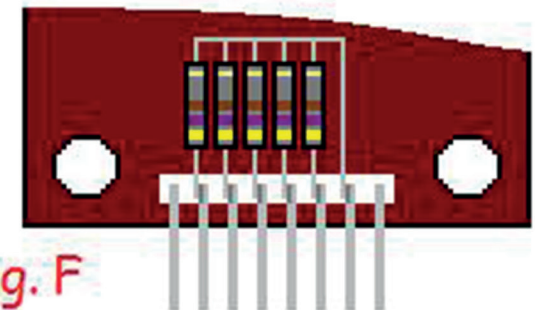


Fig. F