## **SIEMENS**

## Data sheet

## 6ES7214-1HG40-0XB0

SIMATIC S7-1200, CPU 1214C, compact CPU, DC/DC/relay, onboard I/O: 14 DI 24 V DC; 10 DO relay 2 A; 2 AI 0-10 V DC, Power supply: DC 20.4-28.8V DC, Program/data memory 100 KB



| General information                                     |  |
|---|--|
| Product type designation                                | CPU 1214C DC/DC/relay                    |
| Firmware version  | V4.2                                     |
| Engineering with  |  |
| Programming package                                     | STEP 7 V14 or higher                     |
| Supply voltage  |  |
| Rated value (DC)  |  |
| • 24 V DC   | Yes                                      |
| permissible range, lower limit (DC)                     | 20.4 V                                   |
| permissible range, upper limit (DC)                     | 28.8 V                                   |
| Reverse polarity protection                             | Yes                                      |
| Load voltage L+   |  |
| • Rated value (DC)                                      | 24 V                                     |
| <ul> <li>permissible range, lower limit (DC)</li> </ul> | 20.4 V                                   |
| • permissible range, upper limit (DC)                   | 28.8 V                                   |
| Input current   |  |
| Current consumption (rated value)                       | 500 mA; CPU only                         |
| Current consumption, max.                               | 1 500 mA; CPU with all expansion modules |

| Output current for backplane bus (5 V DC), max.  I 600 mA; Max. 5 V DC for SM and CM  Encoder supply 24 V encoder supply  • 24 V  L+ minus 4 V DC min.  Power loss Power loss, typ.  12 W  Memory  Work memory  • integrated • expandable No  Load memory  • integrated • Plug-in (SIMATIC Memory Card), max.  Backup • present • maintenance-free • without battery  CPU processing times for bit operations, typ. for floating point arithmetic, typ.  CPU-blocks Number of blocks (total)  DBs, FCs, FBs, counters and timers. The maximum number of addressable blocks ranges from 1 to 65535. There is no restriction, the entire working memory can be used  Data areas and their retentivity Retentive data area (incl. timers, counters, flags), max.  1 600 mA; Max. 5 V DC for SM and CM  Encoder supply  1 600 mA; Max. 5 V DC for SM and CM  Encoder supply  1 100 max. 5 V DC for SM and CM  A Wax. 5 V D | Inrush current, max.                                    | 12 A; at 28.8 V   |
|--|---|---|
| Output current for backplane bus (5 V DC), max.  I 600 mA; Max. 5 V DC for SM and CM  Encoder supply 24 V encoder supply 24 V encoder supply 24 V encoder supply 26 V expandable  Integrated Engrated Engrated Integrated In |   |   |
| for backplane bus (5 V DC), max.  1 600 mA; Max. 5 V DC for SM and CM  Encoder supply  24 V encoder supply  24 V encoder supply  24 V = L+ minus 4 V DC min.  Power loss Power loss, typ.  12 W  Memory  Work memory  integrated expandable Load memory  integrated Plug-in (SIMATIC Memory Card), max.  Backup present maintenance-free without battery  Yes without battery  Yes  CPU processing times for bit operations, typ. for floating point arithmetic, typ.  ON8 µs; / instruction  CPU-blocks  Number of blocks (total)  DBs, FCs, FBs, counters and timers. The maximum number of addressable blocks ranges from 1 to 65535. There is no restriction, the entire working memory can be used  Data areas and their retentivity  Retentive data area (incl. timers, counters, flags), max.  1 600 mA; Max. 5 V DC for SM and CM  L+ minus 4 V DC min.  L-  |   | 0.0 A 3   |
| Encoder supply  24 V encoder supply  24 V L+ minus 4 V DC min.  Power loss Power loss, typ.  12 W  Memory  Work memory  integrated expandable  Load memory  integrated Plug-in (SIMATIC Memory Card), max.  Backup present expandable vith SIMATIC memory card  Present expandable vith SIMATIC memory card  Present expandable  Output present expandable present expandable expandable  Output present expandable expandable expandable No  Load memory expandable expandable expandable No  Output present expandable e | Output current  |   |
| 24 V encoder supply  • 24 V L+ minus 4 V DC min.  Power loss Power loss, typ.  12 W  Memory  Work memory  • integrated • expandable  Load memory  • integrated • Plug-in (SIMATIC Memory Card), max.  Backup • present • maintenance-free • without battery  Pyes  CPU processing times for bit operations, typ. for word operations, typ. for word operations, typ.  CPU-blocks Number of blocks (total)  DBs, FCs, FBs, counters and timers. The maximum number of addressable blocks ranges from 1 to 65535. There is no restriction, the entire working memory can be used  Data areas and their retentivity  Retentive data area (incl. timers, counters, flags), max.  10 kbyte  | for backplane bus (5 V DC), max.                        | 1 600 mA; Max. 5 V DC for SM and CM   |
| 24 V encoder supply  • 24 V L+ minus 4 V DC min.  Power loss Power loss, typ.  12 W  Memory  Work memory  • integrated • expandable  Load memory  • integrated • Plug-in (SIMATIC Memory Card), max.  Backup • present • maintenance-free • without battery  Pyes  CPU processing times for bit operations, typ. for word operations, typ. for word operations, typ.  CPU-blocks Number of blocks (total)  DBs, FCs, FBs, counters and timers. The maximum number of addressable blocks ranges from 1 to 65535. There is no restriction, the entire working memory can be used  Data areas and their retentivity  Retentive data area (incl. timers, counters, flags), max.  10 kbyte  | Encoder supply  |   |
| Power loss Power loss, typ.  Memory  Work memory  integrated expandable  Load memory  integrated Plug-in (SIMATIC Memory Card), max.  Backup  present maintenance-free without battery  Poss  CPU processing times for bit operations, typ. for word operations, typ. for word operations, typ.  To floating point arithmetic, typ.  DBs, FCs, FBs, counters and timers. The maximum number of addressable blocks ranges from 1 to 65535. There is no restriction, the entire working memory can be used  Data areas and their retentivity  Retentive data area (incl. timers, counters, flags), max.  100 kbyte  A Mbyte  A Mbyte  With SIMATIC memory card  Backup   Dos, Fish, instruction  DBs, FCs, FBs, counters and timers. The maximum number of addressable blocks ranges from 1 to 65535. There is no restriction, the entire working memory can be used  OB  Number, max.  Limited only by RAM for code  |   |   |
| Power loss, typ. 12 W  Memory  Work memory  integrated 100 kbyte expandable No  Load memory  integrated 4 Mbyte Plug-in (SIMATIC Memory Card), max.  Backup  present Yes maintenance-free Yes without battery Yes  CPU processing times for bit operations, typ. 1.7 µs; / instruction for word operations, typ. 2.3 µs; / instruction  CPU-blocks  Number of blocks (total)  DBs, FCs, FBs, counters and timers. The maximum number of addressable blocks ranges from 1 to 65535. There is no restriction, the entire working memory can be used  OB Number, max. Limited only by RAM for code  Data areas and their retentivity  Retentive data area (incl. timers, counters, flags), max.   | • 24 V  | L+ minus 4 V DC min.  |
| Power loss, typ. 12 W  Memory  Work memory  integrated 100 kbyte expandable No  Load memory  integrated 4 Mbyte Plug-in (SIMATIC Memory Card), max.  Backup  present Yes maintenance-free Yes without battery Yes  CPU processing times for bit operations, typ. 1.7 µs; / instruction for word operations, typ. 2.3 µs; / instruction  CPU-blocks  Number of blocks (total)  DBs, FCs, FBs, counters and timers. The maximum number of addressable blocks ranges from 1 to 65535. There is no restriction, the entire working memory can be used  OB Number, max. Limited only by RAM for code  Data areas and their retentivity  Retentive data area (incl. timers, counters, flags), max.   |   |   |
| Memory  Work memory  integrated expandable  No  Load memory  integrated Plug-in (SIMATIC Memory Card), max.  Backup  present maintenance-free without battery  CPU processing times for bit operations, typ. for floating point arithmetic, typ.  CPU-blocks Number of blocks (total)  DBs, FCs, FBs, counters and timers. The maximum number of addressable blocks ranges from 1 to 65535. There is no restriction, the entire working memory can be used  Data areas and their retentivity  Retentive data area (incl. timers, counters, flags), max.  100 kbyte  4 Mbyte with SIMATIC memory card  4 Mbyte with SIMATIC memory card  9 Ves  100 A   |   | 40 W  |
| Work memory  ● integrated  ● expandable  Load memory  ● integrated  ● Plug-in (SIMATIC Memory Card), max.  Backup  ● present  ● present  ● maintenance-free  ● without battery  CPU processing times  for bit operations, typ.  for word operations, typ.  for word operations, typ.  1.7 µs; / instruction  for floating point arithmetic, typ.  CPU-blocks  Number of blocks (total)  DBs, FCs, FBs, counters and timers. The maximum number of addressable blocks ranges from 1 to 65535. There is no restriction, the entire working memory can be used  OB  ● Number, max.  Limited only by RAM for code  Data areas and their retentivity  Retentive data area (incl. timers, counters, flags), max.   | Power loss, typ.  | 12 VV   |
| integrated expandable  No  Load memory  integrated Plug-in (SIMATIC Memory Card), max.  Backup  present maintenance-free without battery  CPU processing times for bit operations, typ. for word operations, typ. for floating point arithmetic, typ.  CPU-blocks  Number of blocks (total)  DBs, FCs, FBs, counters and timers. The maximum number of addressable blocks ranges from 1 to 65535. There is no restriction, the entire working memory can be used  Data areas and their retentivity  Retentive data area (incl. timers, counters, flags), max.  100 kbyte  A Mbyte  No  No  1 Mbyte  A Mbyte  | Memory  |   |
| expandable  Load memory      integrated Plug-in (SIMATIC Memory Card), max.  Backup      present     maintenance-free     without battery  CPU processing times  for bit operations, typ. for word operations, typ. for floating point arithmetic, typ.  CPU-blocks  Number of blocks (total)  DBs, FCs, FBs, counters and timers. The maximum number of addressable blocks ranges from 1 to 65535. There is no restriction, the entire working memory can be used  OB  Number, max.  Limited only by RAM for code  Data areas and their retentivity  Retentive data area (incl. timers, counters, flags), max.  10 kbyte  | Work memory   |   |
| Load memory  integrated Plug-in (SIMATIC Memory Card), max.  Backup  present maintenance-free without battery  CPU processing times for bit operations, typ. for floating point arithmetic, typ.  CPU-blocks Number of blocks (total)  DBs, FCs, FBs, counters and timers. The maximum number of addressable blocks ranges from 1 to 65535. There is no restriction, the entire working memory can be used  Data areas and their retentivity  Retentive data area (incl. timers, counters, flags), max.  | • integrated  | 100 kbyte   |
| integrated Plug-in (SIMATIC Memory Card), max.  Backup  present maintenance-free without battery  CPU processing times for bit operations, typ. for word operations, typ. for floating point arithmetic, typ.  CPU-blocks  Number of blocks (total)  DBs, FCs, FBs, counters and timers. The maximum number of addressable blocks ranges from 1 to 65535. There is no restriction, the entire working memory can be used  OB Number, max.  Limited only by RAM for code  Data areas and their retentivity  Retentive data area (incl. timers, counters, flags), max.  10 kbyte   | • expandable  | No  |
| Plug-in (SIMATIC Memory Card), max. with SIMATIC memory card  Backup  present  maintenance-free  without battery  Pes  without battery  CPU processing times for bit operations, typ. for word operations, typ. for floating point arithmetic, typ.  CPU-blocks  Number of blocks (total)  DBs, FCs, FBs, counters and timers. The maximum number of addressable blocks ranges from 1 to 65535. There is no restriction, the entire working memory can be used  OB  Number, max.  Limited only by RAM for code  Data areas and their retentivity  Retentive data area (incl. timers, counters, flags), max.  10 kbyte  | Load memory   |   |
| ■ present       Yes         ■ maintenance-free       Yes         ■ without battery       Yes         CPU processing times         for bit operations, typ.       0.08 μs; / instruction         for word operations, typ.       1.7 μs; / instruction         for floating point arithmetic, typ.       2.3 μs; / instruction         CPU-blocks         Number of blocks (total)       DBs, FCs, FBs, counters and timers. The maximum number of addressable blocks ranges from 1 to 65535. There is no restriction, the entire working memory can be used         OB       • Number, max.       Limited only by RAM for code         Data areas and their retentivity       Retentive data area (incl. timers, counters, flags), max.       10 kbyte   | • integrated  | 4 Mbyte   |
| <ul> <li>present</li> <li>maintenance-free</li> <li>without battery</li> <li>Yes</li> <li>without battery</li> <li>CPU processing times</li> <li>for bit operations, typ.</li> <li>for word operations, typ.</li> <li>1.7 μs; / instruction</li> <li>for floating point arithmetic, typ.</li> <li>2.3 μs; / instruction</li> <li>CPU-blocks</li> <li>Number of blocks (total)</li> <li>DBs, FCs, FBs, counters and timers. The maximum number of addressable blocks ranges from 1 to 65535. There is no restriction, the entire working memory can be used</li> <li>OB</li> <li>Number, max.</li> <li>Limited only by RAM for code</li> <li>Data areas and their retentivity</li> <li>Retentive data area (incl. timers, counters, flags), max.</li> <li>10 kbyte</li> </ul>   | <ul> <li>Plug-in (SIMATIC Memory Card), max.</li> </ul> | with SIMATIC memory card  |
| <ul> <li>maintenance-free</li> <li>without battery</li> <li>Yes</li> </ul> CPU processing times <ul> <li>for bit operations, typ.</li> <li>for word operations, typ.</li> <li>for floating point arithmetic, typ.</li> </ul> CPU-blocks Number of blocks (total) <ul> <li>DBs, FCs, FBs, counters and timers. The maximum number of addressable blocks ranges from 1 to 65535. There is no restriction, the entire working memory can be used</li> </ul> OB <ul> <li>Number, max.</li> <li>Limited only by RAM for code</li> </ul> Data areas and their retentivity Retentive data area (incl. timers, counters, flags), max. <ul> <li>10 kbyte</li> </ul>   | Backup  |   |
| • without battery  Yes  CPU processing times  for bit operations, typ.  for word operations, typ.  for floating point arithmetic, typ.  CPU-blocks  Number of blocks (total)  DBs, FCs, FBs, counters and timers. The maximum number of addressable blocks ranges from 1 to 65535. There is no restriction, the entire working memory can be used  OB  • Number, max.  Limited only by RAM for code  Data areas and their retentivity  Retentive data area (incl. timers, counters, flags), max.  10 kbyte   | • present   | Yes   |
| CPU processing times  for bit operations, typ.  for word operations, typ.  for floating point arithmetic, typ.  CPU-blocks  Number of blocks (total)  DBs, FCs, FBs, counters and timers. The maximum number of addressable blocks ranges from 1 to 65535. There is no restriction, the entire working memory can be used  OB  Number, max.  Limited only by RAM for code  Data areas and their retentivity  Retentive data area (incl. timers, counters, flags), max.   | maintenance-free  | Yes   |
| for bit operations, typ.  for word operations, typ.  for word operations, typ.  for floating point arithmetic, typ.  CPU-blocks  Number of blocks (total)  DBs, FCs, FBs, counters and timers. The maximum number of addressable blocks ranges from 1 to 65535. There is no restriction, the entire working memory can be used  OB  Number, max.  Limited only by RAM for code  Data areas and their retentivity  Retentive data area (incl. timers, counters, flags), max.  | without battery   | Yes   |
| for word operations, typ.  for floating point arithmetic, typ.  CPU-blocks  Number of blocks (total)  DBs, FCs, FBs, counters and timers. The maximum number of addressable blocks ranges from 1 to 65535. There is no restriction, the entire working memory can be used  OB  Number, max.  Limited only by RAM for code  Data areas and their retentivity  Retentive data area (incl. timers, counters, flags), max.   | CPU processing times                                    |   |
| for floating point arithmetic, typ.  2.3 µs; / instruction  CPU-blocks  Number of blocks (total)  DBs, FCs, FBs, counters and timers. The maximum number of addressable blocks ranges from 1 to 65535. There is no restriction, the entire working memory can be used  OB  Number, max.  Limited only by RAM for code  Data areas and their retentivity  Retentive data area (incl. timers, counters, flags), max.   | for bit operations, typ.                                | 0.08 μs; / instruction  |
| CPU-blocks  Number of blocks (total)  DBs, FCs, FBs, counters and timers. The maximum number of addressable blocks ranges from 1 to 65535. There is no restriction, the entire working memory can be used  OB  Number, max.  Limited only by RAM for code  Data areas and their retentivity  Retentive data area (incl. timers, counters, flags), max.   | for word operations, typ.                               | 1.7 μs; / instruction   |
| Number of blocks (total)  DBs, FCs, FBs, counters and timers. The maximum number of addressable blocks ranges from 1 to 65535. There is no restriction, the entire working memory can be used  OB  • Number, max.  Limited only by RAM for code  Data areas and their retentivity  Retentive data area (incl. timers, counters, flags), max.   | for floating point arithmetic, typ.                     | 2.3 μs; / instruction   |
| Number of blocks (total)  DBs, FCs, FBs, counters and timers. The maximum number of addressable blocks ranges from 1 to 65535. There is no restriction, the entire working memory can be used  OB  • Number, max.  Limited only by RAM for code  Data areas and their retentivity  Retentive data area (incl. timers, counters, flags), max.   | CPU-blocks  |   |
| Number, max.  Limited only by RAM for code  Data areas and their retentivity  Retentive data area (incl. timers, counters, flags), max.  10 kbyte  | Number of blocks (total)                                | -   |
| Data areas and their retentivity  Retentive data area (incl. timers, counters, flags), max.  10 kbyte  | ОВ  |   |
| Retentive data area (incl. timers, counters, flags), max.  | Number, max.  | Limited only by RAM for code  |
| max.   | Data areas and their retentivity                        |   |
|  | Retentive data area (incl. timers, counters, flags),    | 10 kbyte  |
| Flag   |   |   |
| · · · · · · · · · · · · · · · · · · ·  | Flag  |   |
| Number, max.     8 kbyte; Size of bit memory address area  |   | 8 kbyte; Size of bit memory address area  |
| Local data   | Local data  |   |
| <ul> <li>per priority class, max.</li> <li>16 kbyte; Priority class 1 (program cycle): 16 KB, priority class</li> <li>to 26: 6 KB</li> </ul>   | • per priority class, max.                              | 16 kbyte; Priority class 1 (program cycle): 16 KB, priority class 2 to 26: 6 KB |
| Address area   | Address area  |   |
| Process image  | Process image   |   |

| • Inputs, adjustable  | 1 kbyte  |
|---|--|
| Outputs, adjustable   | 1 kbyte  |
| Hardware configuration  |  |
| Number of modules per system, max.                              | 3 comm. modules, 1 signal board, 8 signal modules  |
| Time of dec   |  |
| Time of day  Clock  |  |
| Hardware clock (real-time)                                      | Yes  |
| Backup time   | 480 h; Typical   |
| Deviation per day, max.   | ±60 s/month at 25 °C   |
| Deviation per day, max.   | 255 5/116/18/1 4: 25 5   |
| Digital inputs  |  |
| Number of digital inputs  | 14; Integrated   |
| of which inputs usable for technological                        | 6; HSC (High Speed Counting)   |
| functions   | Voo  |
| Source/sink input  Number of simultaneously controllable inputs | Yes  |
|   |  |
| all mounting positions  | 14   |
| — up to 40 °C, max.   | 14   |
| Input voltage   | 24 V   |
| Rated value (DC)  |  |
| • for signal "0"  | 5 V DC at 1 mA   |
| • for signal "1"  | 15 V DC at 2.5 mA  |
| Input delay (for rated value of input voltage)                  |  |
| for standard inputs   | 0.0 0.4 0.0 4.0 0.0 0.4 4.0 0  |
| — parameterizable   | 0.2 ms, 0.4 ms, 0.8 ms, 1.6 ms, 3.2 ms, 6.4 ms and 12.8 ms, selectable in groups of four |
| — at "0" to "1", min.   | 0.2 ms   |
| — at "0" to "1", max.   | 12.8 ms  |
| for interrupt inputs  |  |
| — parameterizable   | Yes  |
| for technological functions                                     |  |
| — parameterizable   | Single phase: 3 @ 100 kHz & 3 @ 30 kHz, differential: 3 @ 80 kHz & 3 @ 30 kHz            |
| Cable length  |  |
| • shielded, max.  | 500 m; 50 m for technological functions  |
| • unshielded, max.  | 300 m; for technological functions: No   |
| Digital outputs   |  |
| Number of digital outputs                                       | 10; Relays   |
| Switching capacity of the outputs                               |  |
| <ul><li>with resistive load, max.</li></ul>                     | 2 A  |
| • on lamp load, max.  | 30 W with DC, 200 W with AC  |
| Output delay with resistive load                                |  |

| • "0" to "1", max.  | 10 ms; max.  |
|---|--|
| • "1" to "0", max.  | 10 ms; max.  |
| Relay outputs   |  |
| Number of relay outputs   | 10   |
| <ul> <li>Number of operating cycles, max.</li> </ul>                | mechanically 10 million, at rated load voltage 100 000 |
| Cable length  |  |
| • shielded, max.  | 500 m  |
| • unshielded, max.  | 150 m  |
| Analog inputs   |  |
| Number of analog inputs   | 2  |
| Input ranges  |  |
| <ul> <li>Voltage</li> </ul>   | Yes  |
| Input ranges (rated values), voltages                               |  |
| • 0 to +10 V  | Yes  |
| <ul><li>— Input resistance (0 to 10 V)</li></ul>                    | ≥100k ohms   |
| Cable length  |  |
| • shielded, max.  | 100 m; twisted and shielded                            |
| Analog outputs  |  |
| Number of analog outputs  | 0  |
| Analog value generation for the inputs                              |  |
| Integration and conversion time/resolution per channel              |  |
| <ul> <li>Resolution with overrange (bit including sign),</li> </ul> | 10 bit   |
| max.  |  |
| <ul> <li>Integration time, parameterizable</li> </ul>               | Yes  |
| Conversion time (per channel)                                       | 625 μs   |
| Encoder   |  |
| Connectable encoders  |  |
| • 2-wire sensor   | Yes  |
| 1. Interface  |  |
| Interface type  | PROFINET   |
| Physics   | Ethernet   |
| Isolated  | Yes  |
| automatic detection of transmission rate                            | Yes  |
| Autonegotiation   | Yes  |
| Autocrossing  | Yes  |
| Interface types   |  |
| Number of ports   | 1  |
| • integrated switch   | No   |
| Protocols   |  |
| PROFINET IO Controller  | Yes  |
|   |  |

| PROFINET IO Device   | Yes   |
|--|---|
| <ul> <li>SIMATIC communication</li> </ul>                    | Yes   |
| <ul> <li>Open IE communication</li> </ul>                    | Yes   |
| • Web server   | Yes   |
| Media redundancy   | No  |
| PROFINET IO Controller                                       |   |
| Transmission rate, max.                                      | 100 Mbit/s  |
| Services   |   |
| <ul><li>— PG/OP communication</li></ul>                      | Yes   |
| — S7 routing   | Yes   |
| <ul> <li>Isochronous mode</li> </ul>                         | No  |
| <ul> <li>Open IE communication</li> </ul>                    | Yes   |
| — IRT  | No  |
| — MRP  | No  |
| — MRPD   | No  |
| — PROFlenergy  | No  |
| <ul> <li>Prioritized startup</li> </ul>                      | Yes   |
| <ul> <li>Number of IO devices with prioritized</li> </ul>    | 16  |
| startup, max.  |   |
| <ul> <li>Number of connectable IO Devices, max.</li> </ul>   | 16  |
| <ul> <li>Number of connectable IO Devices for RT,</li> </ul> | 16  |
| max.   |   |
| — of which in line, max.                                     | 16  |
| <ul> <li>Activation/deactivation of IO Devices</li> </ul>    | Yes   |
| Number of IO Devices that can be                             | 8   |
| simultaneously activated/deactivated, max.                   | The articles was called a filter and at a time at a day and a sufficient  |
| — Updating time  | The minimum value of the update time also depends on the communication component set for PROFINET IO, on the number |
|  | of IO devices and the quantity of configured user data.   |
| PROFINET IO Device   |   |
| Services   |   |
| — PG/OP communication  | Yes   |
| — S7 routing   | Yes   |
| — Isochronous mode   | No  |
| — Open IE communication                                      | Yes   |
| — IRT  | No  |
| — MRP  | No  |
| — MRPD   | No  |
| — PROFlenergy  | Yes   |
| — Shared device  | Yes   |
| — Number of IO Controllers with shared                       | 2   |
| device, max.   |   |
|  |   |

| Protocols                                   |  |
|---|--|
| Supports protocol for PROFINET IO           | Yes  |
| PROFIBUS                                    | Yes; CM 1243-5 (master) or CM 1242-5 (slave) required                |
| AS-Interface                                | Yes; CM 1243-2 required  |
| Protocols (Ethernet)                        |  |
| • TCP/IP                                    | Yes  |
| • DHCP                                      | No   |
| • SNMP                                      | Yes  |
| • DCP                                       | Yes  |
| • LLDP                                      | Yes  |
| Open IE communication                       |  |
| • TCP/IP                                    | Yes  |
| — Data length, max.                         | 8 kbyte  |
| • ISO-on-TCP (RFC1006)                      | Yes  |
| — Data length, max.                         | 8 kbyte  |
| • UDP                                       | Yes  |
| — Data length, max.                         | 1 472 byte   |
| Web server                                  |  |
| • supported                                 | Yes  |
| <ul> <li>User-defined websites</li> </ul>   | Yes  |
| Further protocols                           |  |
| • MODBUS                                    | Yes  |
| Communication functions                     |  |
| S7 communication                            |  |
| • supported                                 | Yes  |
| • as server                                 | Yes  |
| • as client                                 | Yes  |
| User data per job, max.                     | See online help (S7 communication, user data size)                   |
| Number of connections                       |  |
| • overall                                   | 16; dynamically  |
|   |  |
| Test commissioning functions Status/control |  |
|   | Yes  |
| Status/control variable     Variables       |  |
| Variables                                   | Inputs/outputs, memory bits, DBs, distributed I/Os, timers, counters |
| Forcing                                     |  |
| • Forcing                                   | Yes  |
| Diagnostic buffer                           |  |
| • present                                   | Yes  |
| Traces                                      |  |
| Number of configurable Traces               | 2  |
| -   |  |

| <ul> <li>Memory size per trace, max.</li> </ul>   | 512 kbyte            |
|---|----------------------|
| Interrupts/diagnostics/status information   |                      |
| Diagnostics indication LED  |                      |
| RUN/STOP LED  | Yes                  |
| • ERROR LED   | Yes                  |
| MAINT LED   | Yes                  |
| Integrated Functions  |                      |
| Number of counters  | 6                    |
| Counting frequency (counter) max.   | 100 kHz              |
| Frequency measurement   | Yes                  |
| controlled positioning  | Yes                  |
| Number of position-controlled positioning axes, max.  | 8                    |
| Number of positioning axes via pulse-direction interface  | Up to 4 with SB 1222 |
| PID controller  | Yes                  |
| Number of alarm inputs  | 4                    |
| Potential separation  |                      |
| Potential separation digital inputs   |                      |
| Potential separation digital inputs   | 500V AC for 1 minute |
| <ul> <li>between the channels, in groups of</li> </ul>  | 1                    |
| Potential separation digital outputs  |                      |
| Potential separation digital outputs  | Relays               |
| • between the channels  | No                   |
| • between the channels, in groups of  | 2                    |
| EMC   |                      |
| Interference immunity against discharge of static electricity   |                      |
| <ul> <li>Interference immunity against discharge of<br/>static electricity acc. to IEC 61000-4-2</li> </ul> | Yes                  |
| Test voltage at air discharge   | 8 kV                 |
| Test voltage at contact discharge   | 6 kV                 |
| Interference immunity to cable-borne interference   |                      |
| <ul> <li>Interference immunity on supply lines acc. to<br/>IEC 61000-4-4</li> </ul>                         | Yes                  |
| <ul> <li>Interference immunity on signal cables acc. to<br/>IEC 61000-4-4</li> </ul>                        | Yes                  |
| Interference immunity against voltage surge   |                      |
| <ul> <li>Interference immunity on supply lines acc. to<br/>IEC 61000-4-5</li> </ul>                         | Yes                  |
| Interference immunity against conducted variable disturbance induced by high-frequency fields               |                      |
| <ul> <li>Interference immunity against high-frequency<br/>radiation acc. to IEC 61000-4-6</li> </ul>        | Yes                  |
| Emission of radio interference acc. to EN 55 011  |                      |

• Limit class A, for use in industrial areas

• Limit class B, for use in residential areas

Yes; Group 1

Yes; When appropriate measures are used to ensure compliance with the limits for Class B according to EN 55011

| Degree and class of protection  |   |
|---|---|
| IP degree of protection   | IP20  |
| Standards, approvals, certificates  |   |
| CE mark   | Yes   |
| UL approval   | Yes   |
| cULus   | Yes   |
| FM approval   | Yes   |
| RCM (formerly C-TICK)   | Yes   |
| KC approval   | Yes   |
| Marine approval   | Yes   |
| Ambient conditions  |   |
| Free fall   |   |
| • Fall height, max.   | 0.3 m; five times, in product package   |
| Ambient temperature during operation  |   |
| • min.  | -20 °C  |
| • max.  | 60 °C; Number of simultaneously activated inputs or outputs 7 or 5 (no adjacent points) at 60 °C horizontal or 50 °C vertical, 14 or 10 at 55 °C horizontal or 45 °C vertical |
| <ul> <li>horizontal installation, min.</li> </ul>                                   | -20 °C  |
| <ul> <li>horizontal installation, max.</li> </ul>                                   | 60 °C   |
| • vertical installation, min.   | -20 °C  |
| • vertical installation, max.   | 50 °C   |
| Ambient temperature during storage/transportation                                   |   |
| • min.  | -40 °C  |
| • max.  | 70 °C   |
| Air pressure acc. to IEC 60068-2-13   |   |
| Operation, min.   | 795 hPa   |
| Operation, max.   | 1 080 hPa   |
| Storage/transport, min.   | 660 hPa   |
| • Storage/transport, max.   | 1 080 hPa   |
| Altitude during operation relating to sea level                                     |   |
| Installation altitude, min.   | -1 000 m  |
| • Installation altitude, max.   | 2 000 m   |
| Relative humidity   |   |
| Operation, max.   | 95 %; no condensation   |
| Vibrations  |   |
| <ul> <li>Vibration resistance during operation acc. to<br/>IEC 60068-2-6</li> </ul> | 2 g (m/s²) wall mounting, 1 g (m/s²) DIN rail   |
| <ul> <li>Operation, tested according to IEC 60068-2-6</li> </ul>                    | Yes   |

| Shock testing   |   |
|---|---|
| • tested according to IEC 60068-2-27                            | Yes; IEC 68, Part 2-27 half-sine: strength of the shock 15 g (peak value), duration 11 ms |
| Pollutant concentrations  |   |
| <ul> <li>SO2 at RH &lt; 60% without condensation</li> </ul>     | S02: < 0.5 ppm; H2S: < 0.1 ppm; RH < 60% condensation-free                                |
| Configuration   |   |
| Programming   |   |
| Programming language  |   |
| — LAD   | Yes   |
| — FBD   | Yes   |
| — SCL   | Yes   |
| Know-how protection   |   |
| <ul> <li>User program protection/password protection</li> </ul> | Yes   |
| Copy protection   | Yes   |
| <ul> <li>Block protection</li> </ul>                            | Yes   |
| Access protection   |   |
| Protection level: Write protection                              | Yes   |
| <ul> <li>Protection level: Read/write protection</li> </ul>     | Yes   |
| <ul> <li>Protection level: Complete protection</li> </ul>       | Yes   |
| Cycle time monitoring   |   |
| adjustable  | Yes   |
| Dimensions  |   |
| Width   | 110 mm  |
| Height  | 100 mm  |
| Depth   | 75 mm   |
| Weights   |   |
| Weight, approx.   | 435 g   |
| last modified:  | 02/24/2020  |