

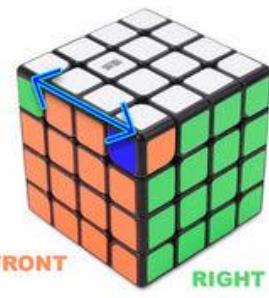
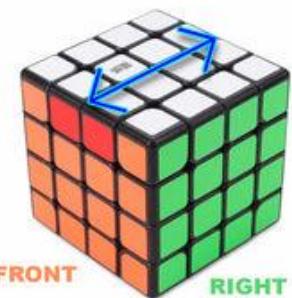
4x4 Parity

OLL Parity



r2 B2 U2 l U2 r' U2 r U2 F2 r F2 l' B2 r2

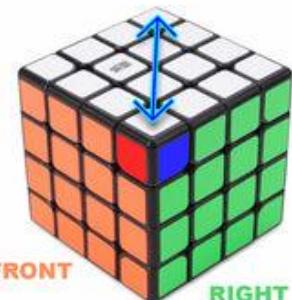
PLL Parity



(Uu)2 (Ll)2 U2 l2 U2 (Ll)2 (Uu)2

L2 D (Ff)2 (Ll)2 F2 l2 F2 (Ll)2
(Ff)2 D' L2

(Uu)2 (Ll)2 U2 l2 U2 (Ll)2
(Uu)2 F' U' F U F R' F2 U F
U F' U' F R



(Uu)2 (Ll)2 U2 l2 U2 (Ll)2 (Uu)2 R
U' L U2 R' U R L' U' L U2 R' U L' U

