

QS1600T603 60A TRIAC Homogeneous Current SiC Schottky Diode



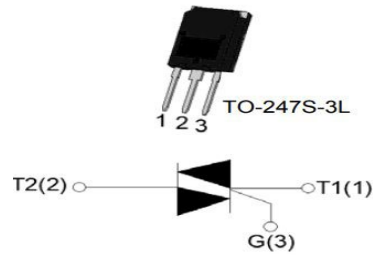
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Description

The QS1600T603 triac is suitable for general purpose AC switching. It can be used as an ON/OFF function in applications such as heating regulation, induction motor starting circuits, for phase control operation in light dimmers, motor speed controllers. QS1600T603 snubberless triac is especially recommended for use on inductive loads. Package TO-247S-3L is RoHS compliant.

Package

TO-247S-3L



Main Features

Symbol	Value	Unit
IT(RMS)	60	A
VDRM /VRRM	1600	V
IGTI/II/III	50/50/50	mA

Feature / Advantages:

- Thyristor for line frequency
- Planar passivated chip
- Long term stability

Application:

- Line rectifying 50/60 Hz
- Softstart AC motor control
- DC Motor control
- Power converter
- Ac power control
- Lighting ad temperature control

Absolute Maximum Ratings

Parameter	Symbol	Value	Unit
Storage junction temperature range	Tstg	-40-150	°C
Operating junction temperature range	T _j	-40-125	°C
Repetitive peak off-state voltage (T _j =25°C)	VDRM	1600	V
Repetitive peak reverse voltage (T _j =25°C)	VRRM	1600	V
RMS on-state current (T _c ≤92°C)	IT(RMS)	60	A
Non repetitive surge peak on-state current (full cycle , t _p =20ms , T _j =25°C)	ITSM	600	A
Non repetitive surge peak on-state current (full cycle , t _p =16.6ms , T _j =25°C)		660	
I ² t value for fusing (t _p =10ms , T _j =25°C)	I ² t	1800	A ² s
Critical rate of rise of on-state current (I _G =2×I _{GT} , f=100Hz , T _j =125°C)	dI/dt	100	A/μs
Peak gate current (t _p =20μs , T _j =125°C)	IGM	8	A
Average gate power dissipation (T _j =125°C)	PG(AV)	0.5	W
Peak gate power	PGM	10	W
Peak pulse voltage (T _j =25°C; non-repetitive,off-state;FIG.7)	Vpp	Vpp	kV

Electrical Characteristics (T_j=25°C unless otherwise specified)

Symbol	Test Condition	Quadrant	Value	Unit
IGT	V _D =12V R _L =33Ω	I-II-III	MAX.	50 mA
VGT		I-II-III	MAX.	1.3 V
VGD	V _D =VDRM T _j =125°C R _L =3.3KΩ	I-II-III	MIN.	0.2 V
IL	I _G =1.2IGT	I-III	MAX.	120 mA
		II		120 mA
IH	I _T =1A		MAX.	80 mA
dV/dt	V _D =1070V Gate Open T _j =125°C		MIN.	1500 V/μs
(dI/dt)_c	(dV/dt) _c =20V/μs T _j =125°C		MIN.	28 A/ms
ton	I _G =80mA I _A =400mA I _R =40mA T _j =25°C		TYP.	7 μs
toff				70 μs

Static Characteristics

Symbol	Parameter	Value(MAX.)	Unit
V_{TM}	$I_{TM}=80A$ $t_p=380\mu s$	$T_j=25^\circ C$	1.7 V
V_{TO}	Threshold voltage	$T_j=125^\circ C$	0.75 V
RD	Dynamic resistance	$T_j=125^\circ C$	24 mΩ
IDRM	VD=VDRMVR =VRRM	$T_j=25^\circ C$	15 μA
IRRM		$T_j=125^\circ C$	10 mA

Thermal Resistances

Symbol	Parameter	Value	Unit
R_{th(j-c)}	junction to case (AC)	0.35	°C/W
R_{th(j-a)}	junction to ambient (AC)	45	°C/W

FIG.1 Maximum power dissipation versus RMS on-state current

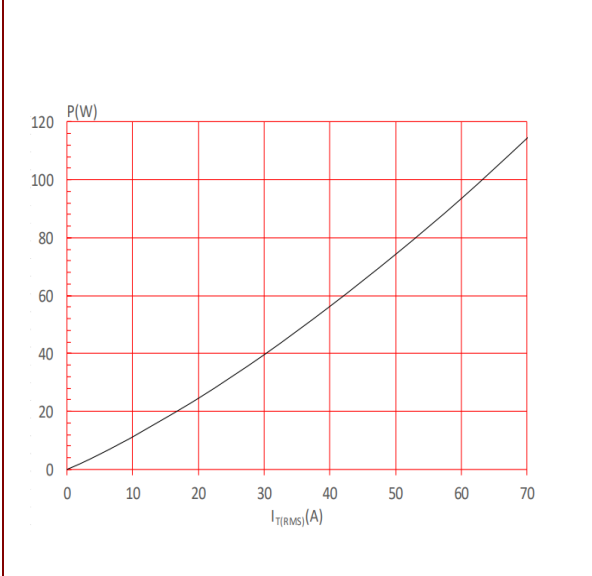


FIG.2: RMS on-state current versus case temperature

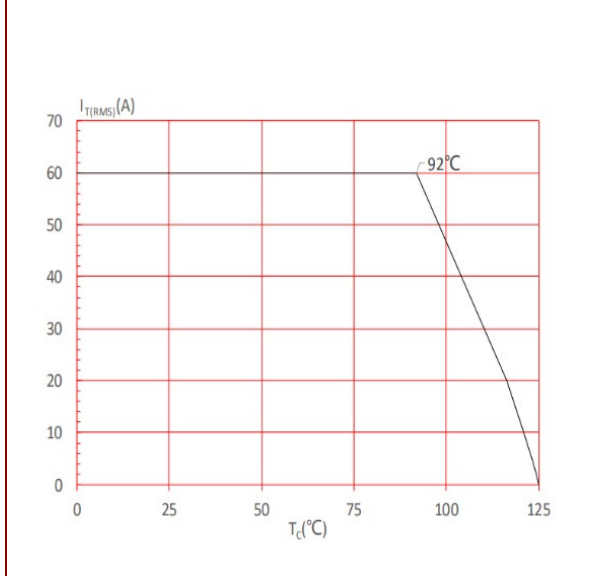


FIG.3: Surge peak on-state current versus number of cycles

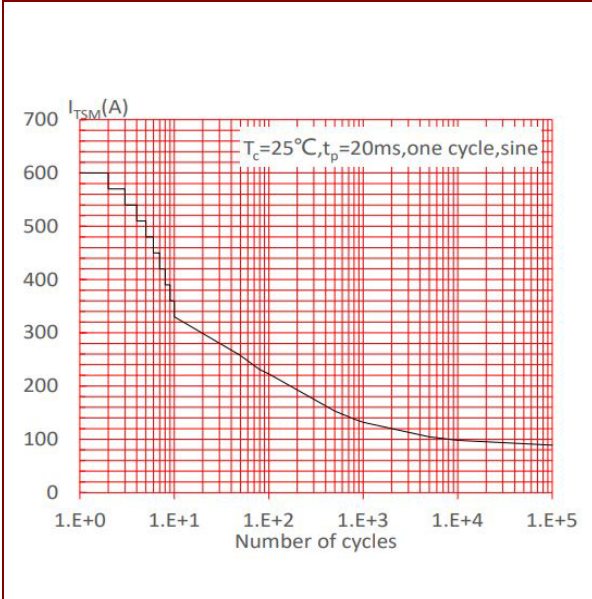
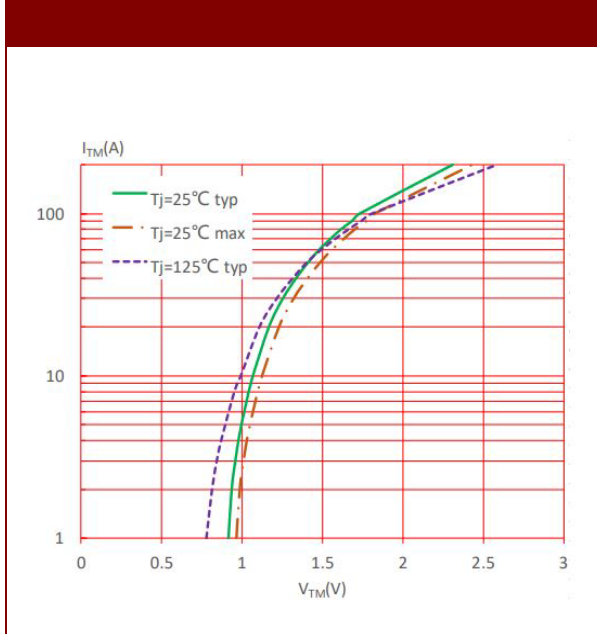


FIG.4: On-state characteristics



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FIG.5: Non-repetitive surge peak on-state current for a sinusoidal pulse with width $t_p < 20\text{ms}$, and corresponding value of I^2t ($di/dt < 100\text{A}/\mu\text{s}$)

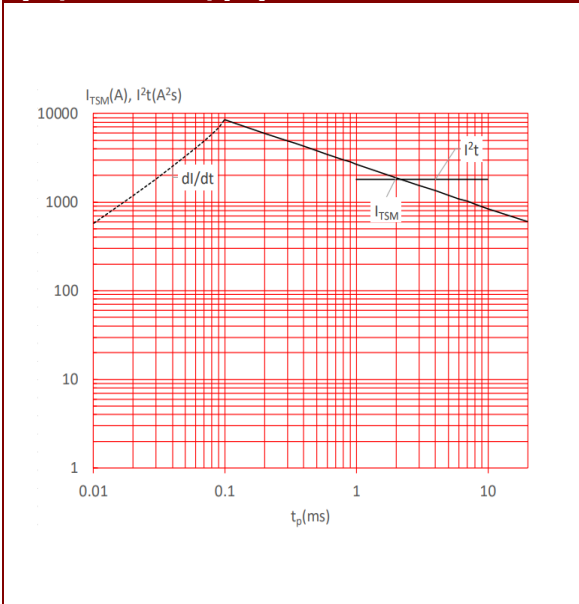


FIG.6: Relative variations of gate trigger current, holding current and latching current versus junction temperature

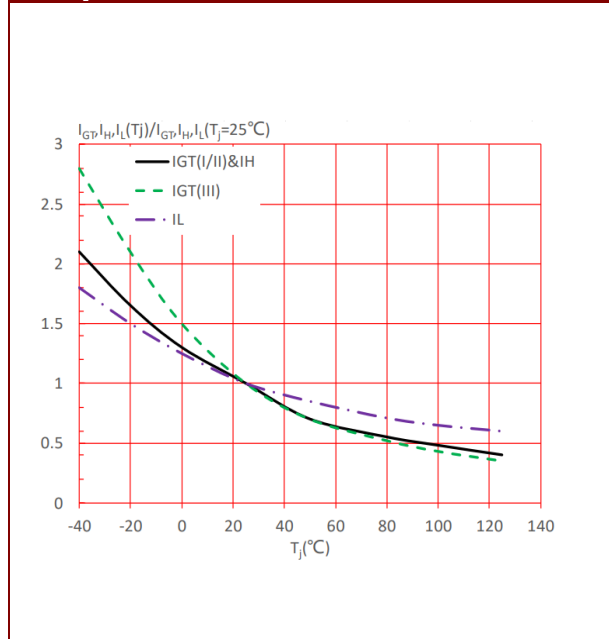
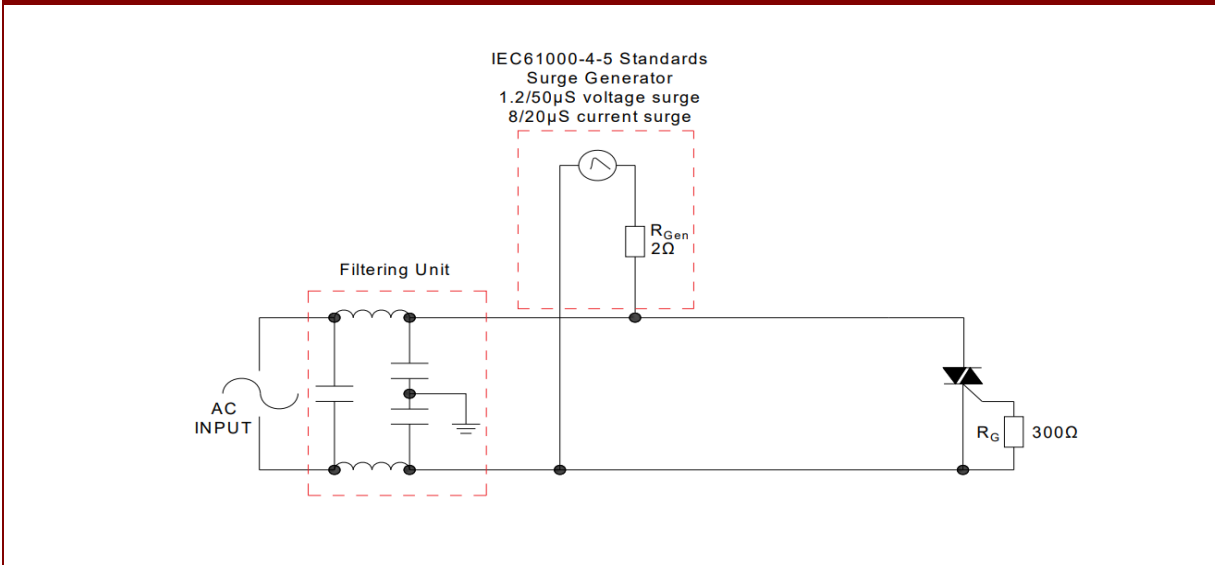


FIG.7 : Test circuit for inductive and resistive loads to IEC-61000-4-5 standards



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Order code	Voltage $V_{DRM}/V_{RRM}(V)$	IGT(mA)	Package	Base qty. (pcs)	Delivery mode
		I - II - III			
QS1600T603	1600	50	TO-247S-3L	30	Tube

Package Mechanical Data

Ref.	Dimensions					
	Millimeters			Inches		
	Min.	Typ.	Max.	Min.	Typ.	Max.
A	15.1		16.1	0.594		0.634
B	19.8		20.8	0.78		0.819
C	13.8		14.8	0.543		0.583
D	3.00		4.00	0.118		0.157
	2.75		3.35	0.108		0.132
F	1.30		1.50	0.051		0.059
G	5.10		5.80	0.201		0.228
H	4.50		5.50	0.177		0.217
J	1.45		2.15	0.057		0.085
K	1.90		2.80	0.075		0.110
L	0.55		0.80	0.022		0.031
P	2.00		2.40	0.079		0.094

