MAKE NOISE MATHS

MATHS CHANNEL 1/4 - Cycling A/D Envelope Generator with EOR or EOC Output

- CHANNEL 1/4 CV Input
 - CHANNEL 1/4 CV In Slew Limiter
 - CHANNEL 1/4 Gate In ASR Envelope Generator
 - CHANNEL 1/4 Curve Affects Rate of CV Change (CV Δ)
- CHANNEL 1/4 GATE TRIG Input Trigger A/D Envelope Generator
- CHANNEL 1/4 CURVE Attack & Decay Coupled to LOG/LIN/EXP setting
- CHANNEL 1/4 CYCLE Button Cycle A/D Envelope
- CHANNEL 1 EOR Delay Trigger Events by Rise Time
 - Gate High when Rise (Attack) Portion of Cycle is Completed
- CHANNEL 4 EOC Clock/Pulse Out

MATHS CHANNELS 1/4 EOR VS EOC

CHANNEL 1/4 CV Δ	CHANNEL 1 EOR	CHANNEL 4 EOC
CV LEVEL RISING	Gate Off Trigger Delay Time (Time Between Gates)	Gate High Gate Length
CV LEVEL FALLING	Gate High Gate Length	Gate Off Clock Tempo (Time Between Gates)

MATHS CHANNEL 2 - Bipolar (-10V to +10V)

- Wider CV Range than CH3
- No Input CV Offset
- Input Signal CV/Audio Attenuverter

MATHS CHANNEL 3 - Bipolar (-5V to +5V)

- Higher CV Precision than CH2
- No Input CV Offset
- Input Signal CV/Audio Attenuverter

MATHS OUTPUTS

- **SUM** CV/Audio Summing Mixer of All Four Outputs
- INV Inverted SUM Output
- **OR** Highest Momentary Positive CV Value of All Four Channels
 - Create Rhythms with Two "Competing" Asynchronous Cycling Envelopes
- CHANNEL 1/4 UNITY Unattenuverted Value CHANNEL 1/4 Outputs