

SPECIFICATION FOR APPROVAL

() Preliminary Specification

() Final Specification

Title

55.0" WUXGA TFT LCD

BUYER	CHN
MODEL	

SUPPLIER	LG DISPLAY Co., Ltd.
*MODEL	LD550DUN
SUFFIX	TKB2(RoHS Verified)

APPROVED BY	SIGNATURE DATE	APPROVED BY	SIGNATURE DATE
/		D. I. HEO / Team Leader	01282
1		REVIEWED BY J. J. LEE / Project Leader	0/20-3 2013-09,18
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Please return 1 copy for your your signature and co		TV Product Desi LG Display Co	



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RECORD OF REVISIONS

Revision No.	Revision Date	Page	Description
0.0	2018.08.01	-	Preliminary Specification(First Draft)
0.1	2018.09.12	4, 23, 24	Change the Body depth dimension 38.9mm \rightarrow 39.2mm
1.0	2018.09.18	-	Final CAS



1. General Description

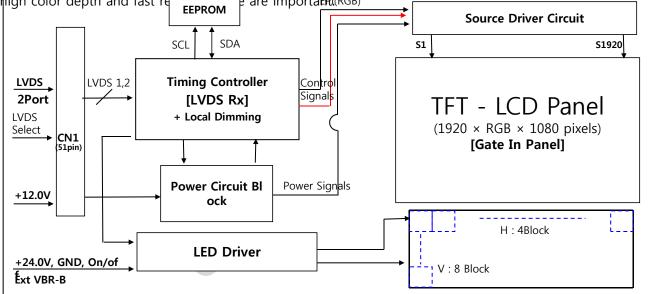
The BL-VW-DISPLAY-55-35mm is a Color Active Matrix Liquid Crystal Display with an integral Light Emitting Diode (LED) Local Block backlight system. The matrix employs a-Si Thin Film Transistor as the active element. It is a transmissive display type which is operating in the normally black mode. It has a 54.64 inch diagona lly measured active display area with WUXGA resolution (1080 vertical by 1920 horizontal pixel array). Each pixel is divided into Red, Green and Blue sub-pixels or dots which are arrayed in vertical stripes. Gray scale or the luminance of the sub-pixel color is determined with a 10-bit gray scale signal for each do

t

Therefore, it can present a palette of more than 1.06Bilion colors.

It has been designed to apply the 10-bit 2-port LVDS interface.

It is intended to support Public Display where high brightness, super wide viewing angle, high color gamut, high color depth and fast references are importalized (RGB)



General Features

Active Screen Size	54.64 inches(1387.80mm) diagonal
Outline Dimension	1213.4(H) x 684.2(V) x 39.2(B) (Typ.) 54.7mm(4 Mount) [mm]
Pixel Pitch	0.630(H) X 0.630(V)
Pixel Format	1920 horiz. by 1080 vert. Pixels, RGB stripe arrangement
Color Depth	10Bit (D), 1.07 Billion colors
Luminance, White	500 cd/m ² (Center 1point ,Typ.)
Viewing Angle (CR>10)	Viewing angle free (R/L 178 (Min.), U/D 178 (Min.))
Power Consumption	Total 162.7W (Typ.) [Logic=7.5W, Backlight=155.2W(@EXTVBR-B = 100%)
Weight	16,000 g (Typ.), 16,800 g (Max.)
Display Mode	Transmissive mode, Normally black
Surface Treatment	Hard coating(3H), Anti-glare treatment of the front polarizer (Haze 10%)
Possible Display Type	Landscape and Portrait Enabled

2. Absolute Maximum Ratings

The following items are maximum values which, if exceeded, may cause faulty operation or damage to t h

LCD module.

Table 1. ABSOLUTE MAXIMUM RATINGS

Paramo	Parameter		Va	lue	Unit	Note
Faranieter		Symbol –	Min	Max	Offic	
Dower Input Voltage	LCD Circuit	Vlcd	-0.3	+14.0	Vdc	
Power Input Voltage	Driver	VBL	-0.3	+ 27.0	Vdc	
	ON/OFF	VON/OFF	-0.3	+3.9	Vdc	1
Driver Control Voltag e	Brightness	EXTVBR-B	-0.3	+3.9	Vdc	Ţ
с 	Status	Status	-0.3	+3.9	Vdc	
T-Con Option Selectior	n Voltage	Vlogic	-0.3	+4.0	Vdc	
Operating Temperature	2	Тор	0	+50	°C	2.2
Storage Temperature		Тѕт	-20	+60	°C	2,3
Panel Front Temperature		Tsur	-	+68	°C	4
Operating Ambient Humidity		Нор	10	90	%RH	2.2
Storage Humidity		Нѕт	5	90	%RH	2,3

Note.

1. Ambient temperature condition (Ta = $25 \pm 2 \text{ °C}$)

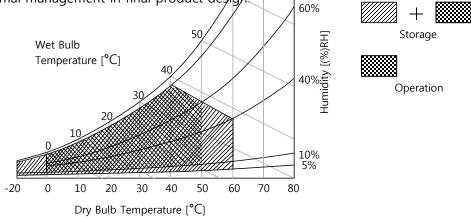
2. Temperature and relative humidity range are shown in the figure below.

Wet bulb temperature should be Max 39°C, and no condensation of water.

3. Gravity mura can be guaranteed below 40°C condition.

- 4. The maximum operating temperatures is based on the test condition that the surface temperature of display area is less than or equal to 68°C with LCD module alone in a temperature controlled chambe r.
- Thermal management should be considered in final product design to prevent the surface temperature of

display area from being over 68°C. The range of operating temperature may be degraded in case of improper thermal management in final product design



3. Electrical Specifications

3-1. Electrical Characteristics

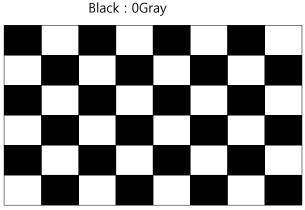
It requires two power inputs. One is employed to power for the LCD circuit. The other Is used for the LE D backlight and LED Driver circuit.

Table 2. ELECTRICAL CHARACTERISTICS

Parameter		Symbol	Value			Unit	Note
Palai	Parameter		Min	Тур	Max	Onit	Note
Circuit :							
Power Input Voltag	e	Vlcd	10.8	12.0	13.2	VDC	
	Power Input Current		-	625	812	mA	1
Power Input Curren			-	825	1072	mA	2
T-CON Option	Input High Voltage	V _{IH}	2.7	-	3.6	Vdc	
Selection Voltage	Input Low Voltage	V_{IL}	0	-	0.7	Vdc	
Power Consumption		Plcd	-	7.5	9.7	Watt	1
Rush current		Irush	-	-	6	А	3

Notes: 1. The specified current and power consumption are under the V_{LCD} =12.0V, 25 ± 2°C, f_v=60Hz condition whereas mosaic pattern(8 x 6) is displayed and f_v is the frame frequency.

- 2. The current is specified at maximum current pattern.
- 3. The duration of rush current is about 2ms and rising time of power input is 0.5ms (min.).



White : 1023Gray

Mosaic Pattern(8 x 6)



Normal temperature [25 $\pm 2^{\circ}$ C]

Table 3. ELECTRICAL CHARACTERISTICS (Continue)

Der	Parameter				Values		l lucit	Netes
Par				Min	Тур	Max	Unit	Notes
LED Driver :								
Power Supply Inpu	ut Voltage		VBL	21.6	24.0	26.4	Vdc	1
Power Supply Inpu	ut Current		IBL	-	6.67	7.72	A	Ext VBR-B = 100%
Power Supply Input Current (In-Rush)		In-rush	-	-	9.1	A	VBL = 21.6V Ext VBR-B = 100% 4	
Power Consumption	Power Consumption		PBL	-	155.2	166.7	W	1 Ext Vbr-в = 100%
	On/Off	On	V on	2.5	-	3.6	Vdc	
	OnyOn	Off	V off	-0.3	0.0	0.7	Vdc	
Input Voltage for C	Brightness Adjust		ExtVbr-b	1	-	100	%	On Duty, 6
ontrol System Sign als	PWM Frequency for N TSC & PAL		PAL		100		Hz	3
ais			NTSC		120		Hz	3
		Pulse Duty Level		2.5	-	3.6	Vdc	HIGH : on duty LOW : off duty
	(PWM)		Low Level	0.0	-	0.7	Vdc	LOW : off dutý
Life Time				50,000	60,000		Hrs	7

Notes :

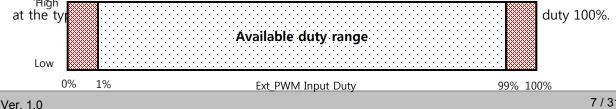
1. Electrical characteristics are determined after the unit has been 'ON' and stable for approximately 60 minutes at 25±2°C. The specified current and power consumption are under the typical supply Input volta

24Vand VBR (ExtVBR-B: 100%), it is total power consumption.

2. The life time (MTTF) is determined as the time which luminance of the LED is 50% compared to that of init value at the typical LED current (ExtVBR-B :100%) on condition of continuous operating in LCM state at

25±2°C.

- 3. LGD recommend that the PWM freq. is synchronized with Two times harmonic of V_sync signal of system. Though PWM frequency is over 120Hz (max 252Hz), function of LED Driver is not affected.
- 4. The duration of rush current is about 200ms. This duration is applied to LED on time.
- 5. Even though inrush current is over the specified value, there is no problem if I²T spec of fuse is satisfied.
- 6. Ext_PWM Signal have to input available duty range. Between 99% and 100% ExtVBR-B duty have to be avoided. (99% < ExtVBR-B < 100%) But ExtVBR-B 0% and 100% is possible.
- 7. The life time is determined as the time at which brightness of the LED is 50% compared to that of initial va High



3-2. Interface Connections

This LCD module employs two kinds of interface connection, a 51-pin connector is used for the module electr onics and Master 14-pin connectors are used for the integral backlight system.

3-2-1. LCD Module

- LCD Connector(CN1): FI-RE51S-HF (manufactured by JAE) or compatible
- Mating Connector : FI-RE51HL or compatible

Table 4. MODULE CONNECTOR(CN1) PIN CONFIGURATION

No	Symbol	Description	No	Symbol	Description
1	NC	No Connection	27	Bit Select	'H' = 10bit(D) only
2	NC	No Connection	28	R2AN	SECOND LVDS Receiver Signal (A-)
3	NC	No Connection	29	R2AP	SECOND LVDS Receiver Signal (A+)
4	NC	No Connection (Reserved for LGD)	30	R2BN	SECOND LVDS Receiver Signal (B-)
5	NC	No Connection (Reserved for LGD)	31	R2BP	SECOND LVDS Receiver Signal (B+)
6	NC	No Connection (Reserved for LGD)	32	R2CN	SECOND LVDS Receiver Signal (C-)
7	LVDS Select	'H' =JEIDA , 'L' = VESA	33	R2CP	SECOND LVDS Receiver Signal (C+)
8	NC	No Connection	34	GND	Ground
9	NC	No Connection	35	R2CLKN	SECOND LVDS Receiver Clock Signal(-)
10	Local Dimming	'H' =Enable only	36	R2CLKP	SECOND LVDS Receiver Clock Signal(+)
11	GND	Ground	37	GND	Ground
12	R1AN	FIRST LVDS Receiver Signal (A-)	38	R2DN	SECOND LVDS Receiver Signal (D-)
13	R1AP	FIRST LVDS Receiver Signal (A+)	39	R2DP	SECOND LVDS Receiver Signal (D+)
14	R1BN	FIRST LVDS Receiver Signal (B-)	40	R2EN	SECOND LVDS Receiver Signal (E-)
15	R1BP	FIRST LVDS Receiver Signal (B+)	41	R2EP	SECOND LVDS Receiver Signal (E+)
16	R1CN	FIRST LVDS Receiver Signal (C-)	42	NC	No Connection
17	R1CP	FIRST LVDS Receiver Signal (C+)	43	NC	No Connection
18	GND	Ground	44	GND	Ground
19	R1CLKN	FIRST LVDS Receiver Clock Signal(-)	45	GND	Ground
20	R1CLKP	FIRST LVDS Receiver Clock Signal(+)	46	GND	Ground
21	GND	Ground	47	NC	No connection
22	R1DN	FIRST LVDS Receiver Signal (D-)	48	VLCD	Power Supply +12.0V
23	R1DP	FIRST LVDS Receiver Signal (D+)	49	VLCD	Power Supply +12.0V
24	R1EN	FIRST LVDS Receiver Signal (E-)	50	VLCD	Power Supply +12.0V
25	R1EP	FIRST LVDS Receiver Signal (E+)	51	VLCD	Power Supply +12.0V
26	NC	No Connection	-	-	-

Notes:

1. All GND(ground) pins should be connected together to the LCD module's metal frame.

2. All VLCD (power input) pins should be connected together.

3. All Input levels of LVDS signals are based on the EIA 644 Standard.

4. #1~#6 & #8~#10 NC (No Connection): These pins are used only for LGD (Do not connect)

5. Specific pin No. #44 is used for "No signal detection" of system signal interface. It should be GND for NSB(No Signal Black) during the system interface signal is not. If this pin is "H", LCD Module displays AGP(Auto Generation Pattern).



3-2-2. Backlight Module

Master

- -LED Driver Connector
- : 20022WR H14B2(Yeonho) or Compatible
- Mating Connector

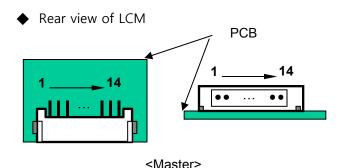
: 20022HS - 14B2(Yeonho) or Compatible

Table 5-1. LED DRIVER CONNECTOR PIN CONFIGULATION

Pin No	Symbol	Description	14PIN	Note
1	VBL	Power Supply +24.0V	VBL	
2	VBL	Power Supply +24.0V	VBL	
3	VBL	Power Supply +24.0V	VBL	
4	VBL	Power Supply +24.0V	VBL	
5	VBL	Power Supply +24.0V	VBL	
6	GND	Backlight Ground	GND	
7	GND	Backlight Ground	GND	
8	GND	Backlight Ground	GND	1
9	GND	Backlight Ground	GND	
10	GND	Backlight Ground	GND	
11	Status	Status	Status	2
12	VON/OFF	Backlight ON/OFF control	VON/OFF	4
13	EXTVBR-B	External PWM	EXTVBR-B	3
14	GND	Backlight Ground	GND	

Notes :1. GND should be connected to the LCD module's metal frame.

- 2. Normal : Low (under 0.7V) / Abnormal :Open
- 3. High : on duty / Low : off duty, Pin#13 can be opened. (if Pin #13 is open , EXTVBR-B is 100%)
- 4. Each impedance of pin #12 and #13 is over 50 $[\mbox{K}\Omega]$.



◆ Status



3-2-3. Local Dimming Interface

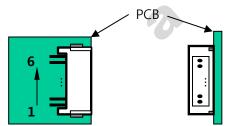
-- Local Dimming Interface Connector : 10003HR-H06L(YEONHO Elec.) or Equivalent

Table 5-2. LOCAL DIMMING INTERFACE CONNECTOR PIN CONFIGULATION

Pin No	Symbol	Description	Note
1	DCLK	Local Dimming Serial Clock	
2	DIN	Local Dimming Serial Data	
3	GND	Backlight Ground	1
4	VSYNC	Vertical Sync signal	
5	N.C	No connection	
6	N.C	No connection	

Notes :1. GND should be connected to the LCD module's metal frame.

Rear view of LCM





3-3. Signal Timing Specifications

Table 6 shows the signal timing required at the input of the LVDS transmitter. All of the interface signal t imings should be satisfied with the following specification for normal operation.

ITE	М	Symbol	Min	Тур	Max	Unit	notes
	Display Period	tHV	960	960	960	tCLK	1920 / 2
Horizontal	Blank	tнв	100	140	240	tCLK	1
	Total	thp	1060	1100	1200	tCLK	
	Display Period	tvv	1080	1080	1080	Lines	
Vertical	Blank	t∨B	20	45	300	Lines	1
	Total	tvp	1100	1125	1380	Lines	

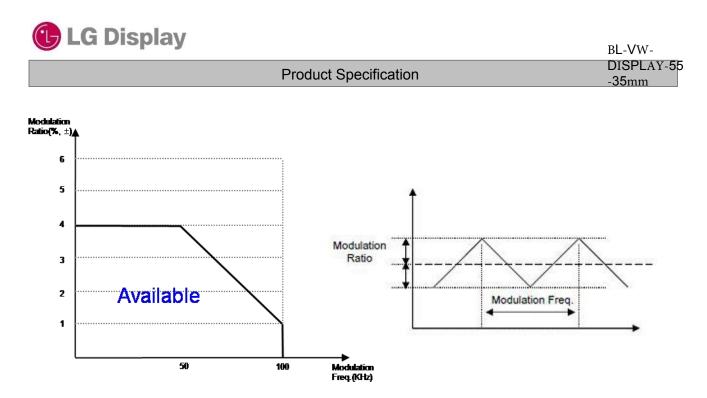
Table 6. TIMING TABLE for NTSC & PAL (DE Only Mode)

ITE	М	Symbol	Min	Тур	Max	Unit	notes
	DCLK	fськ	60.00	74.25	78.00	MHz	
Frequency	Horizontal	fн	57.3	67.5	70	KHz	2
	Vertical	fv	47	60	63	Hz	2

- notes: 1. The input of HSYNC & VSYNC signal does not have an effect on normal operation (DE Only Mode). If you use spread spectrum of EMI, add some additional clock to minimum value for clock margin.
 - 2. The performance of the electro-optical characteristics may be influenced by variance of the vertic
- al

refresh rate and the horizontal frequency

- Spread Spectrum Rate (SSR) for 50KHz ~ 100kHz Modulation Frequency(FMOD) is calculated by (7 – 0.06*Fmod), where Modulation Frequency (FMOD) unit is KHz. LVDS Receiver Spread spectrum Clock is defined as below figure
- % Timing should be set based on clock frequency.



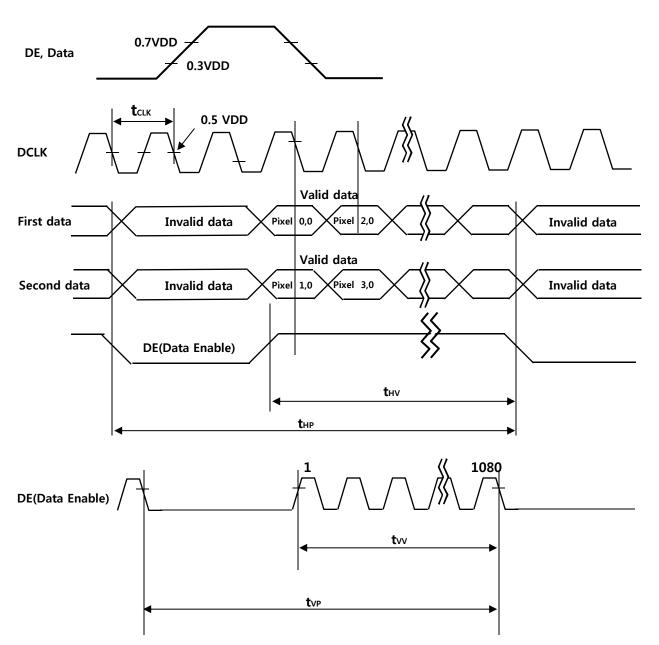
* Please pay attention to the followings when you set Spread Spectrum Rate(SSR) and Modulation Frequency(FM OD)

- 1. Please set proper Spread Spectrum Rate(SSR) and Modulation Frequency (FMOD) of TV system LVDS output.
- 2. Please check FOS after you set Spread Spectrum Rate(SSR) and Modulation Frequency(FMOD) to avoid abnormal display. Especially, harmonic noise can appear when you use Spread Spectrum under FMOD 30 KHz.



3-4. LVDS Signal Specification

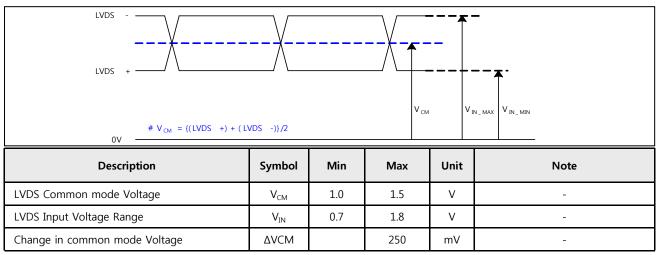
3-4-1. LVDS Input Signal Timing Diagram



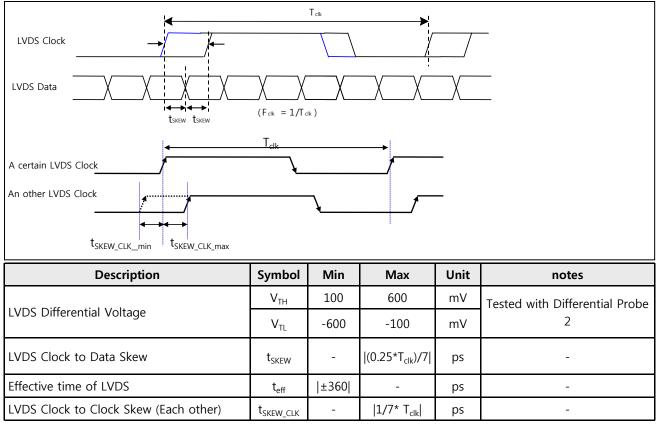


3-4-2. LVDS Input Signal Characteristics

1) DC Specification

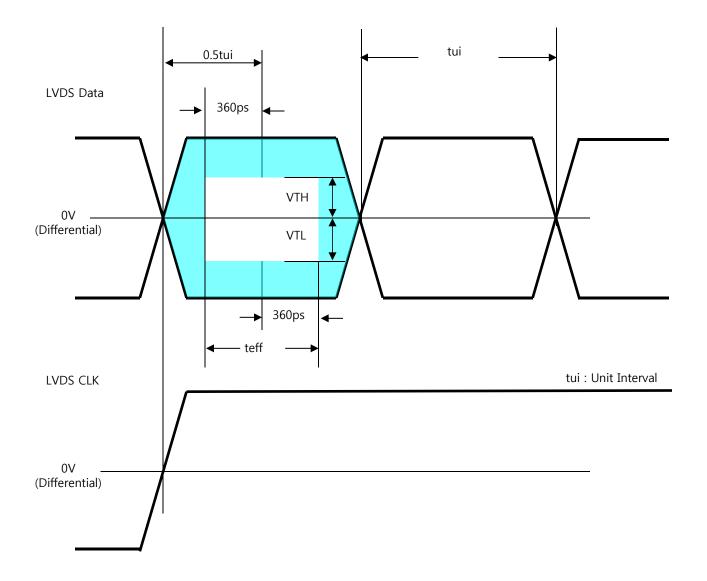


2) AC Specification



notes 1. All Input levels of LVDS signals are based on the EIA 644 Standard. 2. LVDS Differential Voltage is defined within **t**_{eff}





* This accumulated waveform is tested with differential probe

3-5. Color Data Reference

The brightness of each primary color(red,green,blue) is based on the 10-bit gray scale data input for the color. The higher binary input, the brighter the color. Table 8 provides a reference for color versus data input.

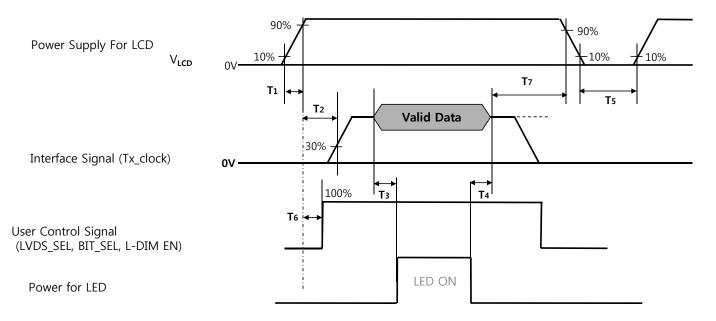
Table 7. COLOR DATA REFERENCE

														Inp	out	Со	lor	Dat	ta												
C	olor	LS				RI							S B				EEN			LS		LS					UE				
	I			8 R7	R6	R 5	R4	R3	R2	R1	R0	G9) G8	G7	G6	G5								Β7	B6	B 5	B4	B 3	B2	B1	B0
	Black	0 	0	0	0	0		0	0	0	0	0	0	0	0	0	0	0	0	0	0	0 	0	0	0	0		0	0	0	0
	Red (1023)	1 	.1	.1	1	.1 	.1	1	.1 	. 1 	1	0		0	.0	.0	. 0	0	0	.0	0	0	.0	.0	. 0			0	.0	.0	0
	Green(1023)	0	0	0	0	0	0	0	0	0	0	1	1	1	.1 	.1 	1	1	1	.1 	.1	0	0	0	0	0		0	0	0	0
Basic	Blue (1023)	0	0	0	0	0	0	0	0	0	0	0	0	0	0	0	0	0	0	0	0	1	1	1	1	1	1	1	1	1	1
Color	Cyan	0	0	0	0	0	0	0	0	0	0	1	1	1	1	1	1	1	1	1	1	1	1	1	1	1	1	1	1	1	1
	Magenta	1	1	1	1	1	1	1	1	1	1	0	0	0	0	0	0	0	0	0	0	1	1	1	1	1	1	1	1	1	1
	Yellow	1	1	1	1	1	1	1	1	1	1	1	1	1	1	1	1	1	1	1	1	0	0	0	0	0	0	0	0	0	0
	White	1	1	1	1	1	1	1	1	1	1	1	1	1	1		1	1	1	1	1		1	1	1	1	1	1	1	1	1
	RED (0000)	0	0	0	0	0	0	0	0	0	0	0	0	0	0	0	0	0	0	0	0	0	0	0	0	0	0	0	0	0	0
	RED (0001)	0	0	0	0	0	0	0	0	0	1	0	0	0	0	0	0	0	0	0	0	 0	0	0	0	0	0	0	0	0	0
RED		••••				•••	••••		•••				• • • •	•••	•••	•••			•••	•••	•••		•••	•••	•••	••••		•••		•••	
	RED (1022)	1	1	1	1	1	1	1	1	1	0	0	0	0	0	0	0	0	0	0	0	 0	0	0	0	0	0	0	0	0	0
	RED (1023)	1	1	1	1	1	1	1	1	1	1	 0	0	0	 0	 0	0	0	0	0	0	 0	 0	0	0	0	 0	0	0	0	 0
	GREEN(0000)	0	0	0	0	0	0	0	0	0	0	0	0	0	0	0	0	0	0	0	0	0	0	0	0	0	0	0	0	0	0
	GREEN(0001)	 0	0	0	0	0	0	0	0	0	0	0	0	0	0	0	0	0	0	0	1	 0	 0	0	0	0	0	0	0	0	
GREEN			• • •	••••		•••	• • • • •		•••		••	 .	• • • •	•••	•••	•••	••••	•••	•••	•••	••••		•••	•••	•••	••••		• • •	•••	•••	· · · ·
	GREEN(1022)	 0	0		0	 0	 0	0	 0	 0	0	 1	1	1	 1	 1	1	1	1	1	 0	 0	 0	 0	 0		 0	0	 0	 0	 0
	GREEN(1023)	 0	0		0	 0	0	0	 0	 0	0	 1	 1	1	 1	 1	 1	1	1	1	1	 0	 0	 0	 0		 0	0	 0	 0	 0
	BLUE (0000)	0	0	0	0	0	0	0	0	0	0	0	0	0	0	0	0	0	0	0	0	0	0	0	0	0	0	0	0	0	0
	BLUE (0001)	 0	0	0	0	 0		0	 0	 0		 0		0	 0	 0		0	0		 0		 0			0	0			 0	 1
BLUE													••••				•••								•••			•••		•••	
DLUL	BLUE (1022)	 0	0		0			0				0		0				0	0		 0	 1				1	 1				 0
	BLUE (1023)	· · ·					••••		• • •		•••	•••	••••		•••		••••			•••			• • •	•••		•••		• • •	•••		•••
	BLUE (1023)	U	U	U	U	U	U	U	U	U	U	U	U	U	U	U	U	U	U	U	U	1	1	-	-	1	<u> </u>	-	-	-	1



3-6. Power Sequence

3-6-1. LCD Driving circuit



Product Specification

Table 8. POWER SEQUENCE

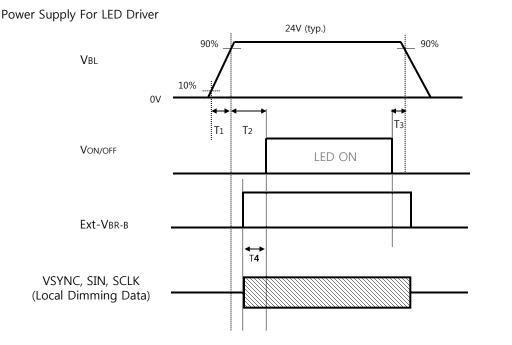
Demonstern		Value		11:4	
Parameter	Min	Тур	Max	Unit	notes
T1	0.5	-	20	ms	1
T2	0	-	400	ms	2
Т3	400	-	-	ms	3
T4	100	-	-	ms	3
T5	1.0	-	-	S	4
T6	0	-	T2	ms	5
T7	0	-	-	ms	7

notes : 1. Even though T1 is over the specified value, there is no problem if I2T spec of fuse is satisfied.

- 2. If T2 is satisfied with specification after removing LVDS Cable, there is no problem.
- 3. The T3 / T4 is recommended value, the case when failed to meet a minimum specification, abnormal display would be shown. There is no reliability problem.
- 4. T5 should be measured after the Module has been fully discharged between power off and on period.
- 5. If the on time of signals (Interface signal and user control signals) precedes the on time of Power (V_{LCD}), it will be happened abnormal display. When T6 is NC status, T6 doesn't need to be measured.
- 7. It is recommendation specification that T8 has to be 100ms as a minimum value.
- X Please avoid floating state of interface signal at invalid period.
- % When the power supply for LCD (VLCD) is off, be sure to pull down the valid and invalid data to 0V.



3-6-2. Sequence for LED Driver



3-6-3. Dip condition for LED Driver

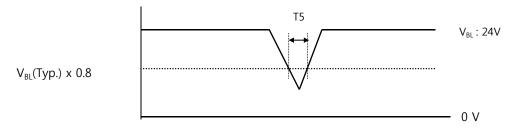


Table 9. Power Sequence for LED Driver

Deremeter		Values		Units	Domorka
Parameter	Min	Тур	Max	Units	Remarks
T1	20	-	-	ms	1
T2	500	-	-	ms	
Т3	10	-	-	ms	
T4	0	-	-	ms	
T5	-	-	10	ms	V _{BL} (Typ) x 0.8

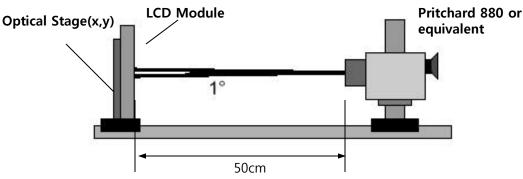
notes : 1. T1 describes rising time of 0V to 24V and this parameter does not applied at restarting time. Even though T1 is over the specified value, there is no problem if I²T spec of fuse is satisfied.



4. Optical Specification

Optical characteristics are determined after the unit has been 'ON' and stable in a dark environment at 25 $\pm 2^{\circ}$ C. The values specified are at an approximate distance 50cm from the LCD surface at a viewing angle o f Φ and θ equal to 0 °.

FIG. 1 shows additional information concerning the measurement equipment and method.





Ta= 25±2°C, V_{LCD}=12.0V, fv=60Hz, Dclk=74.25MHz, ExtVBR-B=100%

Table 10. OPTICAL CHARACTERISTICS

Value Parameter Symbol Unit Note Min Тур Max 1400 Contrast Ratio CR 1000 1 _ Surface Luminance, white 400 500 cd/m² 2 L_{WH} Luminance Variation 9P δ_{WHITE} 80 90 3 **Response Time** Gray-to-Gray G to G 8 12 4 ms Rx 0.647 RED Ry 0.334 Gx 0.313 Typ GREEN Тур -0.03 Gy 0.602 +0.03Color Coordinates [CIE1931] Вx 0.152 BLUE 0.051 Bу Wx 0.279 Тур Тур WHITE -0.02 +0.02 Wy 0.292 Color Temperature 10,000 Κ 72 % Color Gamut Viewing Angle (CR>10) x axis, right($\phi=0^\circ$) 89 θr _ x axis, left (ϕ =180°) θΙ 89 -degree 5 y axis, up (ϕ =90°) θu 89 _ y axis, down (ϕ =270°) θd 89 Gray Scale _ 6 _

Ver. 1.0



Notes :1. Contrast Ratio(CR) is defined mathematically as :

- CR(Contrast Ratio) = Maximum CRn (n=1, 2, 3, 4, 5)
 - Surface Luminance at position n with all white pixels
- CRn = Surface Luminance at position n with all black pixels
- n = the Position number(1, 2, 3, 4, 5). For more information, see FIG 2-1.
- 2. Surface luminance are determined after the unit has been 'ON' and 60min after lighting the backlight in a dark environment at 25±2°C. Surface luminance is the luminance value at cent
- er

1-point across the LCD surface 50cm from the surface with all pixels displaying white. For more information see the FIG. 2-1.

3. δ WHITE(9P) = Minimum(Lon1,Lon2, Lon3,..., Lon8, Lon9) / Maximum(Lon1,Lon2, Lon3,..., Lon8, Lon9)*100(%)

Where Lon1 to Lon9 are the luminance with all pixels displaying white at 9 locations . For more information, see the FIG. 2-2.

4. Response time is the time required for the display to transition from G(N) to G(M) (Rise Time,

and from G(M) to G(N) (Decay Time, Tr_D). For additional information see the FIG. 3. (N<M) \times G to G Spec is average of measured time (N, M = 0 (Black) ~ 1023(White), 128 gray step).

5. Viewing angle is the angle at which the contrast ratio is greater than 10. The angles are determined for the horizontal or x axis and the vertical or y axis with respect to the z axis whi

с

Tr_R)

is normal to the LCD module surface. For more information, see the FIG. 4.

Table 11. GRAY SCALE SPECIFICATION	
Gray Level	Luminance [%] (Typ.)
LO	0.08
L63	0.27
L127	1.04
L191	2.49
L255	4.68
L319	7.66
L383	11.5
L447	16.1
L511	21.6
L575	28.1

6. Gray scale specification

L639

L703

L767

L831

L895

L959

L1023

Table 1 Gamma Value is approximately 2.2. For more information, see the Table 12.

35.4

43.7

53.0

63.2

74.5

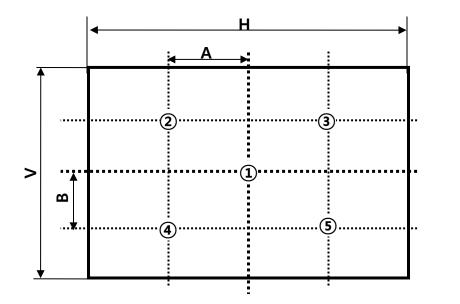
86.7

100

h



Measuring point for surface luminance & measuring point for luminance variation.



A : H / 4 mm B : V / 4 mm @ H,V : Active Area



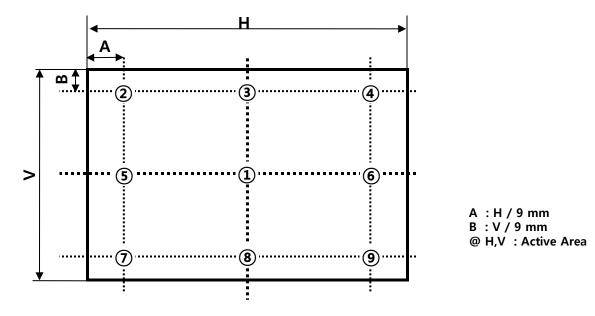
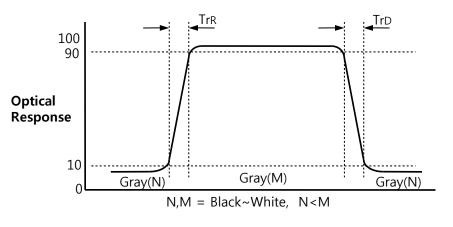


FIG. 2-2 9 Points for Luminance Measure

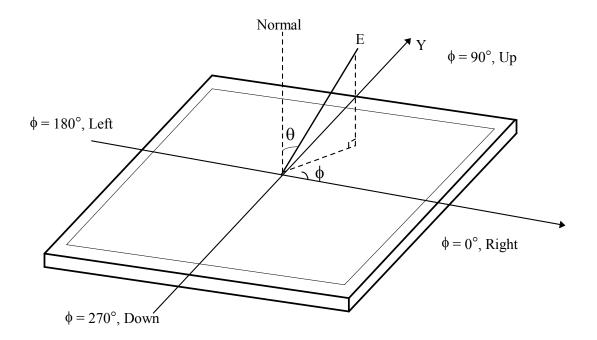


Response time is defined as the following figure and shall be measured by switching the input signal for "Gray(N)" and "Gray(M)".





Dimension of viewing angle range







5. Mechanical Characteristics

Table 12 provides general mechanical characteristics.

Table 12. MECHANICAL CHARACTERISTICS

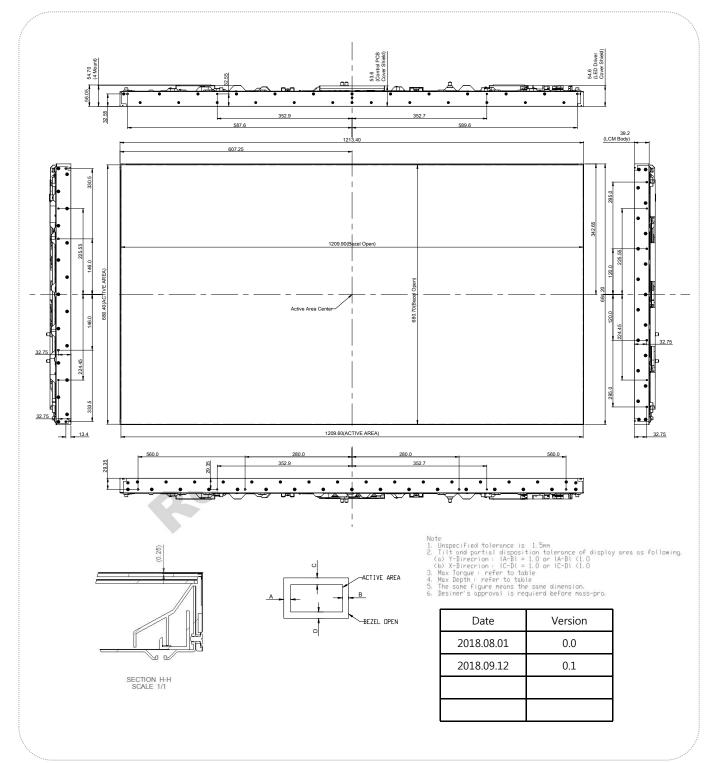
Item		Value				
	Horizontal	1213.4 mm				
Outline Dimension	Vertical	684.2 mm				
	Depth	39.2 mm				
Derel Area	Horizontal	1209.9 mm				
Bezel Area	Vertical	680.7 mm				
Active Dieplay Area	Horizontal	1209.6 mm				
Active Display Area	Vertical	680.4 mm				
Weight	16,000 g (T <u>y</u>	yp.) , 16,800 g (Max.)				

Note : Please refer to a mechanic drawing in terms of tolerance at the next page.



BL-VW-DISPLAY-55 -35mm

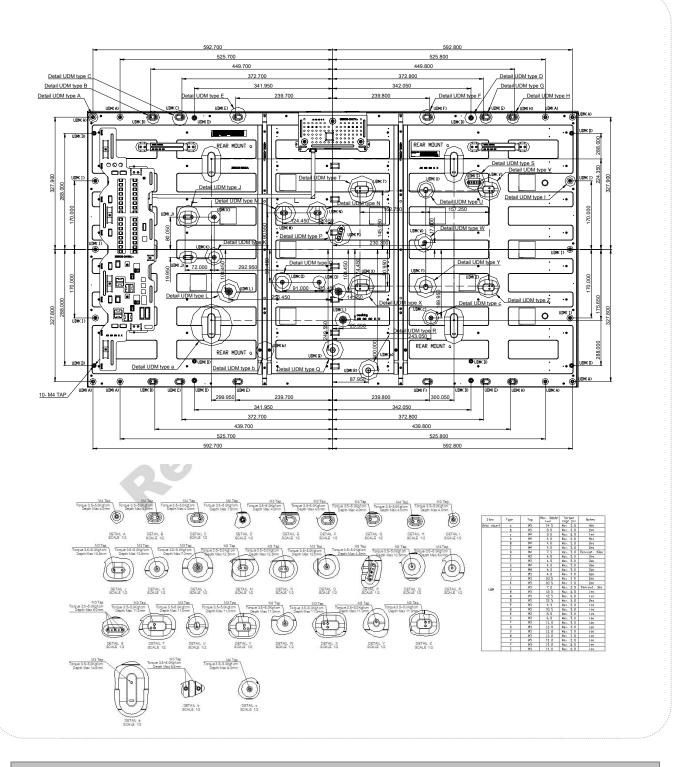
[FRONT VIEW]





BL-VW-DISPLAY-55 -35mm

<REAR VIEW>





6. Reliability

Table 13. ENVIRONMENT TEST CONDITION

No.	Test Item	Condition
1	High temperature storage test	Ta= 60°C, 240h
2	Low temperature storage test	Ta= -20°C 240h
3	High temperature operation test	Ta= 50°C 50%RH 500h
4	Low temperature operation test	Ta= 0°C 500h
5	Humidity condition Operation	Ta= 40 °C ,90%RH
6	Altitude operating storage / shipment	0 - 15,000 ft 0 - 40,000 ft

Note : 1. Before and after Reliability test, LCM should be operated with normal function.

2. These conditions are for LGD's internal test. Please refer to Absolute Maximum Ratings (Table1) f

or

guaranteed condition.



7. International Standards

7-1. Safety

- a) UL 60950-1, Underwriters Laboratories Inc. Information Technology Equipment - Safety - Part 1 : General Requirements.
 b) CAN/CSA-C22.2 No. 60950-1-07, Canadian Standards Association.
- Information Technology Equipment Safety Part 1 : General Requirements.
- c) EN 60950-1, European Committee for Electrotechnical Standardization (CENELEC). Information Technology Equipment - Safety - Part 1 : General Requirements.
- d) IEC 60950-1, The International Electrotechnical Commission (IEC). Information Technology Equipment - Safety - Part 1 : General Requirements

7-2. Environment

a) RoHS, Directive 2011/65/EU of the European Parliament and of the council of 8 June 2011



8. Packing

8-1. Information of LCM Label

a) Lot Mark



E: MONTH

F ~ M : SERIAL NO.

Note

1. YEAR

Year	2011	2012	2013	2014	2015	2016	2017	2018	2019	2020
Mark	А	В	С	D	E	F	G	Н	J	К

2. MONTH

Month	Jan	Feb	Mar	Apr	May	Jun	Jul	Aug	Sep	Oct	Nov	Dec
Mark	1	2	3	4	5	6	7	8	9	А	В	С

b) Location of Lot Mark

Serial No. is printed on the label. The label is attached to the backside of the LCD module. This is subject to change without prior notice.

8-2. Packing Form

- a) Package quantity in one box : 10 pcs
- b) Pallet Assy Size : 1440mm(W) X 1140mm(D) X 950mm(H)



9. Precautions

Please pay attention to the followings when you use this TFT LCD module.

9-1. Mounting Precautions

- (1) You must mount a module using specified mounting holes (Details refer to the drawings).
- (2) You should consider the mounting structure so that uneven force (ex. Twisted stress, Concentrated str ess)

is not applied to the module. And the case on which a module is mounted should have sufficient str ength

so that external force is not transmitted directly to the module.

- (3) Please attach the surface transparent protective plate to the surface in order to protect the polarizer. Transparent protective plate should have sufficient strength in order to the resist external force.
- (4) You should adopt radiation structure to satisfy the temperature specification.
- (5) Acetic acid type and chlorine type materials for the cover case are not desirable because the former generates corrosive gas of attacking the polarizer at high temperature and the latter causes circuit br

eak

by electro-chemical reaction.

(6) Do not touch, push or rub the exposed polarizers with glass, tweezers or anything harder than HB pencil lead. And please do not rub with dust clothes with chemical treatment.

Do not touch the surface of polarizer for bare hand or greasy cloth. (Some cosmetics are detrimental to the polarizer.)

(7) When the surface becomes dusty, please wipe gently with absorbent cotton or other soft materials lik e

chamois soaks with petroleum benzine. Normal-hexane is recommended for cleaning the adhesives used to attach front / rear polarizers. Do not use acetone, toluene and alcohol because they cause chemical damage to the polarizer.

- 982. Wine off failing prevater fores as soon as possible. Their long time contact with polarizer causes deformations and color fading.
 (4) Ble spike poise cause the mes instantion of circuits. It should be lower than following voltage : V=±200mV(Over and under shoot voltage)
 - (2) Response time depends on the temperature. (In lower temperature, it becomes longer.)
 - (3) Brightness depends on the temperature. (In lower temperature, it becomes lower.) And in lower temperature, response time(required time that brightness is stable after turned on) becomes longer
- (4) Be careful for condensation at sudden temperature change.Condensation makes damage to polarizer 0 r

electrical contacted parts. And after fading condensation, smear or spot will occur.

- (5) When fixed patterns are displayed for a long time, remnant image is likely to occur.
- (6) Module has high frequency circuits. Sufficient suppression to the electromagnetic interference shall b

done by system manufacturers. Grounding and shielding methods may be important to minimized t

h

- interference.
- (7) Please do not give any mechanical and/or acoustical impact to LCM. Otherwise, LCM can't be operate d

its full characteristics perfectly.

- (8) A screw which is fastened up the steels should be a machine screw.
 - (if not, it can causes conductive particles and deal LCM a fatal blow)
- (9) Please do not set LCD on its edge.
- (10) The conductive material and signal cables are kept away from LED driver inductor to prevent abnor mal

Ver display, sound noise and temperature rising.

P



9-3. Electrostatic Discharge Control

Since a module is composed of electronic circuits, it is not strong to electrostatic discharge. Make certain t t h а treatment persons are connected to ground through wrist band etc. And don't touch interface pin directly.

9-4. Precautions for Strong Light Exposure

Strong light exposure causes degradation of polarizer and color filter.

9-5. Storage

When storing modules as spares for a long time, the following precautions are necessary.

(1) Store them in a dark place. Do not expose the module to sunlight or fluorescent light. Keep the temperatu re

- between 5°C and 35°C at normal humidity.
- (2) The polarizer surface should not come in contact with any other object.
- It is recommended that they be stored in the container in which they were shipped.

9-6. Handling Precautions for Protection Film

- (1) The protection film is attached to the bezel with a small masking tape. When the protection film is peeled off, static electricity is generated between the film and polarizer. This should be peeled off slowly and carefully by people who are electrically grounded and with well ionblown equipment or in such a condition, etc.
- (2) When the module with protection film attached is stored for a long time, sometimes there remains a ver У

small amount of glue still on the bezel after the protection film is peeled off.

(3) You can remove the glue easily. When the glue remains on the bezel surface or its vestige is recognized, please wipe them off with absorbent cotton waste or other soft material like chamois soaked with normal

9-7heAppropriate Condition for Commercial Display

- Generally large-sized LCD modules are designed for consumer applications (TV).

Accordingly, a long-term display like in Commercial Display application, can cause uneven display including image sticking. To optimize module's lifetime and function, several operating usages are required.

- 1. Normal operating condition
 - Temperature: 0 ~ 40°C
 - Operating Ambient Humidity : 10 ~ 90 %
 - Display pattern: dynamic pattern (Real display)

Note) Long-term static display can cause image sticking.

- 2. Operating usages under abnormal condition
 - a. Ambient condition
 - Well-ventilated place is recommended to set up Commercial Display system.
 - b. Power and screen save
 - Periodical power-off or screen save is needed after long-term display.



- 3. Operating usages to protect against image sticking due to long-term static display
 - a. Suitable operating time: under 24 hours a day.
 - (* The moving picture can be allowed for 24 hours a day)
 - b. Static information display recommended to use with moving image.
 - Cycling display between 5 minutes' information(static) display and 10 seconds' moving image.
 - c. Background and character (image) color change
 - Use different colors for background and character, respectively.
 - Change colors themselves periodically.
- d. Avoid combination of background and character with large different luminance.
 - 1) Abnormal condition just means conditions except normal condition.
 - 2) Black image or moving image is strongly recommended as a screen save.
- 4. Lifetime in this spec. is guaranteed only when Commercial Display is used according to operating usages.
- 5. Module should be turned clockwise based on front view when used in portrait mode.



APPENDIX-I-1

■ Required signal assignment for Flat Link (Thine : THC63LVD103) Transmitter(Pin7="L or NC")

Host System 30 Bit	THC63LVI or Compa					Timing
RED0	33			•		Controller
RED1	34		FI	-RE51S-I	HF	
RED2	35					
RED3	36		21			
RED4	37	TA-	_31	12		RA1N
RED5	38	TA+	30	13	100Ω ≷	RA1P
RED6	59	17.11		15		
RED7	61		29			
RED8	4	TB-	28	14	100Ω ≷	RB1N
RED9	5	TB+	20	15	10022 <	- RB1P
GREEN0	40					
GREEN1	41	TC-	25	16		RC1N
GREEN2	42	TC+	24	17	100Ω Š	- RC1P
GREEN3	44	IC+				
GREEN4	45		23			
GREEN5	46	TCLK-		19	200	RCLK1N
GREEN6	62	TCLK+	22	20	100Ω Š	RCLK1P
GREEN7	63					
GREEN8	6	TD-	21	22		RD1N
GREEN9	8	TD+	20	23	<u>100Ω ξ</u>	RD1N RD1P
BLUE0	48	ID+		23	20012	- RDIP
BLUE1	49		19			
BLUE2	50	TE-		24	>	RE1N
BLUE3	52	TE+	18	25	100Ω ≷	RE1P
BLUE4	53					
BLUE5	54			7		
BLUE6	64					VESA / JEIDA
BLUE7	1					
BLUE8	9					
BLUE9	11			Í		
Hsync	55					
Vsync	57		ଦ୍ର		LCM Module	
Data Enable	58		GND			
	12					

Notes:

- 1. The LCD module uses a 100 $Ohm(\Omega)$ resistor between positive and negative lines of each receiver input.
- 2. Refer to LVDS transmitter data sheet for detail descriptions. (THC63LVD103 or Compatible)
- 3. '9' means MSB and '0' means LSB at R,G,B pixel data.



APPENDIX-I-2

■ Required signal assignment for Flat Link (Thine : THC63LVD103) Transmitter(Pin7="H")

Host System	THC63	BLVD103				
30 Bit	or Coi	mpatible				Timing
RED0	4					Controller
RED1	5		FI	-RE51S-H	HF	
RED2	59					
RED3	61		31			
RED4	33	TA-		12	100Ω ≷	RA1N
RED5	34	TA+	30	13	20002 \$	RA1P
RED6	35					
RED7	36	TB-	29	14		RB1N
RED8	37		28		100Ω Š	
RED9	38	TB+		15		RB1P
GREEN0	6		25			
GREEN1	8	TC-	25	16	>	RC1N
GREEN2	62	TC+	24	17	100Ω ≷	RC1P
GREEN3	63					
GREEN4	40	TCLK	23	10		DCLI/1N
GREEN5	41	TCLK-	22	19	100Ω ξ	RCLK1N
GREEN6	42	TCLK+		20	10012 ζ	RCLK1P
GREEN7	44		21			
GREEN8	45	TD-	21	22	>	RD1N
GREEN9	46	TD+	20	23	100Ω Š	RD1P
BLUE0	9					
BLUE1	11	те	19	24		
BLUE2	64	TE-	18	24	100Ω ≷	RE1N
BLUE3	1	TE+	10	25	10012 <	RE1P
BLUE4	48					
BLUE5	49		<u> </u>	7		VESA / JEIDA
BLUE6	50					
BLUE7	52					
BLUE8	53					
BLUE9	54					L
Hsync	55				LCM Module	
Vsync	57					
Data Enable	58					
CLOCK	12					

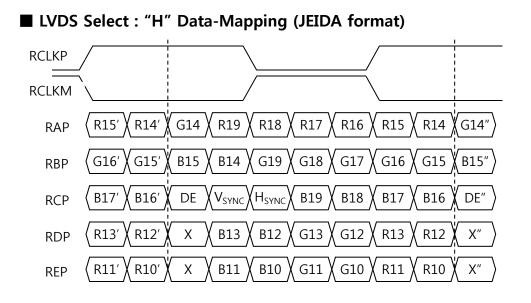
Notes:

- 1. The LCD module uses a 100 $Ohm(\Omega)$ resistor between positive and negative lines of each receiver input.
- 2. Refer to LVDS transmitter data sheet for detail descriptions. (THC63LVD103 or Compatible)
- 3. '9' means MSB and '0' means LSB at R,G,B pixel data.

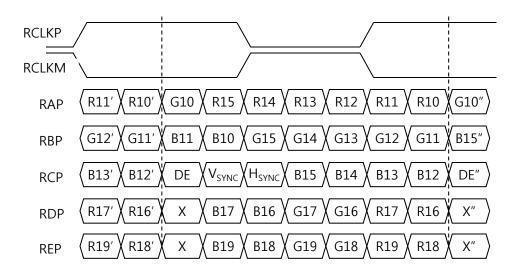


APPENDIX- II

LVDS Data-Mapping info. (10bit)



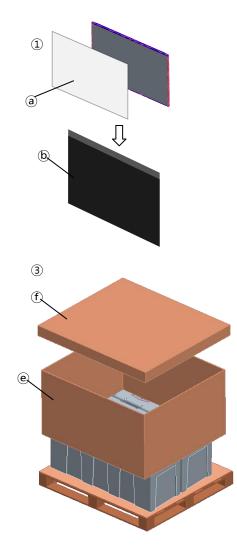
■ LVDS Select : "L" Data-Mapping (VESA format)

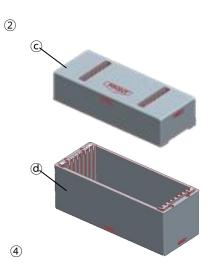




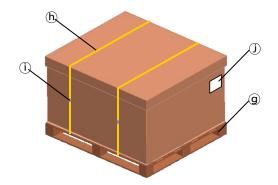
APPENDIX-III

Pallet Ass'y





Product Specification



No.	Description	Material
a	Protect Film	PE
b	BAG	AL
C	Packing Top	EPS
đ	Packing Bottom	EPS
e	Angle Packing	Double Wall
F	Angle Cover	Single Wall
g	Pallet	Plywood
h	Band	PP
í	Clip	Steel
J	Label	Paper

APPENDIX 4/8



APPENDIX-IV

LCM Label



Product Specification



APPENDIX- V

Pallet Label

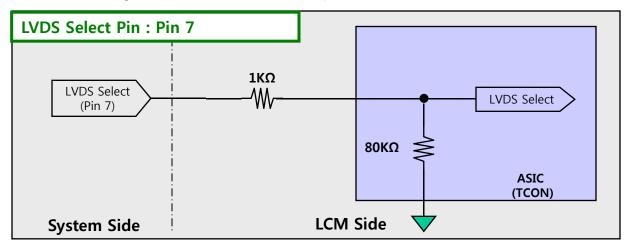




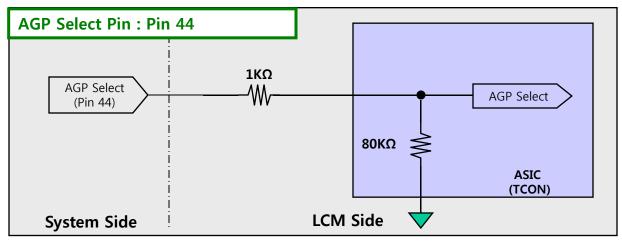
APPENDIX- VI

■ Option Pin Circuit Block Diagram

1) Circuit Block Diagram of LVDS Format Selection pin



2) Circuit Block Diagram of AGP Option Selection pin

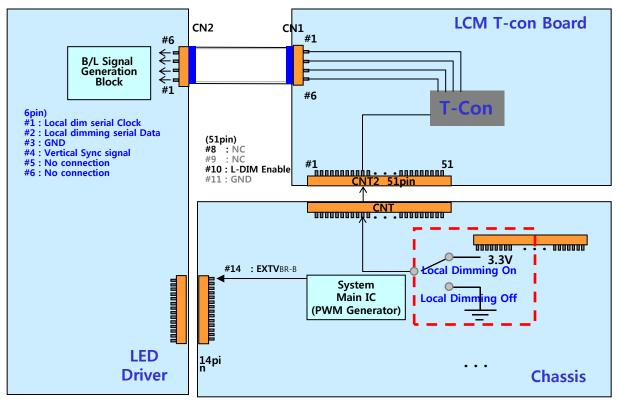




APPENDIX- VI

EXTVBR-B & Local Dimming Design Guide

- 1) When L-Dim Enable is "L", Vertical Sync Signal = System Dimming with 100Hz or 120Hz frequency.
- 2) Local Dimming signals are synchronized with V-Sync Freq. of System in T-Con Board.
- 3) EXTVBR-B Specification (VCC = 3.3V) @ Local Dimming
 - a) High Voltage Range : 2.5 V ~ 3.6 V
 - b) Low Voltage Range $: 0.0 \text{ V} \sim 0.7 \text{ V}$
- 4) LCD Connector(CN1): IS100-L06B-C20 (manufactured by UJU)
- 5) LCD Connector(CN2): 1003HR-H06L(BK) (manufactured by YEONHO)



<With Driver Model>

EXTV BR-B Frequency	100 Hz for PAL 120 Hz for NTSC
Rising Time	MAX 10.0 µs
Falling Time	MAX 10.0 µs

