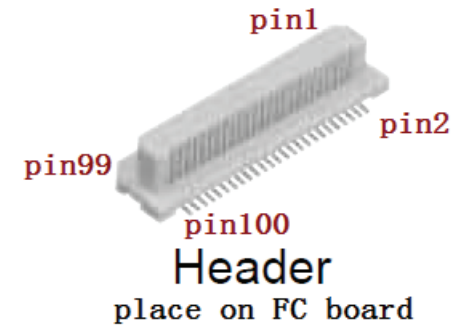
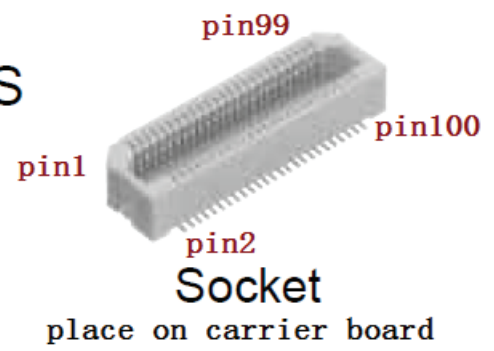


• P5KS



Header on FMU

99	P99	IO_CH8	Gnd	P100	100
97	P97	IO_CH7	(0.2A max) IO_VDD_3V3_OUT	P98	98
95	P95	IO_CH6	(0.2A max) VDD_3V3_SPEKTRUM	P96	96
93	P93	IO_CH5	SPEKTRUM_dsm_IN	P94	94
91	P91	IO_CH4	SAFETY_SWITCH_IN	P92	92
89	P89	IO_CH3	nSAFETY_LED_OUT	P90	90
87	P87	IO_CH2	Buzzer-	P88	88
85	P85	IO_CH1	SBUS_OUT	P86	86
83	P83	Gnd	SBUS_RC_IN	P84	84
81	P81	VDD_servo_IN	PPM_RC_IN	P82	82
79	P79	FMU_CH8	RSSI_IN	P80	80
77	P77	FMU_CH7	Gnd	P78	78
75	P75	FMU_CH6	USART6_TX_TELEM3	P76	76
73	P73	FMU_CH5	(0.3A max) VDD_5V_SBUS_RC	P74	74
71	P71	FMU_CH4	VDD_5V_PERIPH	P72	72
69	P69	FMU_CH3	(1.5A max) VDD_5V_PERIPH	P70	70
67	P67	FMU_CH2	VDD_5V_PERIPH	P68	68
65	P65	FMU_CH1	VDD_5V_HIPOWER	P66	66
63	P63	TIM5_SPARE_4	(1.5A max) VDD_5V_HIPOWER	P64	64
61	P61	FMU_CAP3	VDD_5V_HIPOWER	P62	62
59	P59	FMU_CAP2	Gnd	P60	60
57	P57	FMU_CAP1	USART6_RX_TELEM3	P58	58
55	P55	USART1_TX_GPS1	SPI5_SCK_EXTERNAL1	P56	56
53	P53	USART1_RX_GPS1	SPI5_MISO_EXTERNAL1	P54	54
51	P51	I2C1_SCL_GPS1	SPI5_MOSI_EXTERNAL1	P52	52
49	P49	I2C1_SDA_GPS1	SPI5_CS1_EXTERNAL1	P50	50
47	P47	Gnd	SPI5_CS2_EXTERNAL1	P48	48
45	P45	USART2_TX_TELEM1	SPI5_SYNC_EXTERNAL1	P46	46
43	P43	USART2_RX_TELEM1	nSPI5_RESET_EXTERNAL1	P44	44
41	P41	USART2_CTS_TELEM1	Gnd	P42	42
39	P39	USART2_RTS_TELEM1	ADC1_3V3_IN	P40	40
37	P37	USART3_TX_TELEM2	ADC1_6V6_IN	P38	38
35	P35	USART3_RX_TELEM2	BAT1_I_sense	P36	36
33	P33	USART3_CTS_TELEM2	BAT1_V_sense	P34	34
31	P31	USART3_RTS_TELEM2	BAT2_I_sense	P32	32
29	P29	UART4_TX_I2C2	BAT2_V_sense	P30	30
27	P27	UART4_RX_I2C2	Gnd	P28	28
25	P25	I2C2_SCL_UART4	VDD5V_Brick1	P26	26
23	P23	I2C2_SDA_UART4	VDD5V_Brick1	P24	24
21	P21	I2C4_SCL_EXTERNAL2	VDD5V_Brick1	P22	22
19	P19	I2C4_SDA_EXTERNAL2	VDD5V_Brick1	P20	20
17	P17	CAN1_H	(3A max) VDD5V_Brick1	P18	18
15	P15	CAN1_L	VDD5V_Brick1	P16	16
13	P13	CAN2_H	AXK6S00647YG	P14	14
11	P11	CAN2_L	Gnd	P12	12
9	P9	Gnd	VDD5V_Brick2	P10	10
7	P7	USB_D+	J6 VDD5V_Brick2	P8	8
5	P5	USB_D-	VDD5V_Brick2	P6	6
3	P3	VBUS (0.5A max)	(3A max) VDD5V_Brick2	P4	4
1	P1	Gnd	VDD5V_Brick2	P2	2

