



STEPS

Description

STEPS has three functional blocks:

- 1) Internal clock
 - a. Rate control and output
- 2) Burst generator
 - a. Pulses output which generates 8 pulses on every trigger
 - b. Gate output which is high during the burst event
 - c. EOB output which fires at the end of a burst event
- 3) Random Looping CV Generator
 - a. R.gate output outputs a random gate during a burst event
 - b. Random stepped output or a loop of 8 steps
 - c. Steps output with attenuverter. The Steps output is held low while there is no burst event.

You can either use this as a random stepped CV source or as a repeating 8 stepped CV source. You then can trigger the burst generator to play the 8 step loop or 8 random steps. All of this can use the internal or external clock signals. To get more complex patterns you can daisy chain STEPS modules together by tying together their clocks and sending EOB to TRIG. This way you can generate 16 step patterns (or longer).

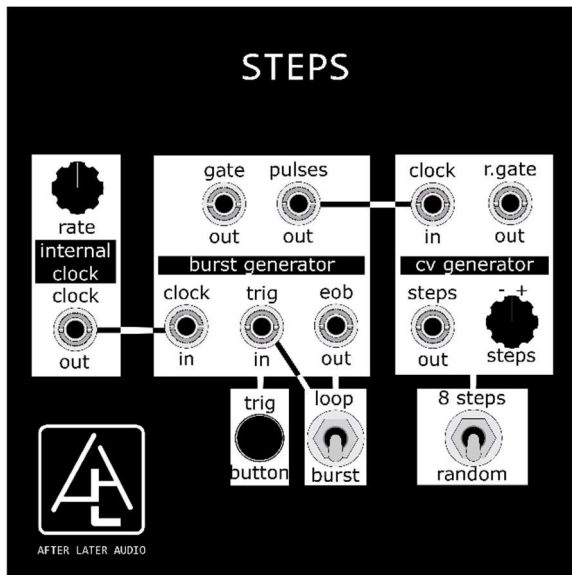
When first turning on STEPS you will need to run the module in RAND mode for at least 8 steps to fill the shift register with data.

A caveat about STEPS:

If a trigger is received off beat from the internal clock, the first step duration will be shortened.



Walkthrough



Technical Details

- Clock range: 1.5s/pulse to 100ms/pulse
- End of burst level: 7.5V
- Pulse output level: 7.5V
- Burst gate level: 6V
- Stepped CV range: -5V to 5V (8 graduations in steps)
- Random gate output level: 7.5V
- Power consumption:
 - 12V - 75mA
 - -12V - 65mA
- Size: 6hp



AFTER LATER AUDIO

