MDC_2SA1962RTU_LT

LTspice Model PNP ON 2SA1962RTU

Model Information

ModelMextram modelCall NameMDC_2SA1962RTU_LTPin Assign1:C 2:B 3:EFile ListModel Library
Model Report

MDC_2SA1962RTU_LT01.lib MDC_2SA1962RTU_LT.pdf (this file)

Verified Simulator Version Note

LTspice version XVII

References

The information which was used for modeling is as follow:

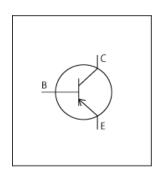
Rev. C 2SA1962RTU ON Semiconductor. IcVce[ib],hFEIc[Temp],Vce(sat)Ic[Temp],Vbe(sat)Ic[Temp],Ic Vbe[Temp]

Simulation Range

This table shows the range of evaluated simulation range that was not occurs any convergence problems in this area.

Item	Range			Unit
	Min.		Max.	
Collector-emitter voltage (DC)	0	to	-250	V
Base current (DC)	0	to	-2	А
Temperature	-50	to	150	deg C

1



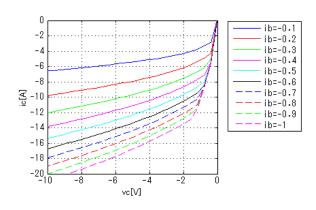




Simulation results are following. Explanatory notes — : simulated

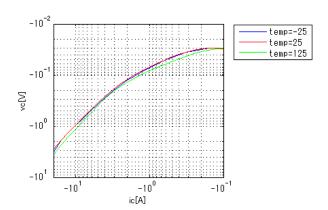
IcVce[ib]

Temp. = 25deg C



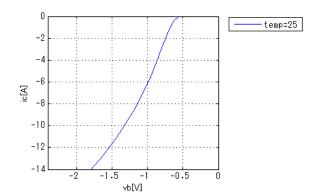
Vce(sat)lc[Temp]

IC/IB = 10



IcVbe[Temp]

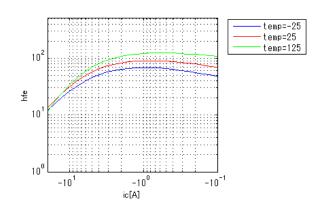
Vce = -5V



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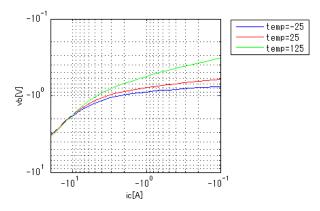
hFElc[Temp]

Vce = -5V



Vbe(sat)lc[Temp]

IC/IB = 10





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