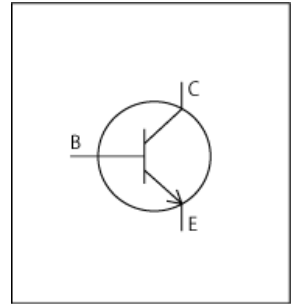


LTspice Model

NPN

ON

FJP2145



Model Information

Model Gummel-Poon model
Call Name MDC_FJP2145_LT
Pin Assign 1:C 2:B 3:E
File List Model Library MDC_FJP2145_LT01.lib
 Model Report MDC_FJP2145_LT.pdf (this file)

Verified Simulator Version LTspice version XVII
Note

References

The information which was used for modeling is as follow:

[Data Sheet]

- Date/Version Rev. 1.1
- Product name FJP2145
- Company name ON Semiconductor.
- Characteristics $I_{cVce}[I_b], h_{FE}I_c[Temp], V_{ce(sat)}I_c[Temp], V_{ce(sat)}I_b[I_c], C_{ob}$

Simulation Range

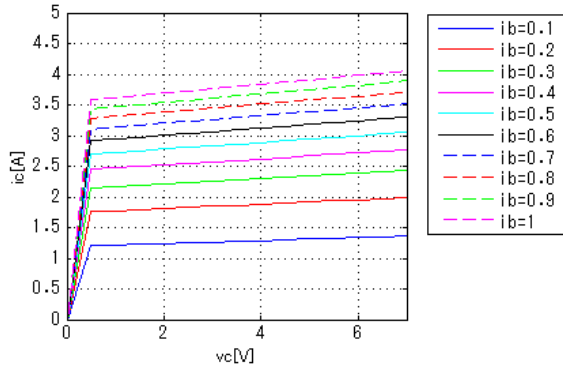
This table shows the range of evaluated simulation range that was not occurs any convergence problems in this area.

Item	Range			Unit
	Min.		Max.	
Collector-emitter voltage (DC)	0	to	800	V
Base current (DC)	0	to	2	A
Temperature	-55	to	150	deg C

Simulation results are following.
 Explanatory notes — : simulated

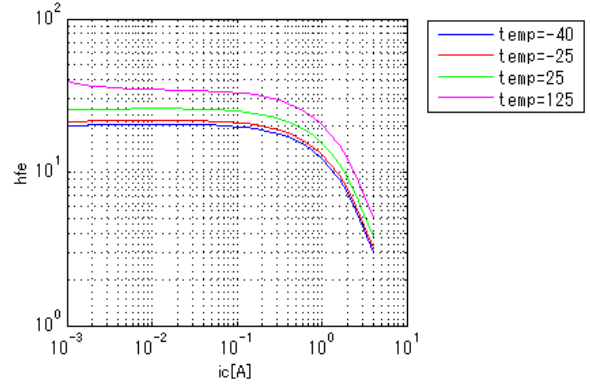
IcVce[ib]

Temp. = 25deg C



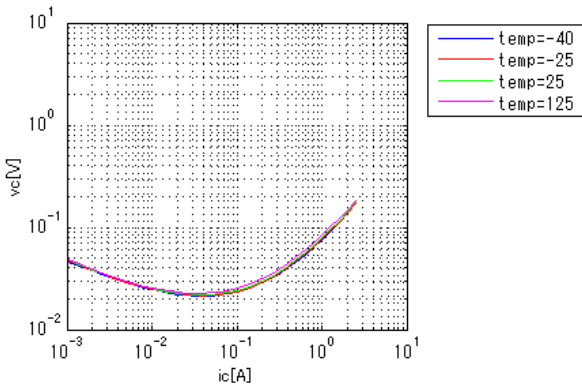
hFEIc[Temp]

Vce = 5V

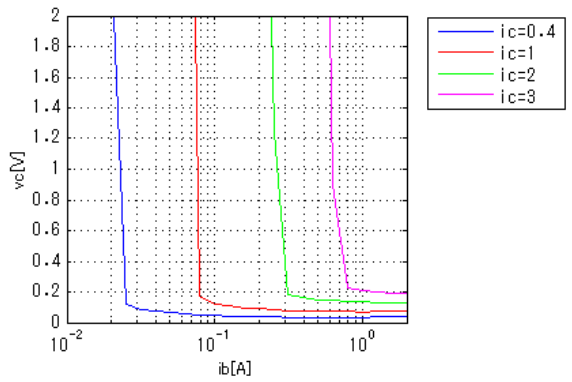


Vce(sat)Ic[Temp]

IC/IB = 3

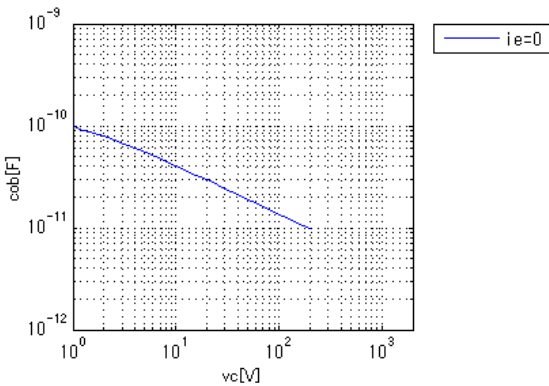


Vce(sat)Ib[Ic]



Cob

Freq. = 1MHz



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