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## PSpice Model <br> CAN Transceiver Infineon <br> MDC_TLE9251V_PS

Model Information<br>Model A macro model<br>Call Name MDC_TLE9251V_PS<br>Pin Assign 1:TxD 2:GND 3:VCC 4:RxD 5:VIO 6:CANL 7:CANH 8:STB<br>File List<br>Model Library MDC_TLE9251V_PS.lib<br>Model Report MDC_TLE9251V_PS.pdf(this file)<br>Verified Simulator Version<br>PSpice V17.2<br>Note

## References

The information which was used for modeling is as follow:
[Data Sheet]

| -Date/Version | 2019-09-19/Rev.1.11 |
| :--- | :--- |
| - Product name | TLE9251V |
| -Company name | Infineon Technologies AG |

[Characteristics listed]
-Characteristics
TxD Timechart
TxD STB VCC VIO Timechart
RxD Timechart

## Simulation Condition

This table shows the range of evaluated simulation range that was not occurs any convergence problems in this area.

| Item | Condition | Unit |
| :--- | :--- | :---: |
| Temperature | 25 | $\operatorname{deg} C$ |

Model Functions Table

| GAN FDFunctions | Implemented |
| :--- | :---: |
| CAN transmitter | $\bigcirc$ |
| CAN receiver | $\bigcirc$ |
| CAN FD 2MBit/s,5MBit/s | $\bigcirc$ |
| Bus Wake-up | $\times$ |

## TxD Timechart

## TestBench



Simulation results are following.
Explanatory notes - : simulated


## TxD Timechart(200ns@1bit)

## TestBench



Simulation results are following.
Explanatory notes - : simulated


## TxD STB VCC VIO Timechart

## TestBench



Simulation results are following.
Explanatory notes - : simulated


## RxD Timechart

## TestBench



Simulation results are following.
Explanatory notes - : simulated


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