

PSpice Model

Capacitor

Panasonic

MDC_EEHZK1E561UV_PS

Model Information

Model A macro model
Call Name MDC_EEHZK1E561UV_PS
Pin Assign 1:Port1 2:Port2
File List Model Library MDC_EEHZK1E561UV_PS.lib
 Model Report MDC_EEHZK1E561UV_PS.pdf(this file)
Verified Simulator Version PSpice V17.2
Note

References

The information which was used for modeling is as follow:

[Data Sheet]

- Date/Version 2019/12/13
- Product name EEHZK1E561UV
- Company name Panasonic Corporation

[Characteristics listed]

- Characteristics Capacitance, Impedance vs Frequency

Simulation Condition

This table shows the range of evaluated simulation range that was not occurs any convergence problems in this area.

Item	Condition	Unit
Temperature	20	deg C

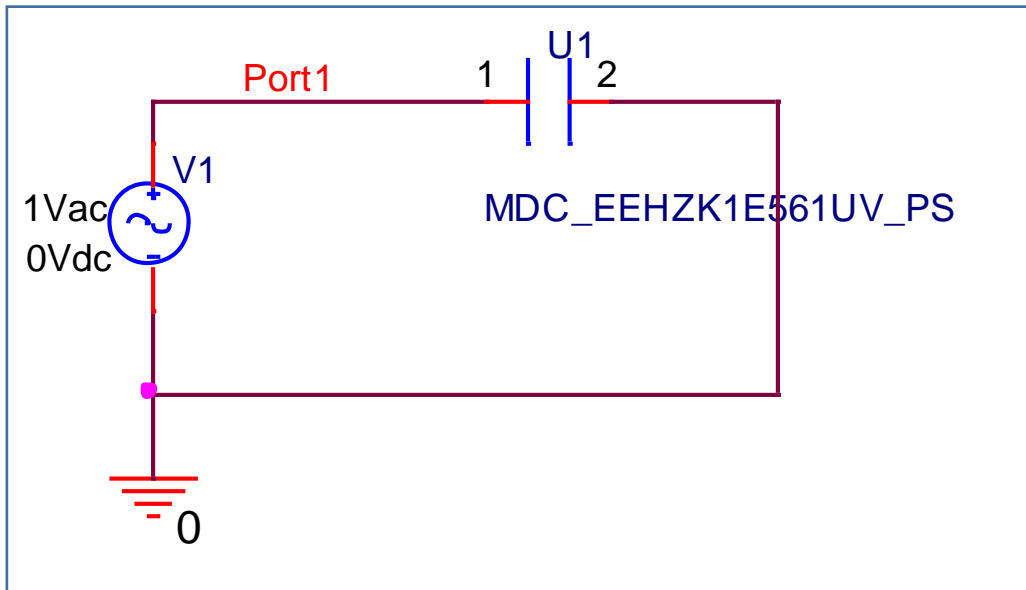
○ : Implemented
 × : Not Implemented
 — : Not applicable

Model Functions Table

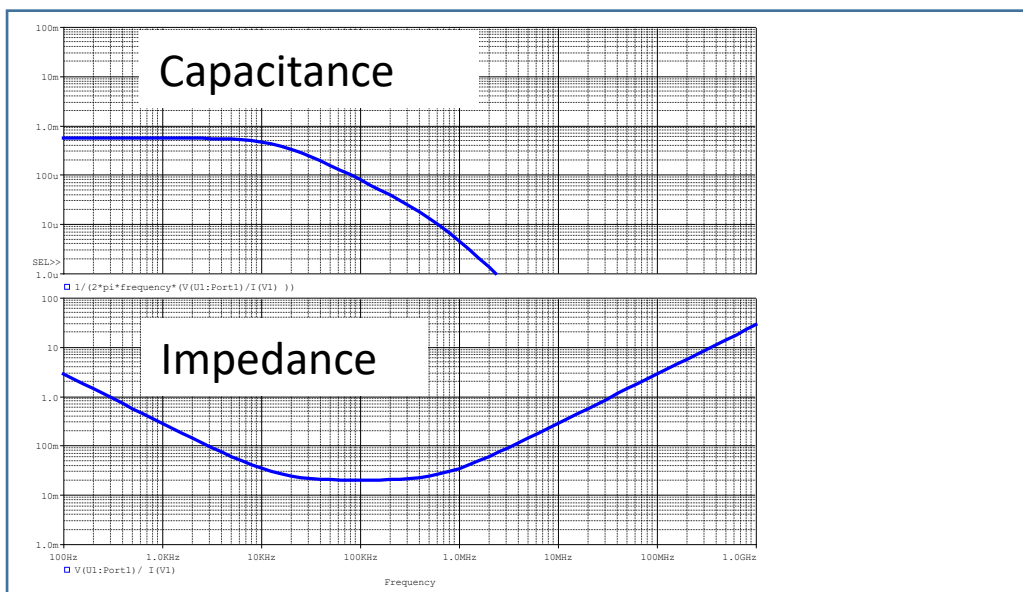
		RANK=1
Functions	RANK	Implemented
Impedance-Frequency	1	○
Capacitance-Frequency	1	○

Capacitance, Impedance vs Frequency

TestBench



Simulation results are following.
 Explanatory notes — : simulated



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