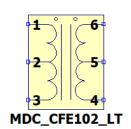


LTspice Model Pulse Transformer JPC MDC_CFE102_LT



Model Information

 Model
 A macro model

 Call Name
 MDC_CFE102_LT

 Pin Assign
 1:1 2:2 3:3 4:4 5:5 6:6

File List Model Library MDC_CFE102_LT.lib

Model Report MDC_CFE102_LT.pdf(this file)

Verified Simulator Version LTspice XVII

Note

References

The information which was used for modeling is as follow:

[Data Sheet]

Date/Version

● Product name CFE102● Company name JPC CO.,LTD

[Characteristics listed]

Characteristics

Inductance vs Frequency
Pulse Response

Simulation Condition

This table shows the range of evaluated simulation range that was not occurs any convergence problems in this area.

Item	Condition	Unit
Temperature	25	deg C

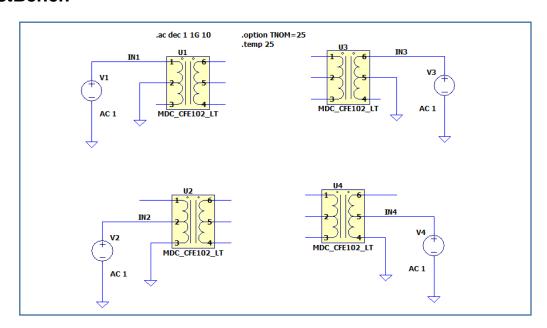


Model Functions Table

Functions	Implemented
Inductance	0
Turns Ratio	0
ET Product	×

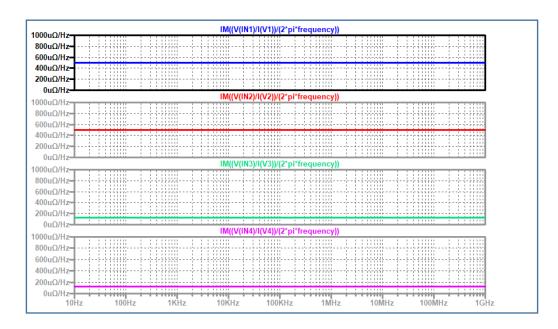


Inductance vs Frequency TestBench



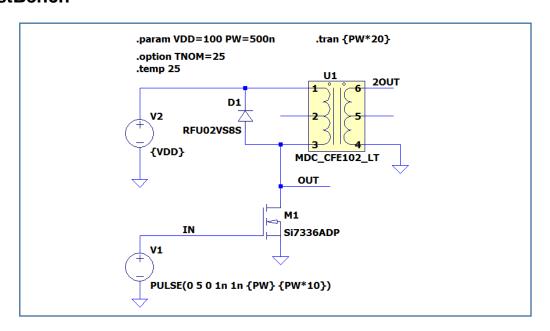
Simulation results are following.

Explanatory notes — : simulated



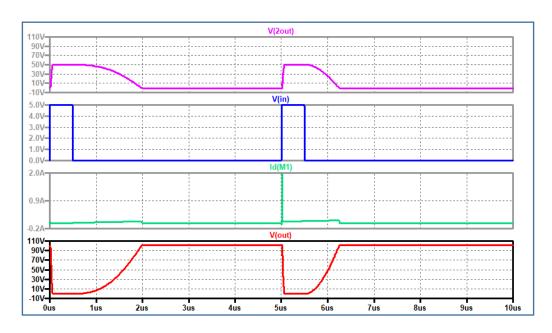


Pulse Response TestBench



Simulation results are following.

Explanatory notes — : simulated





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