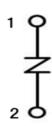


# LTspice Model Varistor Panasonic ERZV14D471



### **Model Information**

Model An original macro model Call Name MDC\_ERZV14D471\_LT

Pin Assign 1:1 2:2

File List Model Library MDC\_ERZV14D471\_LT02.lib

Model Report MDC\_ERZV14D471\_LT.pdf (this file)

**Verified Simulator Version** 

Note

LTspice version XVII

### References

The information which was used for modeling is as follow:

[Data Sheet]

Date/Version 2021/12/23Product name ERZV14D471

Company name
Panasonic Corporation

• Characteristics Vrlr[Temp],CjVr,SurgeWaveform

### Simulation Range

This table shows the range of evaluated simulation range that was not occurs any convergence problems in this area.

Item	Range			Unit
	Min.		Max.	
Temperature	-40	to	125	deg C



**Model Functions Table** 

## Varistor

O: Implemented

× : Not Implemented

—: Not applicable

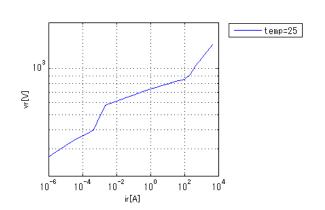
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	T.	41	N	<b>\</b> -	= 1

Functions	RANK	Implemented
V-I(Temp)	1	0
Capacitance	1	0
Surge current-Transient	1	0



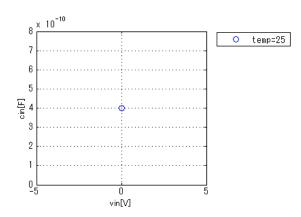
Simulation results are following. Explanatory notes — : simulated

## Vrlr[Temp]

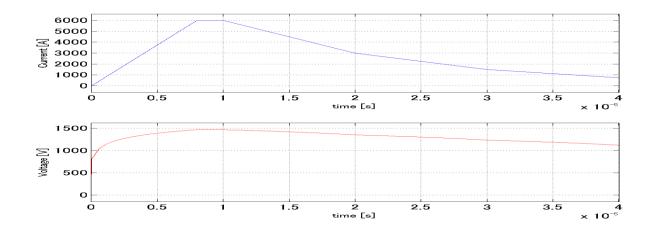


## CjVr

Freq. = 0.001MHz



### Surge Current Waveform (8u/20u)





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