

LTspice Model

Other IC

TOREX

XC6119N10ANR-G

Model Information

Model An original macro model
Call Name MDC_XC6119N10ANR-G_LT
Pin Assign 1:VIN 2:VSS 3:Cd 4:VOUT
File List Model Library MDC_XC6119N10ANR-G_LT01.lib
 Model Report MDC_XC6119N10ANR-G_LT.pdf (this file)
Verified Simulator Version LTspice version XVII
Note

References

The information which was used for modeling is as follow:

[Data Sheet]

- Date/Version JTR02016-004
- Product name XC6119N10ANR-G
- Company name TOREX SEMICONDUCTOR LTD.
- Characteristics Transient,Transient2

Simulation Condition

This table shows the value of evaluated simulation condition that was not occurs any convergence problems in this area.

Item	Condition	Unit
Temperature	25	deg C

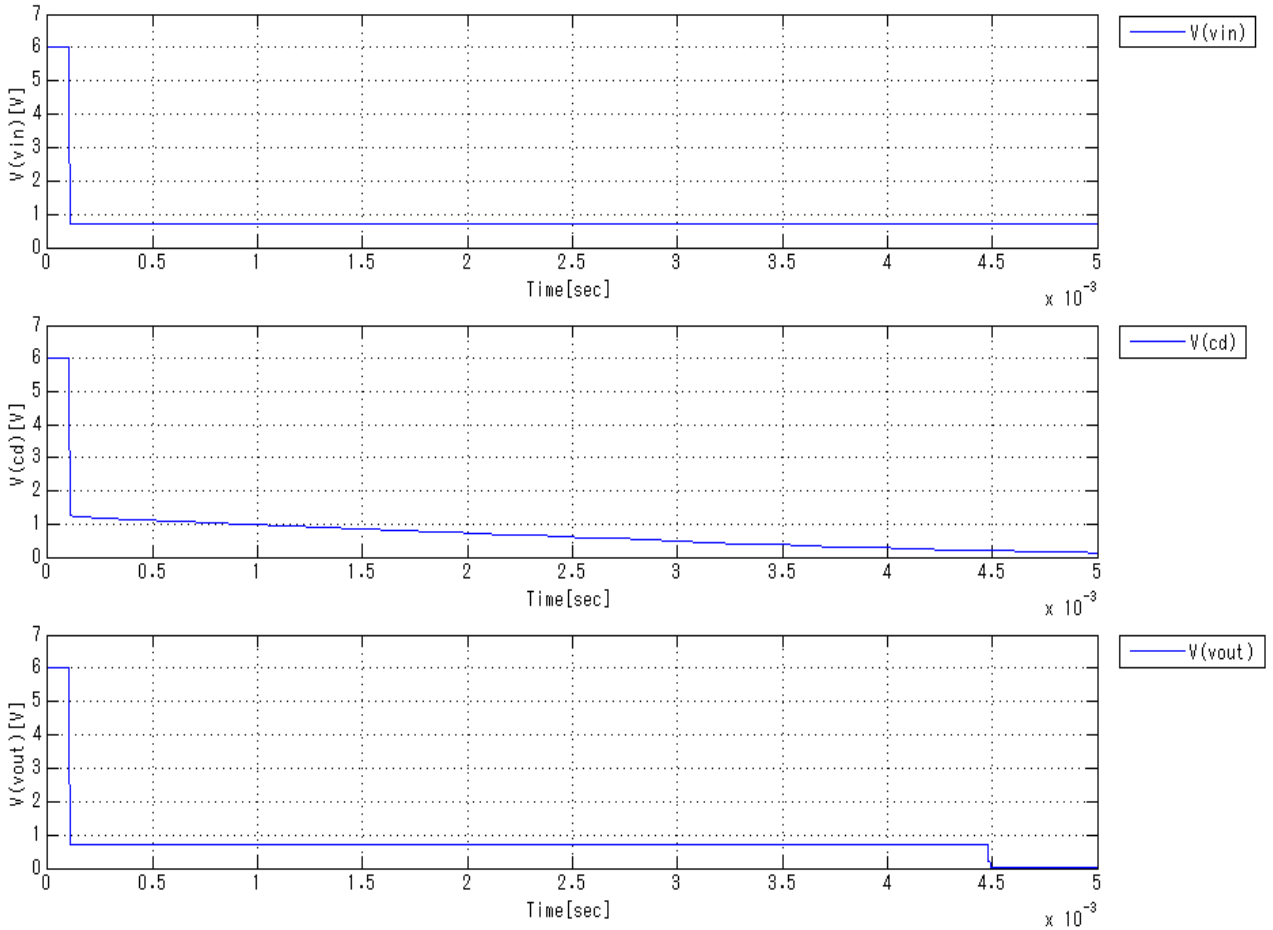
Simulation results are following.
Explanatory notes — : simulated

Transient

Temp. = 25deg C

DELAY CAPACITANCE : 10[nF]

VIN(min)=0.7V VIN(max)=6.0V
tf=5us



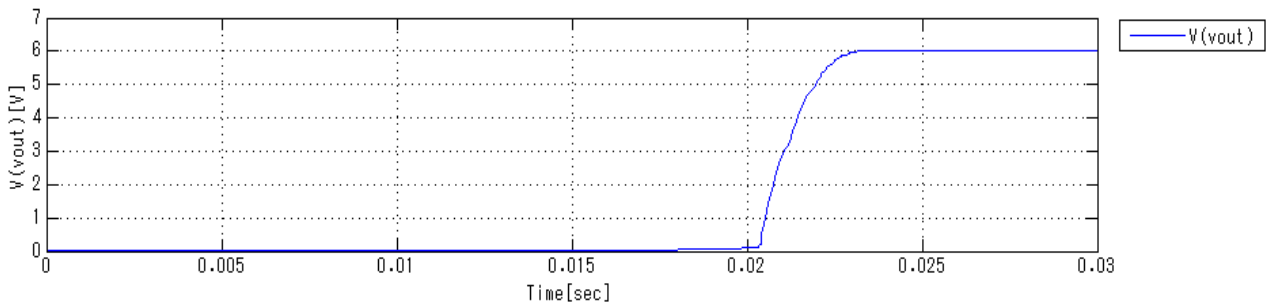
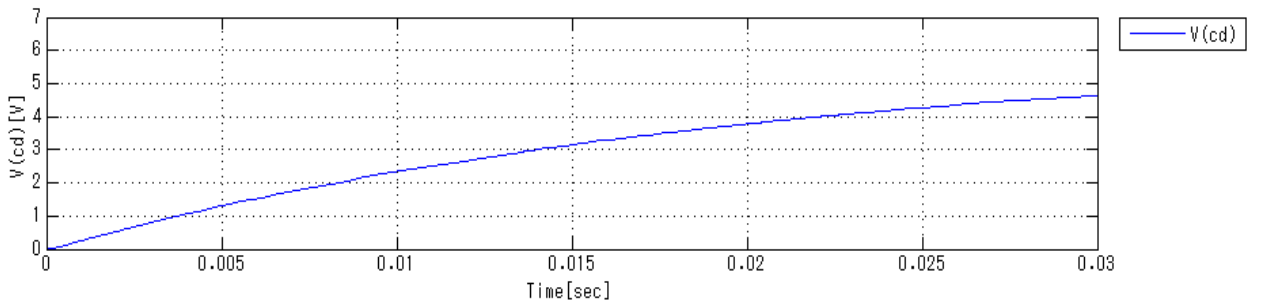
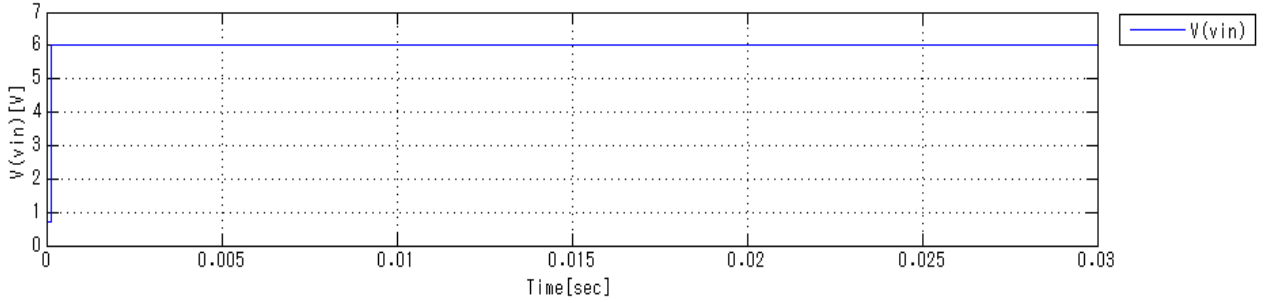
Simulation results are following.
Explanatory notes — : simulated

Transient2

Temp. = 25deg C

DELAY CAPACITANCE : 10[nF]

VIN(min)=0.7V VIN(max)=6.0V
tr=5us



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