

MDC_ERZVA7V391_LT

LTspice Model Varistor Panasonic ERZVA7V391

Model Information

 Model
 A macro model

 Call Name
 MDC_ERZVA7V391_LT

 Pin Assign
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 File List
 Model Library Model Report

MDC_ERZVA7V391_LT01.lib MDC_ERZVA7V391_LT.pdf (this file)

Verified Simulator Version Note

LTspice version XVII

References

The information which was used for modeling is as follow:

2021/12/23 ERZVA7V391 Panasonic Corporation VfIfTemp,VaristorVoltage,CapacitanceVr,ClampingCurrentW aveform,SurgeCurrentWaveform

Simulation Range

This table shows the range of evaluated simulation range that was not occurs any convergence problems in this area.

Item	Range			Unit
	Min.		Max.	
VaristorVoltage(typ)	390(at 1mA)	to	390(at 1mA)	V
ClampingVoltage(max)	650(at 10A)	to	650(at 10A)	V
Temperature	-40	to	125	deg C





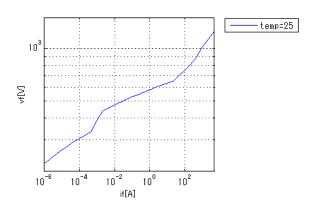
× : Not Implemented

Model Functions Table	RANK=1	— : Not applicable	
Functions	RANK	Implemented	
IF-VF(Temp)	1	0	
IR-VR(Temp)	1	0	
Capacitance	1	0	



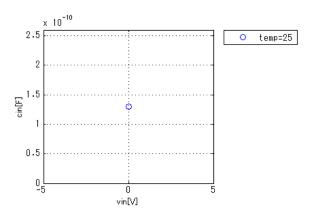
Simulation results are following. Explanatory notes -: simulated

Vflf[Temp] (same Vrlr[Temp])

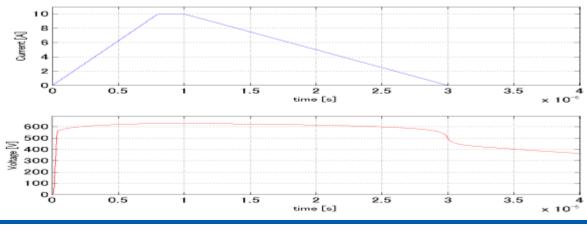


CapacitanceVr

Freq = 1000Hz

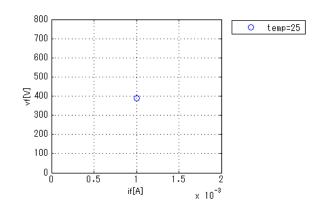


Clamping Current Waveform



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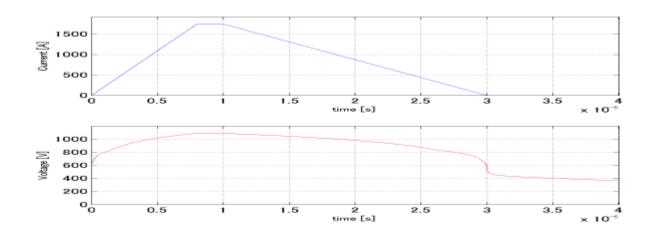
Varistor Voltage





Simulation results are following. Explanatory notes -: simulated

Surge Current Waveform





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