

MDC_UVR1H4R7MDD_LT

LTspice Model Capacitor Nichicon UVR1H4R7MDD

Model Information

Model	A macro mode	l	
Call Name	MDC_UVR1H4R7MDD_LT		
Pin Assign	1:1 2:2		
File List	Model Library	MDC_UVR1H4R7MDD_LT.lib	
	Model Report	MDC_UVR1H4R7MDD_LT.pdf(this file)	
Verified Simulator Version		LTspice XVII	

Note

References

The information which was used for modeling is as follow:

[Data Sheet]

Date/Version	
Product name	UVR1H4R7MDD
Company name	Nichicon Corp.

[Characteristics listed] • Characteristics

Capacitance , Impedance - Frequency

Simulation Condition

This table shows the range of evaluated simulation range that was not occurs any convergence problems in this area.

Item	Condition	Unit
Temperature	20	deg C



O:Implemented

× : Not Implemented

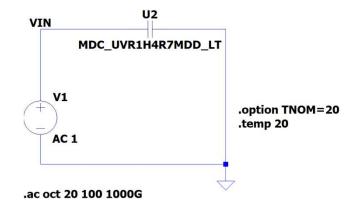
-: Not applicable

Model Functions Table	RANK=1	— : Not applicable
Functions	RANK	Implemented
Capacitance-Frequency	1	0
Impedance-Frequency	1	0



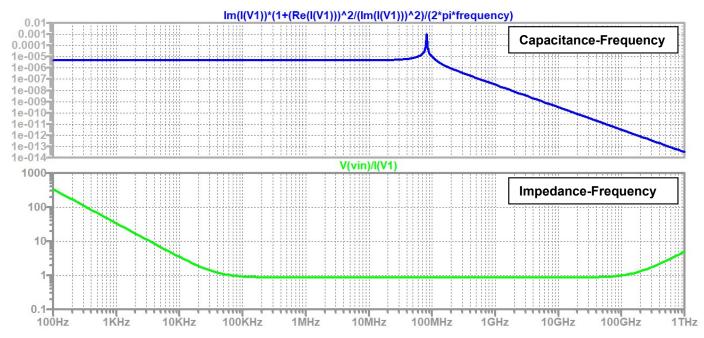
Capacitance , Impedance-Frequency Testbench

Referred to Data Sheet



Simulation results are following. Explanatory notes -: simulated

Capacitance, Impedance-Frequency





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