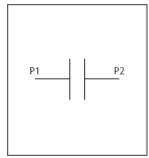
PSpice Model Film Capacitor NISSEI MPEC1250J1030B59P045



Model Information

 Model
 An original macro model

 Call Name
 MDC_MPEC1250J1030B59P045_PS

 Pin Assign
 1:P1 2:P2

 File List
 Model Library
 MDC_MPEC1250J1030B59P045_PS01.lib

 Model Report
 MDC_MPEC1250J1030B59P045_PS.pdf (this file)

Verified Simulator Version Note

PSpice version 17.2

References

The information which was used for modeling is as follow:

[Data Sheet]
Date/Version
Product name
Company name
Characteristics

20.05.20 MPEC1250J1030B59P045 NISSEI Electric Co., Ltd. CapVf,CapTemp[Vf],CapVac,ImpedanceFreq[Vf],ESRFreq[V f],InductanceFreq[Vf],CapFreq[Vf],ResistanceVf[Temp]

Simulation Range

This table shows the range of evaluated simulation range that was not occurs any convergence problems in this area.

Item	Range			Unit
	Min.		Max.	
Temperature	-40	to	105	deg C

Modech

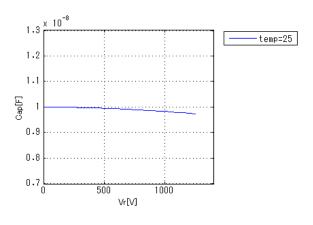
Capacitor		O : Implemented × : Not Implemented	
Model Functions Table	RANK=1	— : Not applicable	
Functions	RANK	Implemented	
Impedance-Frequency	1	0	
ESR-Frequecy	1	0	
Capacitance-Frequency	1	0	
Inductance-Frequency	1	0	
Capacitance-Voltage	1	0	
Capacitance-Temp	1	0	
Capacitance-Vac	1	0	
Resistance-Voltage	1	0	



Simulation results are following. Explanatory notes -: simulated

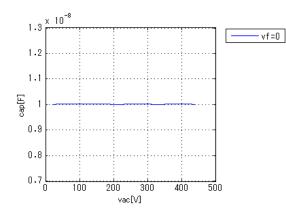
CapVf





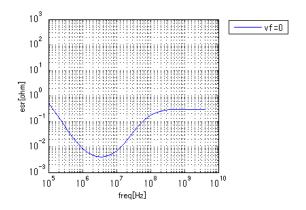
CapVac

Freq = 1000Hz, VF = 0V



ESRFreq[Vf]

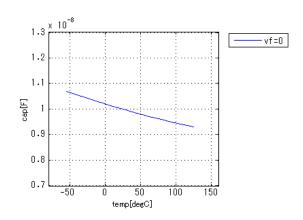
temp = 25degC



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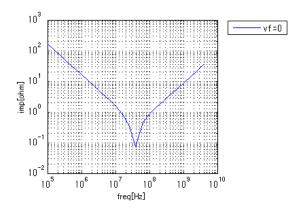
CapTemp[Vf]

Freq = 1000Hz



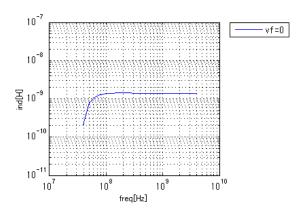
ImpedanceFreq[Vf]

temp = 25degC



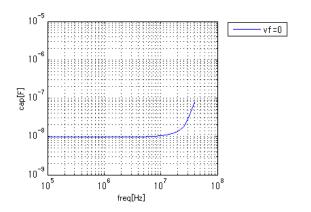
InductanceFreq[Vf]

temp = 25degC

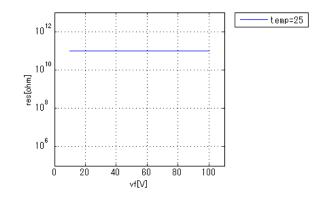




CapFreq[Vf]



ResistanceVf[Temp]





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