

MDC_2SD2402_LT

LTspice Model NPN RENESAS 2SD2402

Model Information

ModelMextram modelCall NameMDC_2SD2402_LTPin Assign1:C 2:B 3:EFile ListModel Library
Model Report

MDC_2SD2402_LT01.lib MDC_2SD2402_LT.pdf (this file)

Verified Simulator Version Note

LTspice version XVII

References

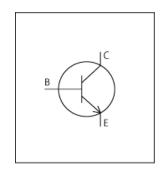
The information which was used for modeling is as follow:

November 1994 2SD2402 Renesas Electronics Corporation IcVce[ib],IcVbe[Temp],hFEIc[Temp],Vce(sat)Ic[Temp],Vbe(s at)Ic[hFE],fTIc[Vce],Cob

Simulation Range

This table shows the range of evaluated simulation range that was not occurs any convergence problems in this area.

Item	Range			Unit
	Min.		Max.	
Collector-emitter voltage (DC)	0	to	30	V
Base current (DC)	0	to	0.2	А
Temperature	-55	to	150	deg C

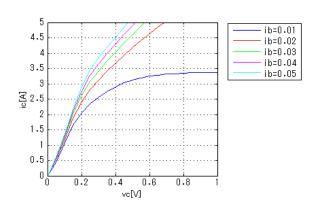




Simulation results are following. Explanatory notes — : simulated

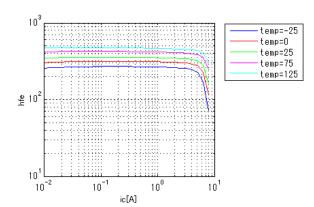
IcVce[ib]

Temp. = 25deg C

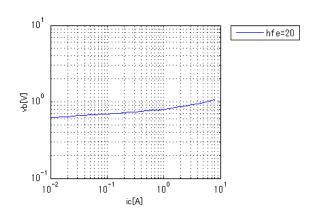


hFElc[Temp]

Vce = 1V



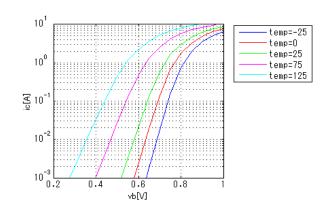
Vbe(sat)lc[hFE]



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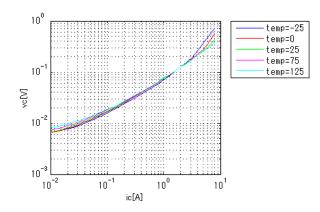
IcVbe[Temp]

Vce = 1V

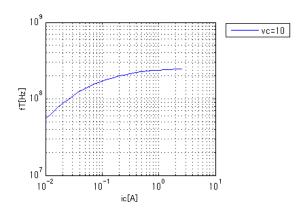


Vce(sat)lc[Temp]

IC/IB = 20





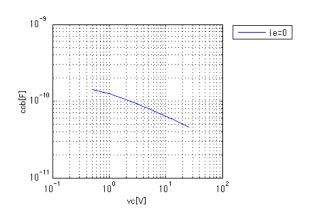




Simulation results are following. Explanatory notes -: simulated

Cob

Freq. = 1MHz





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