

LTspice Model TVS SHINDENGEN ST06-33CE

Model Information

 Model
 A macro model based on general SPICE diode model

 Call Name
 MDC_ST06-33CE_LT

 Pin Assign
 1:C 2:A

 File List
 Model Library
 MDC_ST06-33CE_LT01.lib

 Model Report
 MDC_ST06-33CE_LT.pdf (this file)

Verified Simulator Version Note

LTspice version XVII

References

The information which was used for modeling is as follow:

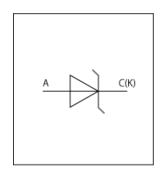
[Data Sheet]
Date/Version
Product name
Company name
Characteristics

2020.01 ST06-33CE Shindengen Electric Manufacturing Co., Ltd. VrIr[Temp],BvTemp,IrTemp[Vr],CapacitanceFreq[Vr],CjVr,Su rgeCurrentWaveform

Simulation Range

This table shows the range of evaluated simulation range that was not occurs any convergence problems in this area.

Item	Range			Unit
	Min.		Max.	
Reverse Voltage	33	to	33	V
Temperature	-55	to	175	deg C



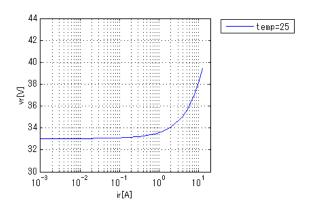
MoDeCH

Diode		O : Implemented × : Not Implemented	
Model Functions Table	RANK=1	— : Not applicable	
Functions	RANK	Implemented	
IF-VF(Temp)	1	—	
IR-VR(Temp)	1	0	
BV(Temp)	1	0	
Capacitance	1	0	
Reverse recovery characteristics	1	—	
Zz-Iz	1	—	
Rectification characteristics(Bridge)	1	—	
Surge-Transient	1	0	
tlp	1	—	

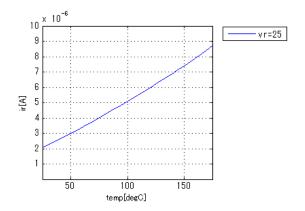


Simulation results are following. Explanatory notes -: simulated

Vrlr[Temp]

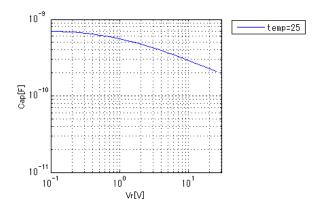


IrTemp[Vr]



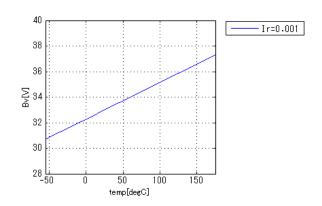


Freq = 100000Hz

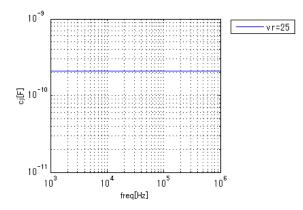


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BvTemp



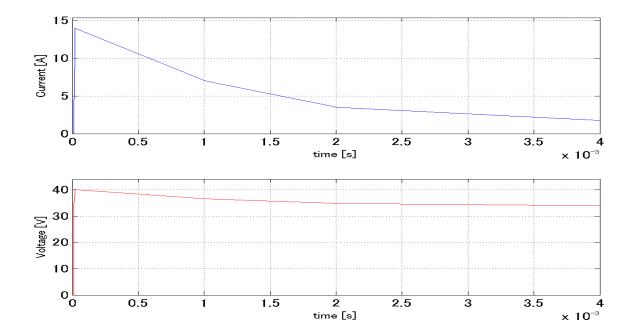
CapacitanceFreq[Vr]





Simulation results are following. Explanatory notes -: simulated

Surge Current Waveform (Reverse 10u/1000u)





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