

# LTspice Model

## 2-input positive-OR gate

### Texas Instruments Inc.

### SN74AHC32QPWRG4Q1

#### Model Information

**Model** A macro model  
**Call Name** MDC\_SN74AHC32QPWRG4Q1\_LT  
**Pin Assign** 1:1A 2:1B 3:1Y 4:2A 5:2B 6:2Y 7:GND 8:3Y 9:3A 10:3B 11:4Y 12:4A 13:4B 14:VCC  
**File List** Model Library MDC\_SN74AHC32QPWRG4Q1\_LT01.lib  
 Model Report MDC\_SN74AHC32QPWRG4Q1\_LT.pdf(this file)  
**Verified Simulator Version** LTspice version XVII

#### Note

#### References

The information which was used for modeling is as follow:

[Data Sheet]

- Date/Version Unknown
- Product name SN74AHC32QPWRG4Q1
- Company name Texas Instruments Inc.

[Characteristics listed]

- Characteristics Operating Range 2-V to 5.5-V VCC  
 VIL, VIH, VOL, VOH, IOL, IOH  
 tpLH, tpHL

#### Simulation Condition

This table shows the range of evaluated simulation range that was not occurs any convergence problems in this area.

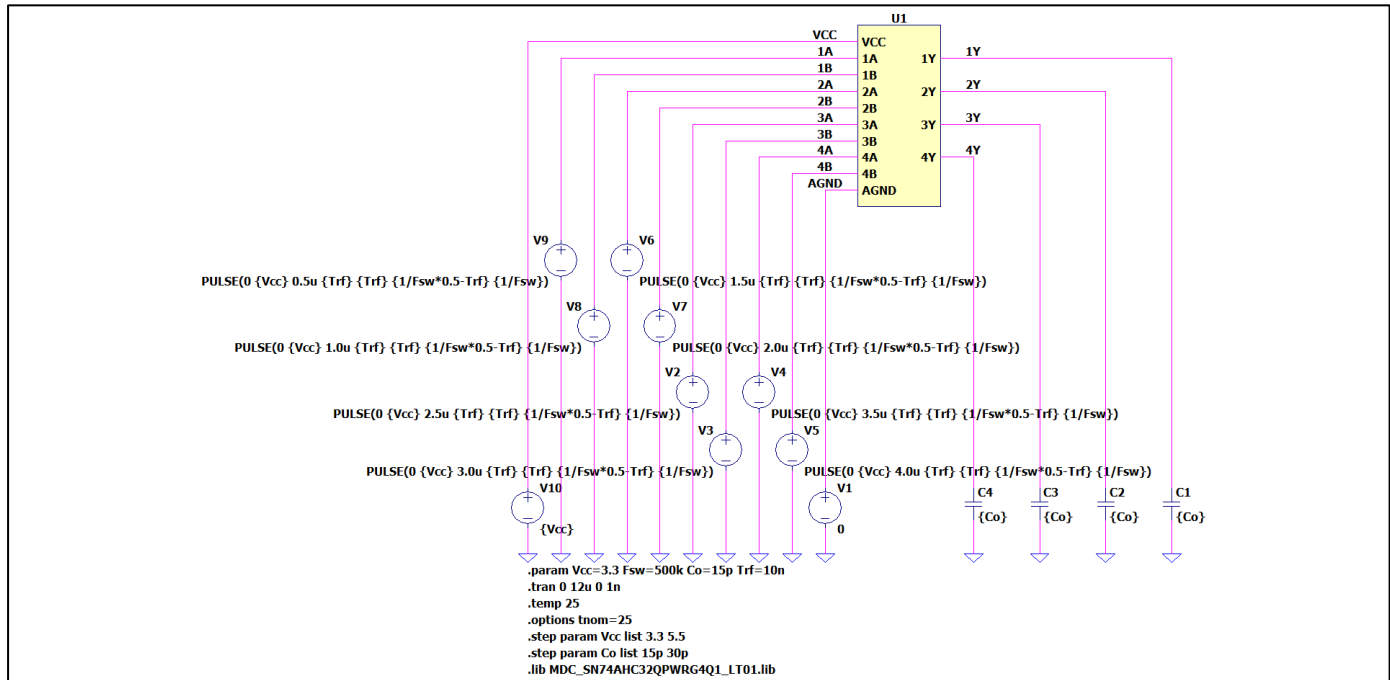
Item	Condition	Unit
Temperature	25	deg C

**Model Functions Table**

Functions	Implemented
2-input OR function	○

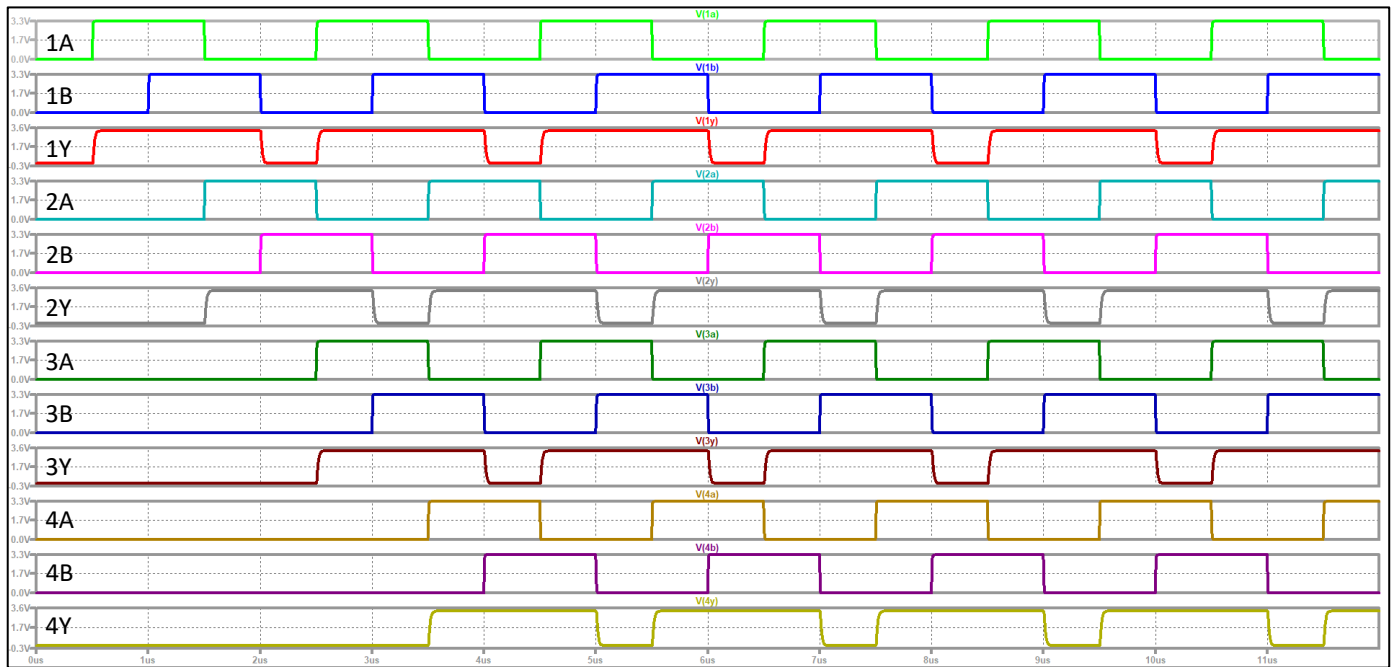
**2-input OR function (Vcc = 3.3V, Co = 50pF) Testbench**

Referred to Data Sheet



Simulation results are following.  
Explanatory notes — : simulated

**2-input OR function (Vcc = 3.3V, Co = 50pF) Testbench**



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