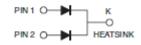


LTspice Model Schottky Diode VISHAY POLYTECH V20PWM45C

SlimDPAK (TO-252AE)



Model Information

 Model
 A macro model based on general SPICE diode model

 Call Name
 MDC_V20PWM45C_LT

 Pin Assign
 1:A1 2:A2 K;C

 File List
 Model Library
 MDC_V20PWM45C_LT01.lib

 Model Report
 MDC_V20PWM45C_LT.pdf (this file)

Verified Simulator Version Note

LTspice version XVII

References

The information which was used for modeling is as follow:

| [Data Sheet] |
|-----------------|
| Date/Version |
| Product name |
| Company name |
| Characteristics |

17-Mar-2022 V20PWM45C Vishay Polytech Co., Ltd. IrVr[Temp],CjVr,IfVf[Temp],SurgeCurrentWaveform

Simulation Range

This table shows the range of evaluated simulation range that was not occurs any convergence problems in this area.

| Item | Range | | | Unit |
|-----------------|-------|----|------|-------|
| | Min. | | Max. | |
| Reverse Voltage | 45 | to | 45 | V |
| Temperature | -55 | to | 175 | deg C |

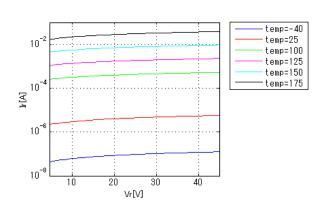
MoDeCH

| Diode Model Functions Table | O : Implemented × : Not Implemente -: Not applicable | |
|---------------------------------------|--|-------------|
| Functions | RANK | Implemented |
| IF-VF(Temp) | 1 | 0 |
| IR-VR(Temp) | 1 | 0 |
| Capacitance | 1 | 0 |
| Reverse recovery characteristics | 1 | — |
| Zz-Iz | 1 | — |
| Rectification characteristics(Bridge) | 1 | — |
| Surge-Transient | 1 | 0 |
| tlp | 1 | — |

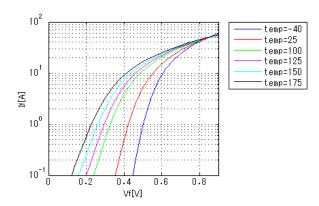


Simulation results are following. Explanatory notes -: simulated

IrVr[Temp]

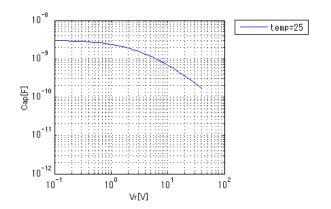


lfVf[Temp]



CjVr

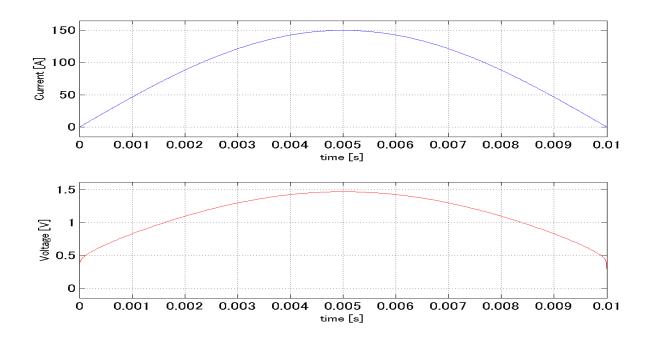
Freq = 1000000Hz





Simulation results are following. Explanatory notes -: simulated

Surge Current Waveform (Forward Sine Half 50Hz)





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