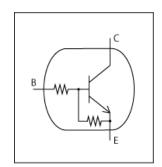


LTspice Model BRT (Bias Resistor Transistor) KEC KRC402



Model Information

Model A macro model based on Gummel-Poon model

Call Name MDC_KRC402_LT

Pin Assign 1:C 2:B 3:E

File List Model Library MDC_KRC402_LT02.lib

Model Report MDC_KRC402_LT.pdf (this file)

Verified Simulator Version

Note

LTspice version XVII

References

The information which was used for modeling is as follow:

[Data Sheet]

Date/Version Revision No : 3

Product name KRC402

Company nameKEC Corporation

● Characteristics IcVi[OFF],IcVi[ON],hFEIc[Temp]

Simulation Range

This table shows the range of evaluated simulation range that was not occurs any convergence problems in this area.

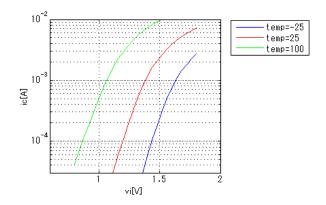
Item	Range			Unit
	Min.		Max.	
Collector-emitter voltage (DC)	0	to	50	V
Base current (DC)	0	to	0.88m	А
Temperature	-55	to	150	deg C



Simulation results are following. Explanatory notes — : simulated

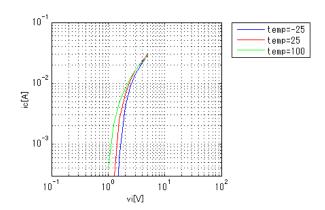
IcVi[OFF]

Vce = 5V



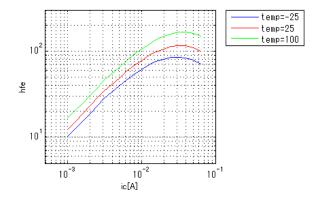
IcVi[ON]

Vce = 0.2V



hFElc[Temp]

Vce = 5V





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MoDeCH Inc.

Head Office

Location: Mitsuiseimei Hachioji Bldg., 5-15 Yokoyama-cho, Hachioji-Shi, Tokyo 192-0081, Japan

Tel:+81-42-656-3360

E-Mail:model-on-support@modech.co.jp

URL:http://www.modech.com/en/