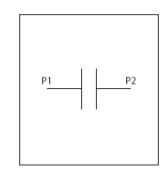


LTspice Model Super Capacitor TOKIN FYH0H224ZF



Model Information

Model An original macro model Call Name MDC_FYH0H224ZF_LT

Pin Assign 1:P1 2:P2
File List Model Libra

Model Library MDC_FYH0H224ZF_LT01.lib

Model Report MDC_FYH0H224ZF_LT.pdf (this file)

Verified Simulator Version

Note

LTspice version XVII

References

The information which was used for modeling is as follow:

[Data Sheet]

Date/VersionProduct nameCompany name2010.11.8FYH0H224ZFTOKIN Corporation

● Characteristics CapFreqVr, EsrFreqVr, ChargeCurrent, DischargeCurrent

Simulation Range

This table shows the range of evaluated simulation range that was not occurs any convergence problems in this area.

| Item | Range | | | Unit |
|------------------|-------|----|-------|-------|
| | Min. | | Max. | |
| Charge system | 0.22 | to | 0.22 | F |
| Discharge system | 0.30 | to | 0.30 | F |
| Frequency | 1,000 | to | 1,000 | Hz |
| Temperature | -25 | to | 70 | deg C |



Model Functions Table

Capacitor

O:Implemented

×: Not Implemented

—: Not applicable

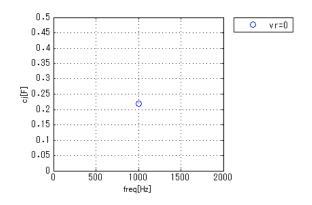
RANK=1

| Functions | RANK | Implemented |
|-----------------------|------|-------------|
| Impedance-Frequency | 1 | _ |
| ESR-Frequecy | 1 | 0 |
| Capacitance-Frequency | 1 | 0 |
| Inductance-Frequency | 1 | _ |

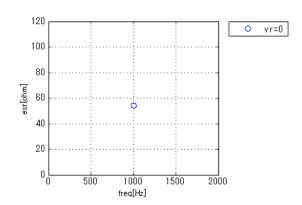


Simulation results are following. Explanatory notes — : simulated

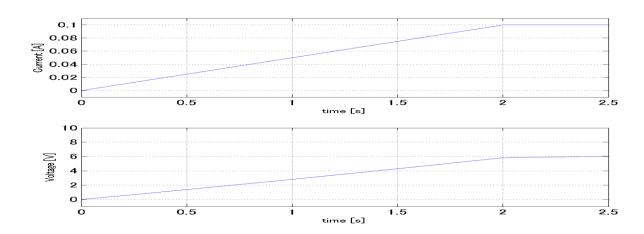
CapFreqVr



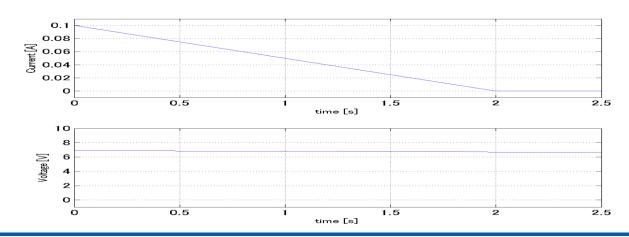
EsrFreqVr



Charge Current



Discharge Current





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