

LTspice Model Transistor Array TOSHIBA TBD62783AFG

Model Information

Model An original macro model
Call Name MDC_TBD62783AFG_LT
Pin Assign 1:11 2:12 3:13 4:14 5:15 6:16 7:17 8:18 9:VCC 10:GND 11:O8 12:O7 13:O6 14:O5 15:O4 16:O3 17:O2 18:O1
File List Model Library MDC_TBD62783AFG_LT01.lib
 Model Report MDC_TBD62783AFG_LT.pdf (this file)
Verified Simulator Version LTspice version XVII
Note

References

The information which was used for modeling is as follow:

[Data Sheet]

- Date/Version 2016-05-11
- Product name TBD62783AFG
- Company name Toshiba Corporation
- Characteristics VIN(ON),Ileak,IIN,VDS(Ron),IF,IR,Transient

Simulation Range

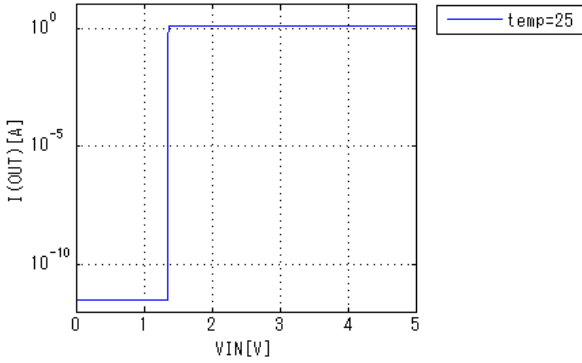
This table shows the range of evaluated simulation range that was not occurs any convergence problems in this area.

Item	Range			Unit
	Min.		Max.	
Temperature	25	to	25	deg C

Simulation results are following.
 Explanatory notes — : simulated

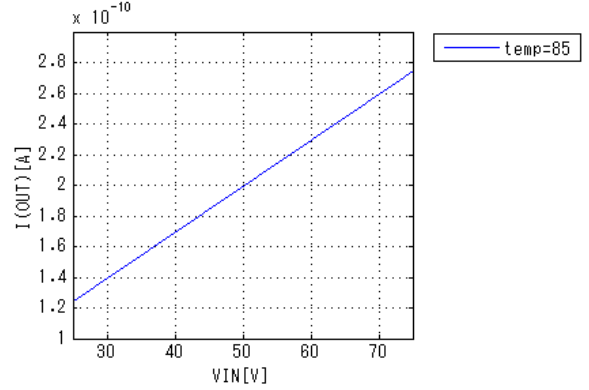
VIN(ON)

VCC = 5V, VOUT = 3V

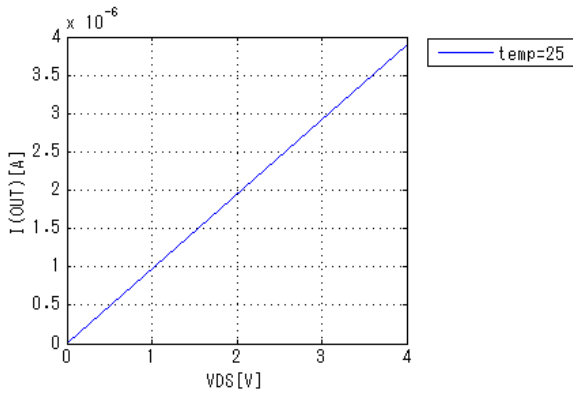


Ileak

VCC = 50V, RL = 125Ω

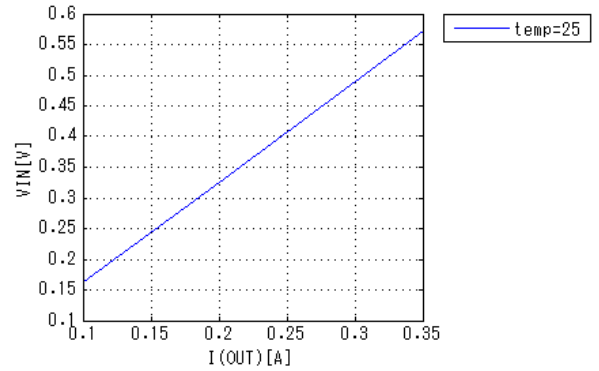


IIN

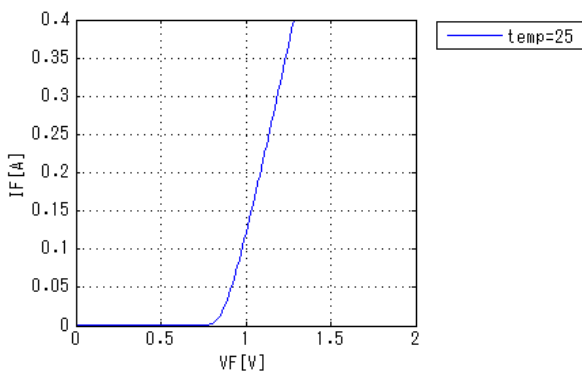


VDS(Ron)

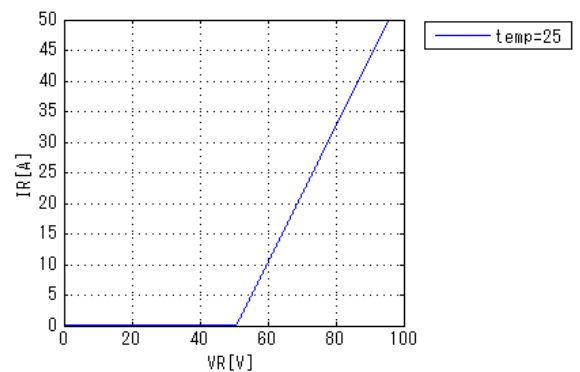
VIN = 5V, VCC = 5V



IF



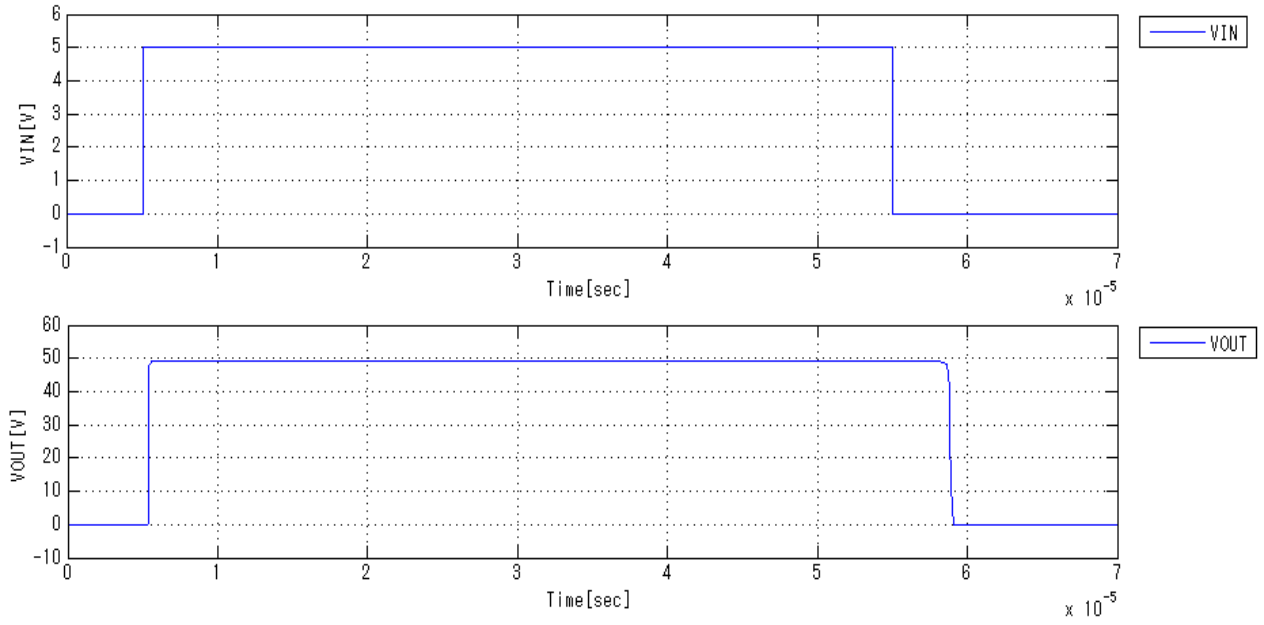
IR



Simulation results are following.
Explanatory notes — : simulated

Transient

Temp. = 25deg C



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