

LTspice Model

Varistor

TDK

S14K300E2K1

Model Information

Model A macro model
Call Name MDC_S14K300E2K1_LT
Pin Assign 1:1 2:2
File List Model Library MDC_S14K300E2K1_LT01.lib
 Model Report MDC_S14K300E2K1_LT.pdf (this file)
Verified Simulator Version LTspice version XVII
Note

References

The information which was used for modeling is as follow:

[Data Sheet]

- Date/Version April 2011
- Product name S14K300E2K1
- Company name TDK Corporation
- Characteristics IRVRTEMP,VRIRTEMP, SurgeWaveform

Simulation Range

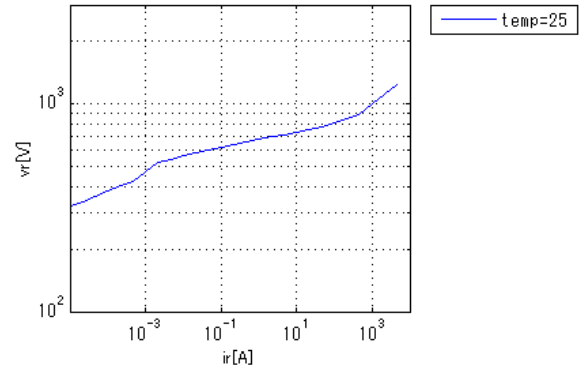
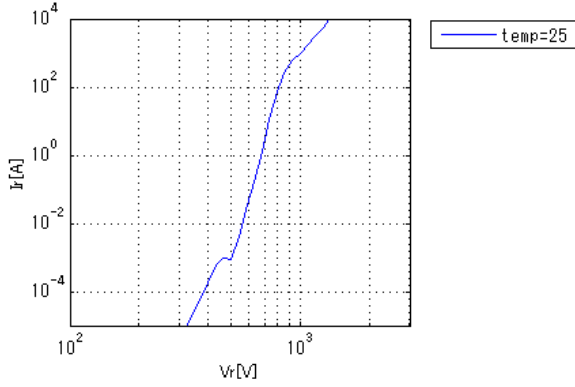
This table shows the range of evaluated simulation range that was not occurs any convergence problems in this area.

Item	Range			Unit
	Min.		Max.	
Reverse Voltage	0	to	775	V
Temperature	-40	to	125	deg C

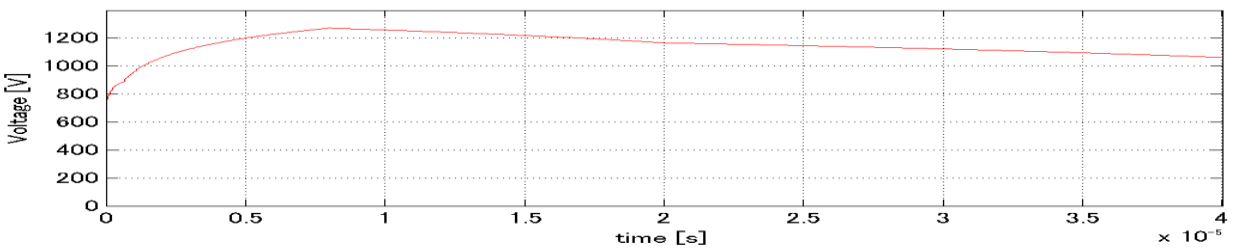
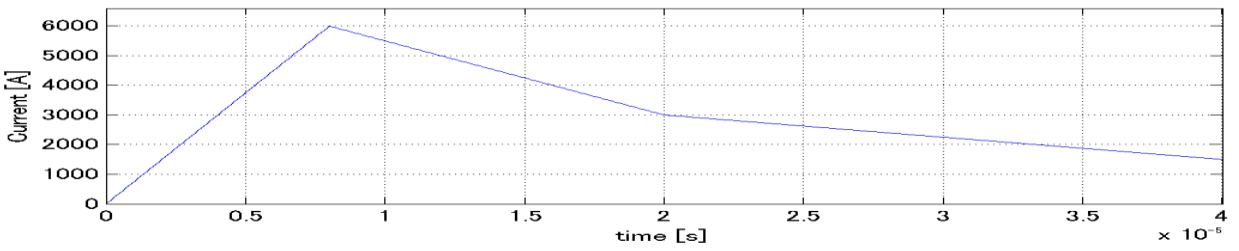
Simulation results are following.
 Explanatory notes — : simulated

IRVRTEMP

VRIRTEMP



Surge Waveform (8u/20u)



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