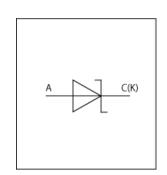


# LTspice Model Zener Diode VISHAY POLYTECH TPSMA33AHM3J\_B



#### **Model Information**

Model A macro model based on general SPICE diode model

Call Name MDC\_TPSMA33AHM3J\_B\_LT

Pin Assign 1:A 2:C

File List Model Library MDC\_TPSMA33AHM3J\_B\_LT02.lib

Model Report MDC\_TPSMA33AHM3J\_B\_LT.pdf (this file)

Verified Simulator Version

Note

LTspice version XVII

#### References

The information which was used for modeling is as follow:

[Data Sheet]

Date/Version 19-Apr-2021

Product nameCompany nameTPSMA33AHM3J\_BVishay Polytech Co., Ltd.

● Characteristics Vflf[Temp], Vrlr[Temp], IrVr[Temp], Vrlr[Temp]2, CjVr, SurgeWa

veform

#### Simulation Range

This table shows the range of evaluated simulation range that was not occurs any convergence problems in this area.

Item	Range			Unit
	Min.		Max.	
ZenerVoltage(typ)	33(at 1mA)	to	33(at 1mA)	V
ClampingVoltage(max)	45.7(at 8.8A)	to	45.7(at 8.8A)	V
Temperature	-65	to	185	deg C



**Model Functions Table** 

## Diode

O: Implemented

×: Not Implemented

—: Not applicable

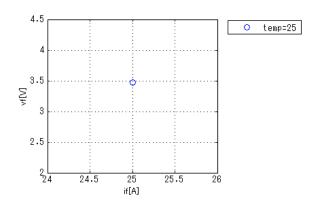
## RANK=1

Functions	RANK	Implemented
IF-VF(Temp)	1	0
IR-VR(Temp)	1	0
Capacitance	1	0
Reverse recovery characteristics	1	
Rectification characteristics(Bridge)	1	-
Surge reverse current-Transient	1	0

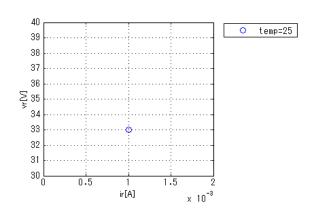


Simulation results are following. Explanatory notes — : simulated

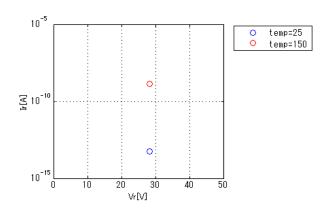
### Vflf[Temp]



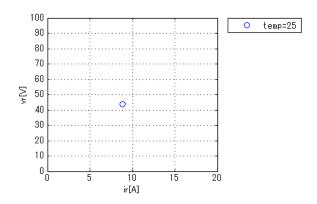
### Vrlr[Temp]



### IrVr[Temp]

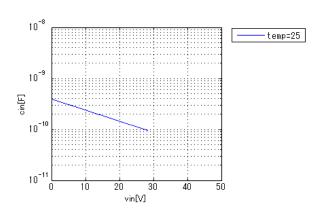


#### VrIr[Temp]2



#### CjVr

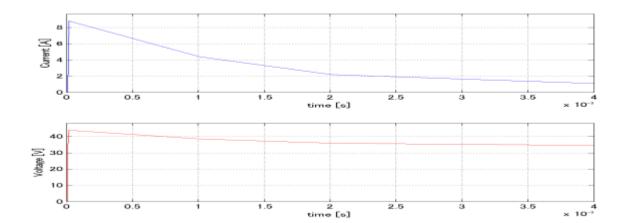
Freq. = 1MHz





Simulation results are following. Explanatory notes — : simulated

### **Surge Reverse Current Waveform**





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