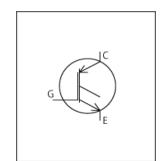


LTspice Model Nch IGBT ROHM RGW80TS65D



Model Information

Model An original macro model based on BSIM3 and Gummel-Poon model

Call Name MDC_RGW80TS65D_LT

Pin Assign 1:C 2:G 3:E

File List Model Library MDC_RGW80TS65D_LT01.lib

Model Report MDC_RGW80TS65D_LT.pdf (this file)

Verified Simulator Version

Note

LTspice version XVII

References

The information which was used for modeling is as follow:

[Data Sheet]

Date/VersionProduct nameCompany name2017.11 - Rev.ARGW80TS65DROHM Co., Ltd.

● Characteristics IcVce[Vge],IcVce[Vge]2,IcVge[Temp],VcesatTemp[Ic],Vce(sa

t)Vge[Ic],Vce(sat)Vge[Ic]2,SwitchingIcc[Tname],SwitchingRg[Tname],CapacitanceVce[Cname],VgeQg[Vcc],IfVf[Temp],Trr

If, Trrlf2, Qrrlf, Qrrlf2, Switching Waveform, TrrWaveform

Simulation Range

This table shows the range of evaluated simulation range that was not occurs any convergence problems in this area.

Item	Range			Unit
	Min.		Max.	
Collector-emitter voltage (DC)	0	to	650	V
Gate-emitter voltage (DC)	-30	to	30	V
Temperature	-55	to	175	deg C



Model Functions Table

IGBT

O: Implemented

×: Not Implemented

—: Not applicable

RANK=1

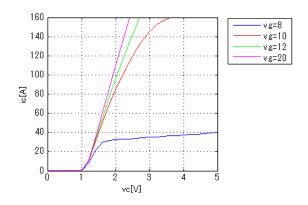
Functions	RANK	Implemented
DC Characteristics(with Temperature)	1	0
Capacitance	1	0
Gate Charge	1	0
Reverse recovery characteristics	1	0
Switching(Typ.) Inductor Load	1	0
trr	1	0



Simulation results are following. Explanatory notes — : simulated

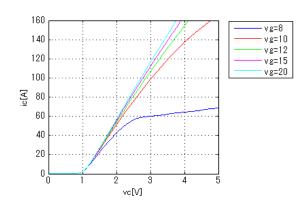
IcVce[Vge]

Temp. = 25deg C



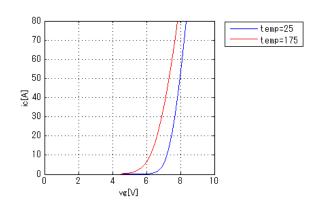
IcVce[Vge]2

Temp. = 175deg C



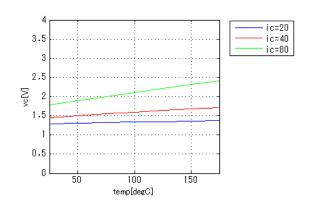
IcVge[Temp]

Vce = 10V



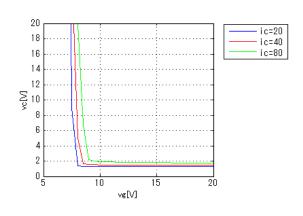
VcesatTemp[lc]

vg = 15V



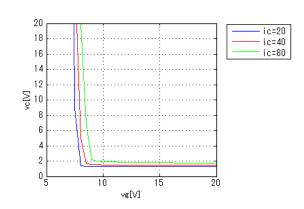
Vce(sat)Vge[lc]

Temp. = 25deg C



Vce(sat)Vge[lc]2

Temp. = 25deg C

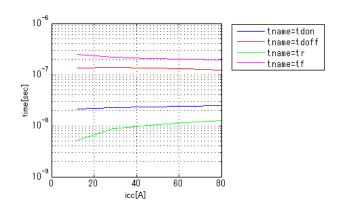




Simulation results are following. Explanatory notes — : simulated

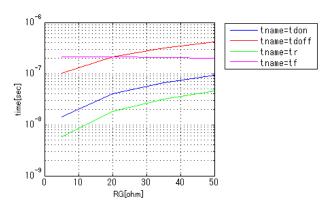
Switchinglcc[Tname]

vgg = 15V, vcc = 400V, RGG = 10ohm, Temp = 175degC



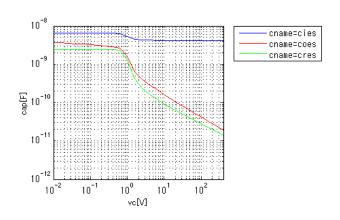
SwitchingRg[Tname]

vgg = 15V, vcc = 400V, icc = 40A, Temp = 175degC



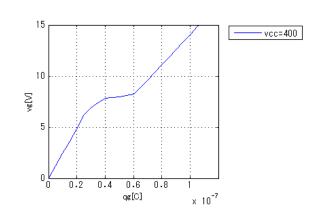
CapacitanceVce[Cname]

freq = 1000000Hz

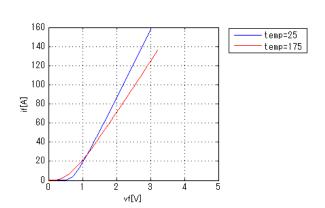


VgeQg[Vcc]

Ic = 40A

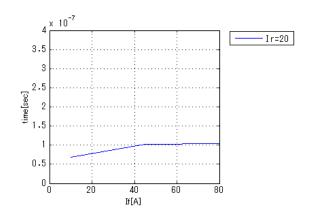


IfVf[Temp]



Trrlf

didt = 200A/us, vcc = 400V, temp = 25degC

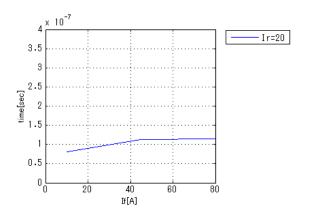




Simulation results are following. Explanatory notes — : simulated

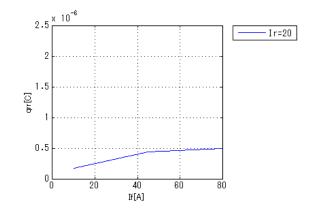
Trrlf2

didt = 200A/us, vcc = 400V, temp = 175degC



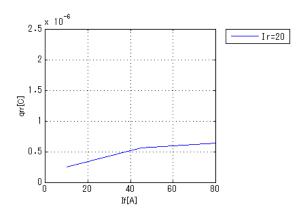
Qrrlf

didt = 200A/us, vcc = 400V, temp = 25degC



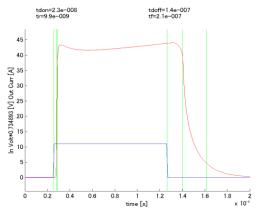
Qrrlf2

didt = 200A/us, vcc = 400V, temp = 175degC



Switching Waveform (Blue: INPUT Red: OUTPUT)

vgg = 15V, vcc = 400V, RGG = 10ohm, Temp = 175degC, Ic = 30A

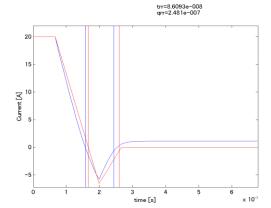




Simulation results are following. Explanatory notes — : simulated

Trr Waveform (Red : Datasheet Blue : Simulation)

didt = 200A/us, vcc = 400V, if = 20A, ir = 6.538A





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