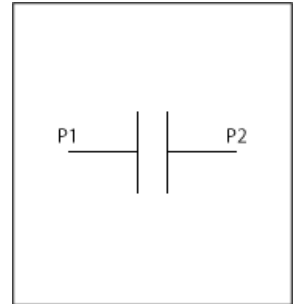


PSpice Model

Ceramic Capacitor

Walsin Technology

0603B104K500CT



Model Information

Model An original macro model
Call Name MDC_0603B104K500CT_PS
Pin Assign 1:P1 2:P2
File List Model Library MDC_0603B104K500CT_PS01.lib
 Model Report MDC_0603B104K500CT_PS.pdf (this file)

Verified Simulator Version PSpice version 17.2
Note

References

The information which was used for modeling is as follow:

[Data Sheet]

- Date/Version Jul. 2023
- Product name 0603B104K500CT
- Company name Walsin Technology Corporation
- Characteristics CapFreq[Vf]

Simulation Range

This table shows the range of evaluated simulation range that was not occurs any convergence problems in this area.

Item	Range			Unit
	Min.		Max.	
Temperature	-55	to	125	deg C

Capacitor

○ : Implemented
 × : Not Implemented
 — : Not applicable

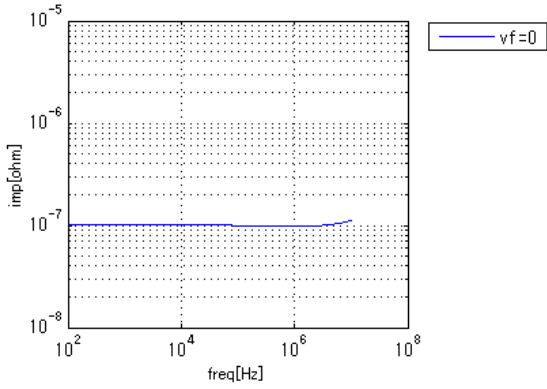
Model Functions Table
RANK=1

Functions	RANK	Implemented
Impedance-Frequency	1	—
ESR-Frequency	1	—
Capacitance-Frequency	1	○
Inductance-Frequency	1	—
Capacitance-Voltage	1	—
Capacitance-Temp	1	—
Capacitance-Vac	1	—
Resistance-Voltage	1	—

Simulation results are following.
Explanatory notes — : simulated

CapFreq[Vf]

Temp = 25degC



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