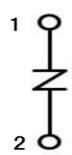


PSpice Model Varistor Panasonic ERZVA7V391



Model Information

Model A macro model

Call Name MDC ERZVA7V391 PS

Pin Assign 1:1 2:2

File List Model Library MDC_ERZVA7V391_PS01.lib

Model Report MDC_ERZVA7V391_PS.pdf (this file)

Verified Simulator Version

Note

PSpice version 17.2

References

The information which was used for modeling is as follow:

[Data Sheet]

Date/VersionProduct name2021/12/23ERZVA7V391

Company name Panasonic Corporation

Characteristics VflfTemp, VaristorVoltage, Capacitance Vr, Clamping CurrentW

aveform, Surge Current Waveform

Simulation Range

This table shows the range of evaluated simulation range that was not occurs any convergence problems in this area.

Item	Range			Unit
	Min.		Max.	
VaristorVoltage(typ)	390(at 1mA)	to	390(at 1mA)	V
ClampingVoltage(max)	650(at 10A)	to	650(at 10A)	V
Temperature	-40	to	125	deg C



O:Implemented

×: Not Implemented

—: Not applicable

Model Functions Table

Model Functions Table	RANK=1	. Not applicable
Functions	RANK	Implemented
IF-VF(Temp)	1	0
IR-VR(Temp)	1	0
Capacitance	1	0

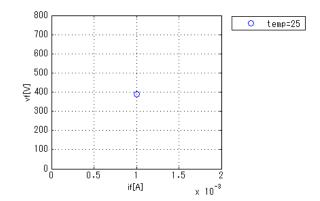


Simulation results are following. Explanatory notes — : simulated

Vflf[Temp] (same Vrlr[Temp])

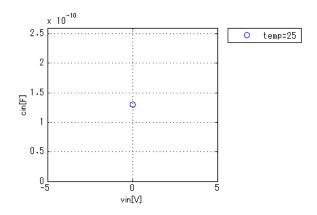
10³ — temp=25

Varistor Voltage

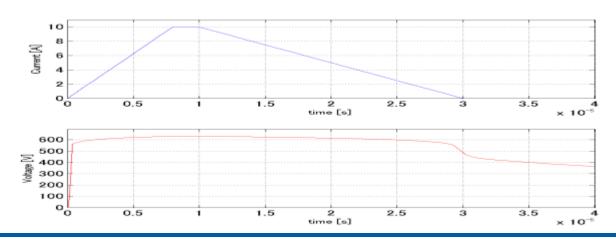


CapacitanceVr

Freq = 1000Hz



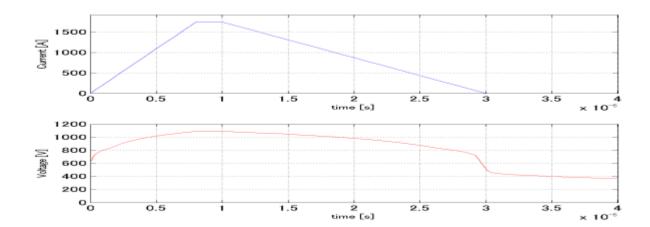
Clamping Current Waveform





Simulation results are following. Explanatory notes — : simulated

Surge Current Waveform





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MoDeCH Inc.

Head Office

Location: 5-15 Yokoyama-cho, Hachioji-Shi, Tokyo 192-0081, Japan

Tel:+81-42-656-3360

E-Mail:model-on-support@modech.co.jp

URL:http://www.modech.com/en/