

PSpice Model Schottky Barrier Diode Nexperia PMEG40T30EP

Model Information

Model A macro model based on general SPICE diode model

Call Name MDC PMEG40T30EP PS

Pin Assign 1:C 2:A

File List Model Library MDC PMEG40T30EP PS.lib

Model Report MDC PMEG40T30EP PS.pdf(this file)

Verified Simulator Version PSpice version 17.2

Note

References

The information which was used for modeling is as follow:

[Data Sheet]

● Date/Version 2017-09-28 ● Product name PMEG40T30EP

Company name Nexperia

[Characteristics listed]

● Characteristics IfVf[Temp], IrVr[Temp], CjVr, TrrlfIr

Simulation Range

This table shows the range of evaluated simulation range that was not occurs any convergence problems in this area.

Item	Range			Unit
	Min.		Max.	
Reverse Voltage	0	to	40	V
Temperature	-40	to	150	deg C





Diode

O: Implemented

× : Not Implemented

─ : Not applicable

Model Functions Table RANK=1

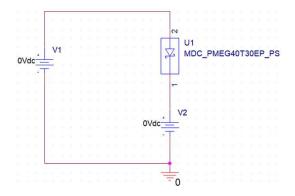
	10 (141)	
Functions	RANK	Implemented
IF-VF(Temp)	1	0
IR-VR(Temp)	1	0
Capacitance	1	0
Reverse recovery characteristics	1	0
Rectification characteristics(Bridge)	1	-
Surge voltage-Transient	2	_



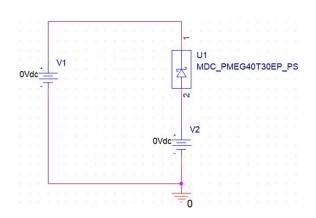
IfVf[Temp], IrVr[Temp] Testbench

Referred to Data Sheet

IfVf[Temp]



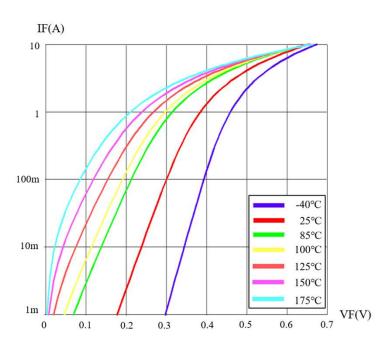
IrVr[Temp]



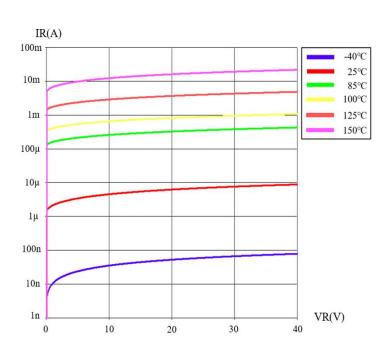
Simulation results are following.

Explanatory notes — : simulated

IfVf[Temp]



IrVr[Temp]

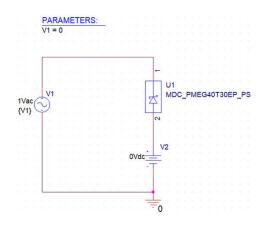


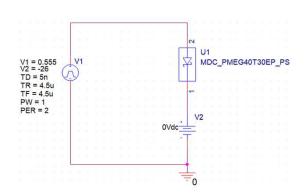


CjVr, trrlflr Testbench

Referred to Data Sheet

CjVr trrlflr

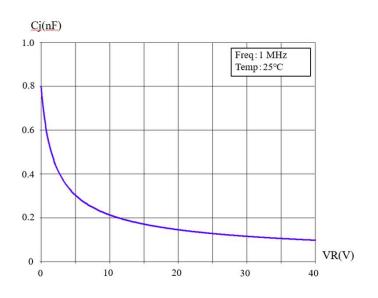


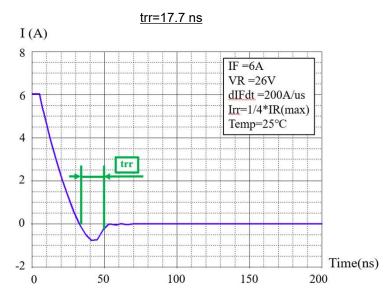


Simulation results are following.

Explanatory notes — : simulated

CjVr trrlflr







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MoDeCH Inc.

Head Office

Location: 5-15 Yokoyama-cho, Hachioji-Shi, Tokyo 192-0081, Japan

Tel:+81-42-656-3360

E-Mail:model-on-support@modech.co.jp

URL:http://www.modech.com/en/