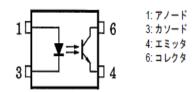


PSpice Model Photocoupler Tr. Output TOSHIBA TLP185



Model Information

Model An original macro model Call Name MDC_TLP185_PS

Pin Assign 1:Anode 2:Cathode 4:Emitter 6:Collector

File List Model Library MDC_TLP185_PS03.lib

Model Report MDC_TLP185_PS.pdf (this file)

Verified Simulator Version

Note

PSpice version 17.2

References

The information which was used for modeling is as follow:

[Data Sheet]

Date/Version 2014/9/1Product name TLP185

Company name Toshiba Corporation

● Characteristics IfVf[Temp],Deltavflf,IcVce[If],Iclf[Vce],IcTemp[Vce],CTRlf[Vc

e],VcesatTemp[If],VcesatTemp[If]2,IcTemp[If],SwitchingRL[Tname],SwitchingTemp[Tname],CjVr,CceVce,SwitchingWavef

orm

Simulation Range

This table shows the range of evaluated simulation range that was not occurs any convergence problems in this area.

| Item | Range | | | Unit |
|--------------------------------|-------|----|------|-------|
| | Min. | | Max. | |
| Collector-emitter voltage (DC) | 0 | to | 80 | V |
| Temperature | -55 | to | 125 | deg C |



Model Functions Table

Photo Coupler

O: Implemented

×: Not Implemented

—: Not applicable

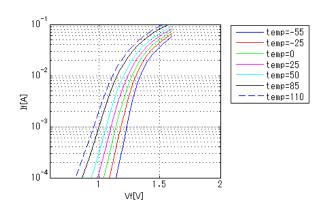
| | A F | VIII. | / | 4 |
|-----|------------|-------|------------|---|
| K / | Δ I | NΙК | | |
| | | | \ — | _ |

| | IVAINIK-I | |
|-------------------|-----------|-------------|
| Functions | RANK | Implemented |
| IF-VF(Temp) | 1 | 0 |
| IC-VCE-IF(Temp) | 1 | 0 |
| Idark-Temp(Vce) | 1 | 0 |
| VCE(sat)-Temp(IF) | 1 | 0 |
| IC-IF(VCE) | 1 | 0 |
| CTR-IF(VCE) | 1 | 0 |
| IC-Temp(IF) | 1 | 0 |
| Switching | 1 | 0 |
| Capacitance | 1 | 0 |

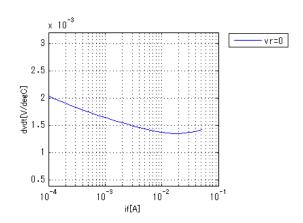


Simulation results are following. Explanatory notes — : simulated

IfVf[Temp]

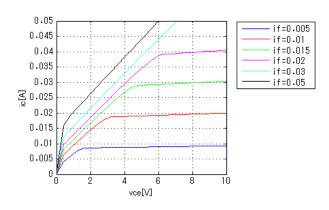


Deltavflf



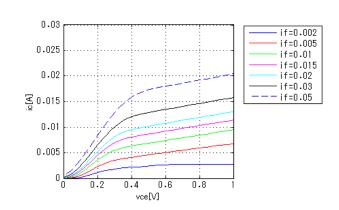
IcVce[If]

temp = 25degC



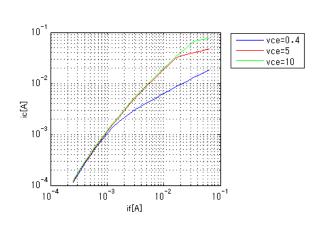
IcVce[If]

temp = 25degC



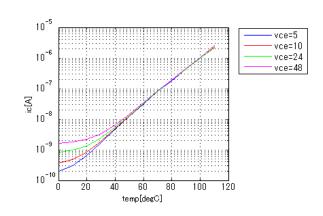
Iclf[Vce]

Temp = 25degC



IcTemp[Vce]

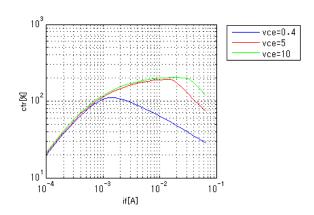
If = 0A





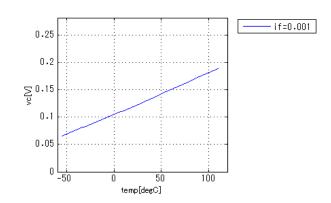
Simulation results are following. Explanatory notes — : simulated

CTRIf[Vce]



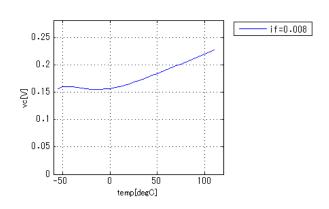
VcesatTemp[If]

Ic = 0.0002A



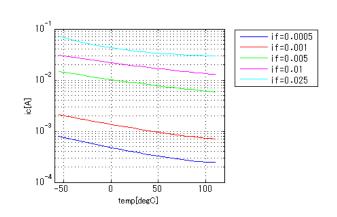
VcesatTemp[lf]2

Ic = 0.0024A



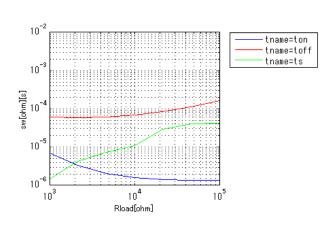
IcTemp[If]

Vce = 5V



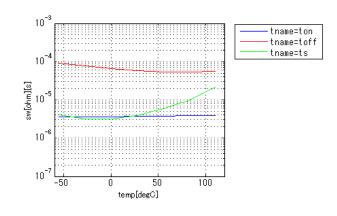
SwitchingRL[Tname]

if = 0.016A, vcc = 5V, temp = 25degC



SwitchingTemp[Tname]

if = 0.016A, vcc = 5V, RL = 1900ohm

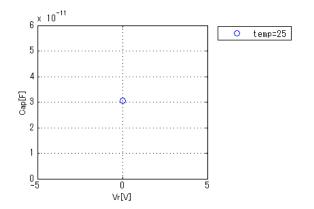




Simulation results are following. Explanatory notes — : simulated

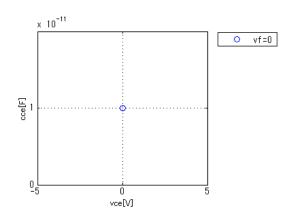
CjVr

freq = 1000000Hz, temp = 25degC



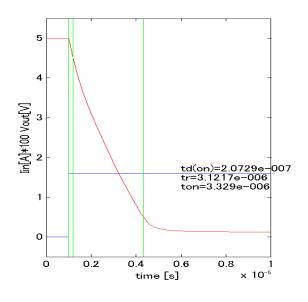
CceVce

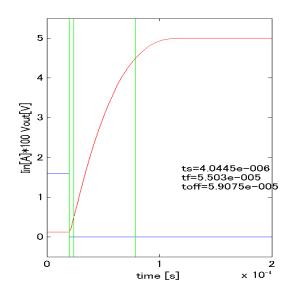
freq = 1000000Hz, temp = 25degC



Switching Waveform (Blue: INPUT Red: OUTPUT)

vcc = 5V, if = 0.016A, Temp = 25degC, RL = 2154ohm







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