# LTspice Model Light Emitting Diode VISHAY POLYTECH VSMB10941X01

### **Model Information**

 Model
 A macro model based on general SPICE diode model

 Call Name
 MDC\_VSMB10941X01\_LT

 Pin Assign
 1:A 2:C

 File List
 Model Library
 MDC\_VSMB10941X01\_LT01.lib

 Model Report
 MDC\_VSMB10941X01\_LT.pdf (this file)

Verified Simulator Version Note

LTspice version XVII

#### References

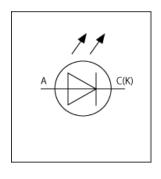
The information which was used for modeling is as follow:

Rev. 1.1, 05-Aug-2019 VSMB10941X01 Vishay Polytech Co., Ltd. IfVf[Temp],IrVr[Temp],CjVr,SurgeWaveform

### Simulation Range

This table shows the range of evaluated simulation range that was not occurs any convergence problems in this area.

Item	Range			Unit
	Min.		Max.	
Reverse Voltage	0	to	5	V
Temperature	-55	to	105	deg C



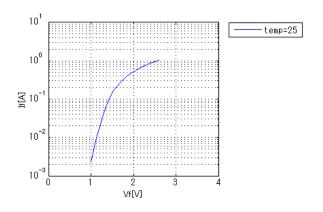
# MoDeCH

Diode Model Functions Table	O : Implemented × : Not Implemented — : Not applicable	
Functions	RANK	Implemented
IF-VF(Temp)	1	0
IR-VR(Temp)	1	0
Capacitance	1	0
Reverse recovery characteristics	1	—
Zz-Iz	1	—
Rectification characteristics(Bridge)	1	—
Surge-Transient	1	0
tlp	1	—



# Simulation results are following. Explanatory notes -: simulated

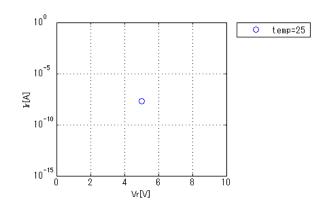
### lfVf[Temp]



#### **CjVr** Freq = 100000Hz

 $\begin{array}{c} 1.2 \times 10^{-10} \\ 0.8 \\ 0.8 \\ 0.6 \\ 0.4 \\ 0.2 \\ 0.5 \\ 0.6 \\ 0.4 \\ 0.7 \\$ 

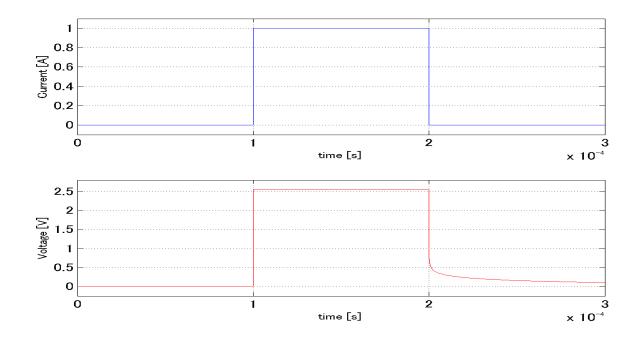
lrVr[Temp]





Simulation results are following. Explanatory notes -: simulated

### Surge Current Waveform ( Forward tp=100us )





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