

# LTspice Model Buck Converter RICHTEK RT7247ANGSP

## Model Information

**Model** An original macro model  
**Call Name** MDC\_RT7247ANGSP\_LT  
**Pin Assign** 1:BOOT 2:VIN 3:SW 4:GND 5:FB 6:COMP 7:EN 8:SS 9:GND  
**File List** Model Library MDC\_RT7247ANGSP\_LT01.lib  
 Model Report MDC\_RT7247ANGSP\_LT.pdf (this file)

**Verified Simulator Version** LTspice version XVII  
**Note**

## References

The information which was used for modeling is as follow:

[Data Sheet]

- Date/Version Unknown
- Product name RT7247ANGSP
- Company name Richtek Technology Corporation
- Characteristics Line regulation,Load regulation

## Simulation Condition

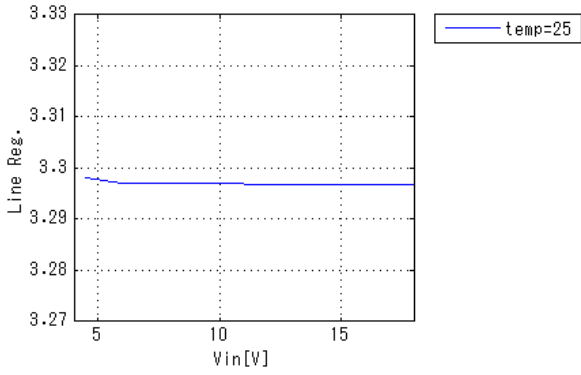
This table shows the value of evaluated simulation condition that was not occurs any convergence problems in this area.

Item	Condition	Unit
Temperature	25	deg C

Simulation results are following.  
Explanatory notes — : simulated

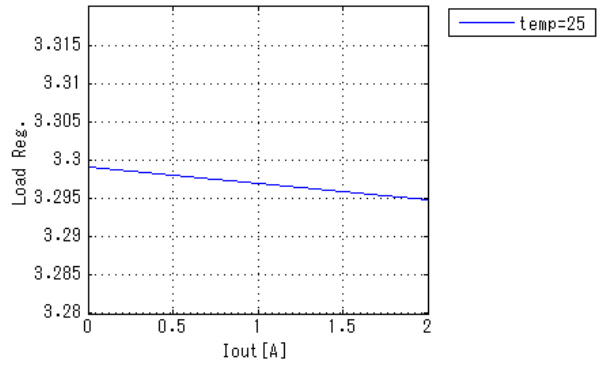
### Line regulation

I<sub>out</sub> = 1A



### Load regulation

V<sub>in</sub> = 12V



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