

# LTspice Model

## Operational Amplifier

### ON

### MC34072DR2G

#### Model Information

**Model** An original macro model  
**Call Name** MDC\_MC34072DR2G\_LT  
**Pin Assign** 1:Output1 2:Input1- 3:Input1+ 4:VEE 5:Input2+ 6:Input2- 7:Output2 8:VCC  
**File List** Model Library MDC\_MC34072DR2G\_LT01.lib  
 Model Report MDC\_MC34072DR2G\_LT.pdf (this file)

**Verified Simulator Version** LTspice version XVII

**Note**

#### References

The information which was used for modeling is as follow:

[Data Sheet]

- Date/Version July,2014-Rev.22
- Product name MC34072DR2G
- Company name ON Semiconductor.
- Characteristics Input offset voltage,Input bias current,Slew rate,GainFreq.[Temp]

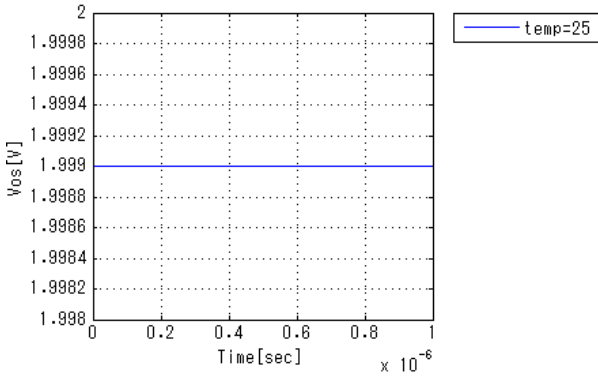
#### Simulation Condition

This table shows the value of evaluated simulation condition that was not occurs any convergence problems in this area.

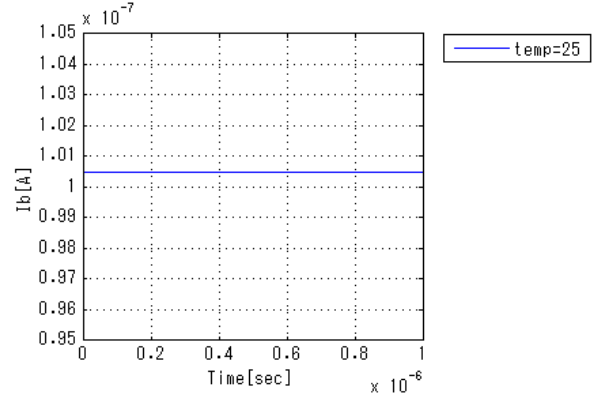
Item	Condition	Unit
Temperature	25	deg C

Simulation results are following.  
 Explanatory notes — : simulated

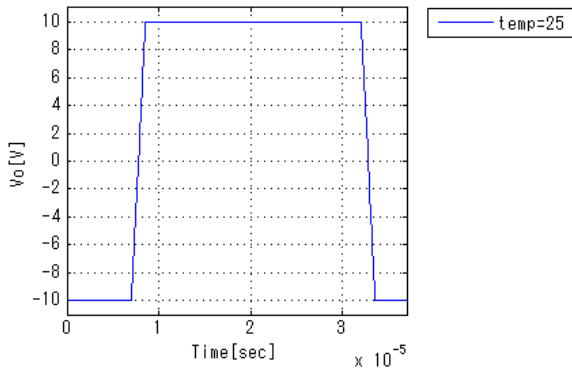
**Input offset voltage**



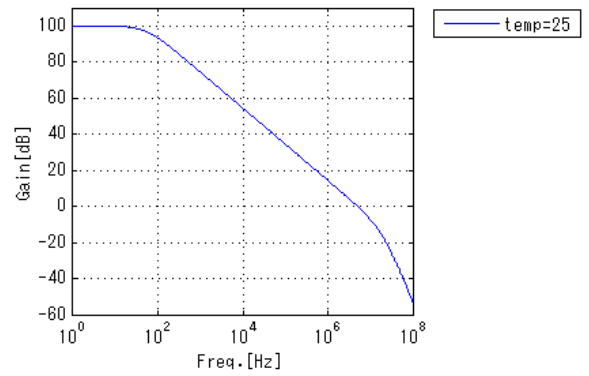
**Input bias current**



**Slew rate**



**GainFreq.[Temp]**



**Slew rate definition: 10%~90% of  $V_o$**

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