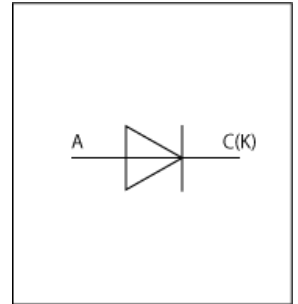


LTspice Model

ESD Protection Diode

Origin Electric

F1C560



Model Information

Model A macro model based on general SPICE diode model
Call Name MDC_F1C560_LT
Pin Assign 1:A 2:C
File List Model Library MDC_F1C560_LT01.lib
 Model Report MDC_F1C560_LT.pdf (this file)

Verified Simulator Version LTspice version XVII
Note

References

The information which was used for modeling is as follow:

[Data Sheet]

- Date/Version OSES1589_PD-03/2019
- Product name F1C560
- Company name Origin Electric Co., Ltd.
- Characteristics IrVr[Temp],IrVr(Breakdown),SurgeWaveform

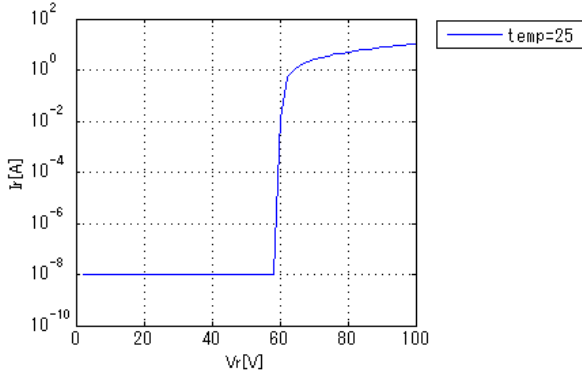
Simulation Range

This table shows the range of evaluated simulation range that was not occurs any convergence problems in this area.

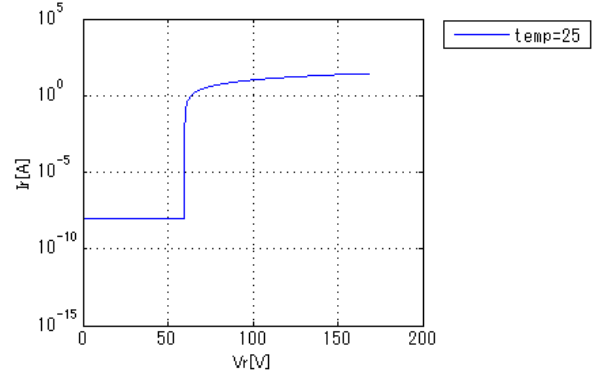
Item	Range			Unit
	Min.		Max.	
Reverse Voltage	0	to	168	V
Temperature	-40	to	125	deg C

Simulation results are following.
 Explanatory notes — : simulated

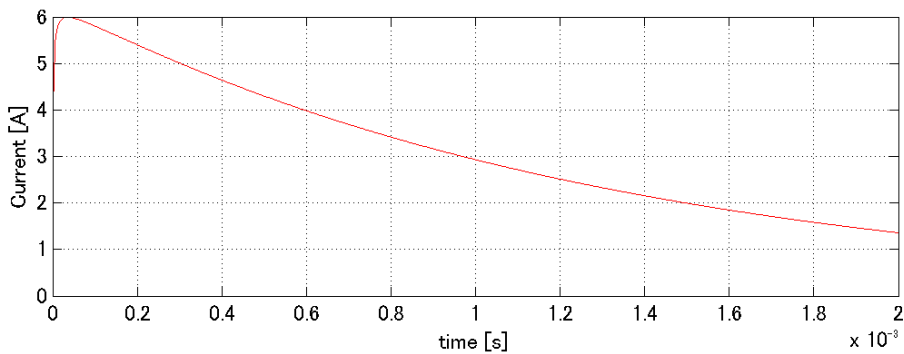
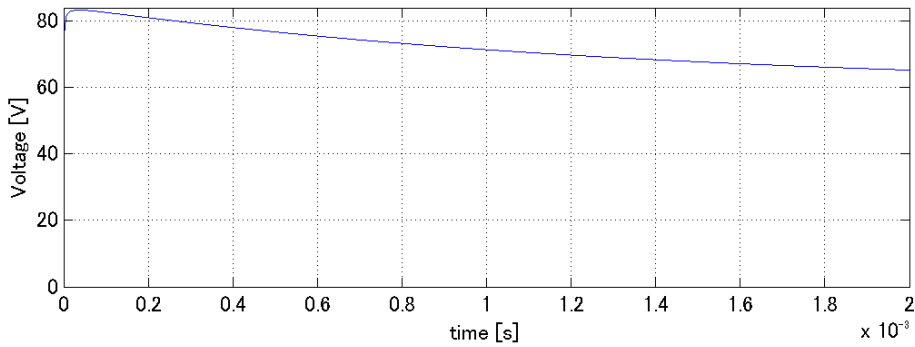
IrVr[Temp]



IrVr(Breakdown)



SurgeWaveform(Reverse Current 10/1000us)



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