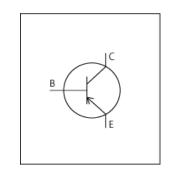


# PSpice Model PNP RENESAS 2SA1330



# **Model Information**

Model Gummel-Poon model
Call Name MDC\_2SA1330\_PS

Pin Assign 1:E 2:B 3:C
File List Model Library

Model Library MDC\_2SA1330\_PS03.lib Model Report MDC\_2SA1330\_PS.pdf (this file)

**Verified Simulator Version** 

Note

PSpice version 17.2

#### References

The information which was used for modeling is as follow:

[Data Sheet]

Date/VersionProduct nameApril 1st, 20102SA1330

Company name Renesas Electronics Corporation

● Characteristics IcVce[ib],IcVbe[Temp],hFEIc[Vce],Vce(sat)Ic[hFE],Vbe(sat)Ic

[hFE],fTle[Vce],Cob,Switchinglcc[Tname],SwitchingWavefor

m

#### Simulation Range

This table shows the range of evaluated simulation range that was not occurs any convergence problems in this area.

Item	Range			Unit
	Min.		Max.	
Collector-emitter voltage (DC)	0	to	-200	V
Collector current (DC)	0	to	-100m	Α
Temperature	-55	to	150	deg C



BJT

O: Implemented

×: Not Implemented

—: Not applicable

Model Functions Table	
Model Functions Table	RANK=1

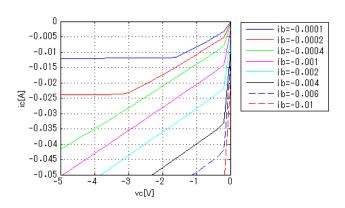
	10 (14)( ±	
Functions	RANK	Implemented
IC-VBE(Temp)	1	0
IC-VCE-IB(Temp)	1	0
IC-hFE(Temp)	1	0
VCE(sat)-IC	1	0
VBE(sat)-IC	1	0
Capacitance	1	0
Transition	1	0
Switching	1	0



Simulation results are following. Explanatory notes — : simulated

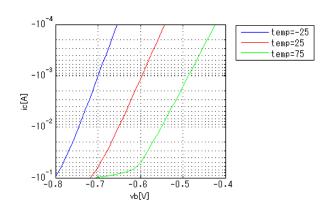
## IcVce[ib]

Temp. = 25degC

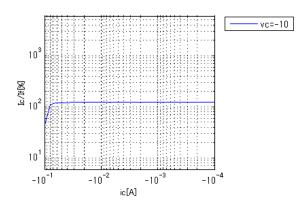


#### IcVbe[Temp]

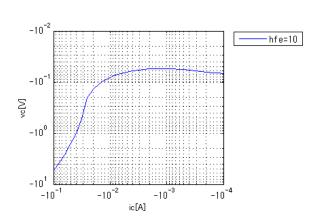
Vce = -10V



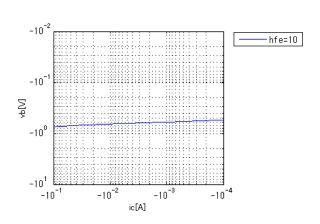
#### hFEIc[Vce]



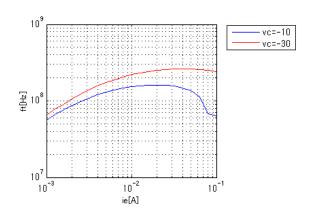
### Vce(sat)lc[hFE]



## Vbe(sat)lc[hFE]



## fTle[Vce]

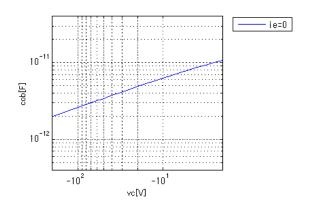




Simulation results are following. Explanatory notes — : simulated

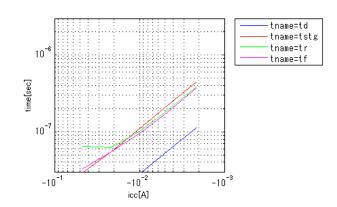
#### Cob

Freq. = 1MHz



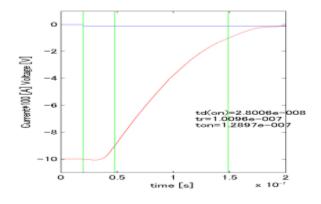
#### Switchinglcc[Tname]

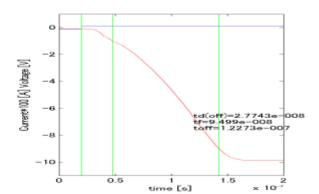
ib. = ic/10, vcc. = -10V, Temp. = 25degC



#### Switching Waveform (Blue: INPUT Red: OUTPUT)

ib. = ic/10, vcc. = -10V, Temp. = 25degC, ic = -0.01A







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